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PTFA192001E PTFA192001F



Thermally-Enhanced High Power RF LDMOS FETs 200 W, 1930 – 1990 MHz

Description

The PTFA192001E and PTFA192001F are 200-watt LDMOS FETs intended for single- and two-carrier WCDMA and CDMA applications from 1930 to 1990 MHz. Features include input and output matching, and thermally-enhanced packages with slotted or earless flanges. Manufactured with Infineon's advanced LDMOS process, these devices provide excellent thermal performance and superior reliability.

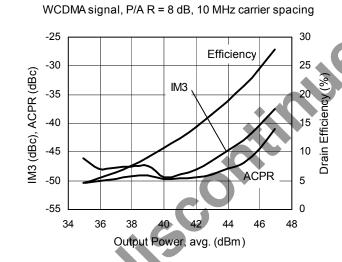
PTFA192001E Package H-36260-2

PTFA192001F Package H-37260-2



2-Carrier WCDMA Drive-up

 V_{DD} = 30 V, I_{DQ} = 1600 mA, f = 1960 MHz, 3GPP WCDMA signal, P/A R = 8 dB, 10 MHz carrier spacing



Features

- Pb-free, RoHS-compliant and thermally-enhanced packages
- Broadband internal matching
- Typical two-carrier WCDMA performance at 1990 MHz, 30 V
 - Average output power = 47.0 dBm
 - Linear Gain = 15.9 dB
 - Efficiency = 27%
 - Intermodulation distortion = -36 dBc
 - Adjacent channel power = -41 dBc
- Typical single-carrier WCDMA performance at 1960 MHz, 30 V, 3GPP signal, P/AR = 7.5 dB
 - Average output power = 48.5 dBm
 - Linear Gain = 15.9 dB
 - Efficiency = 34%
 - Intermodulation distortion = -37 dBc
 - Adjacent channel power = -40 dBc
- Typical CW performance, 1960 MHz, 30 V
 - Output power at P-1dB = 240 W
 - Efficiency = 57%
- Integrated ESD protection: Human Body Model, Class 2 (minimum)
- · Excellent thermal stability, low HCI drift
- Capable of handling 5:1 VSWR @ 30 V, 200 W (CW) output power

All published data at T_{CASE} = 25°C unless otherwise indicated

ESD: Electrostatic discharge sensitive device—observe handling precautions!

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RF Characteristics

WCDMA Measurements (tested in Infineon test fixture)

 $V_{DD} = 30 \text{ V}$, $I_{DQ} = 1.8 \text{ A}$, $P_{OUT} = 50 \text{ W}$ average

 f_1 = 1985 MHz, f_2 = 1995 MHz, 3GPP signal, channel bandwidth = 3.84 MHz, peak/average = 8 dB @ 0.01% CCDF

Characteristic	Symbol	Min	Тур	Max	Unit
Gain	G_ps	15.3	15.9	_	dB
Drain Efficiency	η_{D}	26.5	27	<u> </u>	%
Intermodulation Distortion	IMD	_	-36	-34	dBc

Two-tone Measurements (not subject to production test—verified by design/characterization in Infineon test fixture) $V_{DD} = 30 \text{ V}$, $I_{DQ} = 1.6 \text{ A}$, $P_{OUT} = 200 \text{ W}$ PEP, f = 1960 MHz, tone spacing = 1 MHz

Characteristic	Symbol Min	Тур	Max	Unit
Gain	G _{ps} —	15.9	_	dB
Drain Efficiency	η _D —	41	_	%
Intermodulation Distortion	IMD —	-30	_	dBc

DC Characteristics

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = 0 V$, $I_{DS} = 10 \text{ mA}$	V _{(BR)DSS}	65	_	_	V
Drain Leakage Current	$V_{DS} = 28 \text{ V}, V_{GS} = 0 \text{ V}$	I _{DSS}	_	_	1.0	μΑ
Drain Leakage Current	$V_{DS} = 63 \text{ V}, V_{GS} = 0 \text{ V}$	I _{DSS}	_	_	10.0	μΑ
On-State Resistance	$V_{GS} = 10 \text{ V}, V_{DS} = 0.1 \text{ V}$	R _{DS(on)}	_	0.05	_	Ω
Operating Gate Voltage	$V_{DS} = 30 \text{ V}, I_{DQ} = 1.8 \text{ A}$	V _{GS}	2.0	2.5	3.0	V
Gate Leakage Current	V _{GS} = 10 V, V _{DS} = 0 V	I _{GSS}	_	_	1.0	μΑ

Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	65	V
Gate-Source Voltage	V _{GS}	-0.5 to +12	V
Junction Temperature	TJ	200	°C
Total Device Dissipation	P _D	625	W
Above 25°C derate by		3.57	W/°C
Storage Temperature Range	T _{STG}	-40 to +150	°C
Thermal Resistance (T _{CASE} = 70°C, 200 W CW)	$R_{ heta JC}$	0.28	°C/W

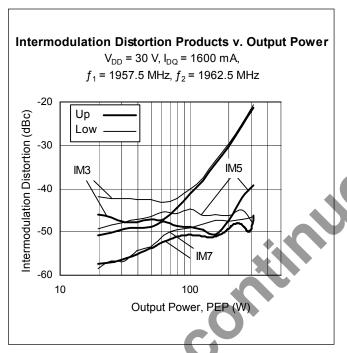
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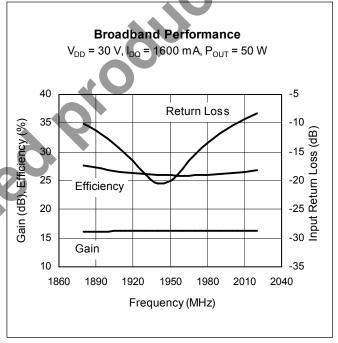


Ordering Information

Type and Version	Package Type	Package Description	Marking
PTFA192001E V4	H-36260-2	Thermally-enhanced slotted flange, single-ended	PTFA192001E
PTFA192001F V4	H-37260-2	Thermally-enhanced earless flange, single-ended	PTFA192001F

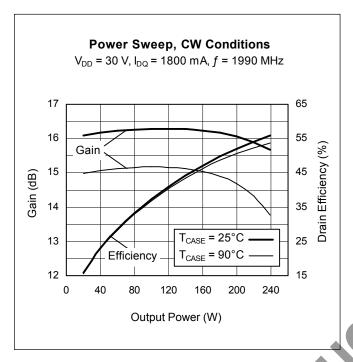
Typical Performance (data taken in a production test fixture)

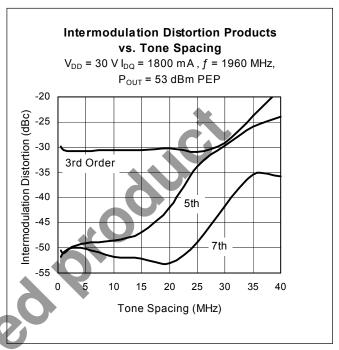


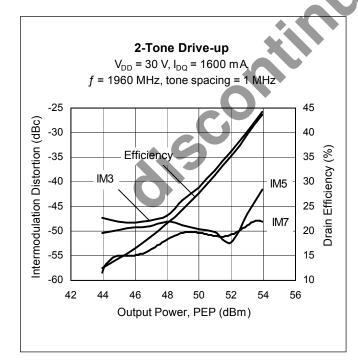


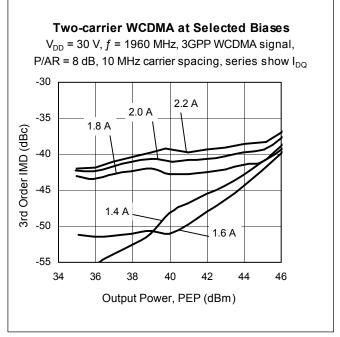


Typical Performance (cont.)



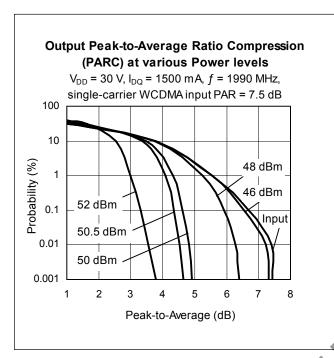


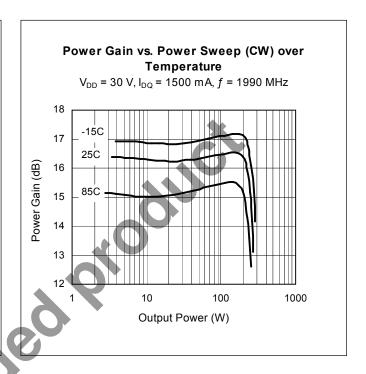


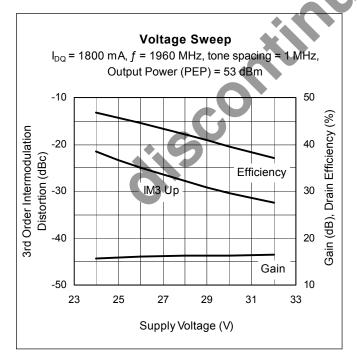


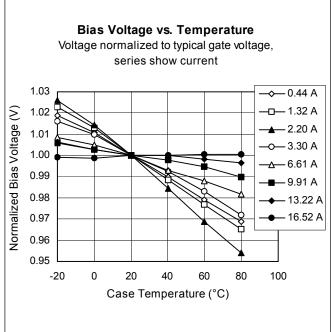


Typical Performance (cont.)



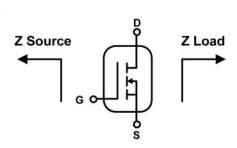




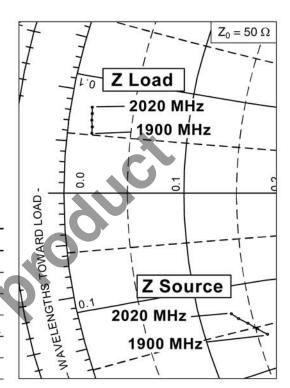




Broadband Circuit Impedance



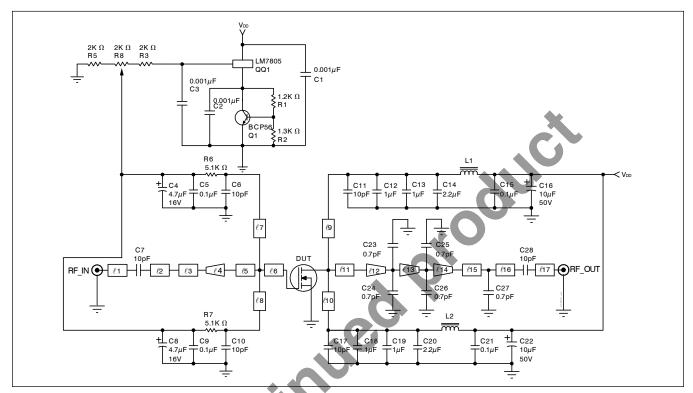
Frequency	equency Z Source Ω		Z Loa	ad Ω
MHz	R	jX	R	jХ
1900	8.033	-8.054	0.943	2.60
1930	7.611	-7.612	0.932	2.87
1960	7.230	-7.197	0.886	3.15
1990	6.839	-6.839	0.863	3.44
2020	6.541	-6.496	0.829	3.71



See next page for circuit information



Reference Circuit



Reference circuit schematic for f = 1960 MHz

Circuit Assembl	y Information		
DUT	PTFA192001E or PTFA192001F	LDMOS Transistor	
PCB	0.76 mm [.030"] thick, &r = 3.48	Rogers RO4350	1 oz. copper

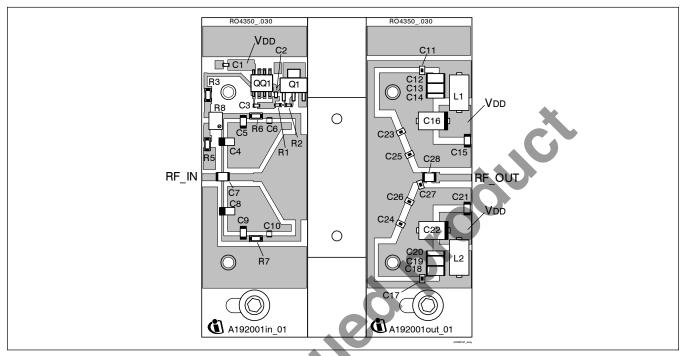
Microstrip	Electrical Characteristics at 1960 MHz ¹	Dimensions: L x W (mm)	Dimensions: L x W (in.)
ℓ 1	0.038 λ, 50.0 Ω	3.51 x 1.70	0.138 x 0.067
ℓ 2	0.071 λ, 50.0 Ω	6.60 x 1.70	0.260 x 0.067
ℓ 3	0.022 λ, 43.0 Ω	2.01 x 2.16	0.079 x 0.085
ℓ 4 (taper)	0.060 λ, 43.0 Ω / 6.9 Ω	5.28 x 2.16 / 20.32	0.208 x 0.085 / 0.800
ℓ 5	0.040 λ, 6.9 Ω	3.33 x 20.32	0.131 x 0.800
ℓ 6	0.026 λ, 6.9 Ω	2.21 x 20.32	0.087 x 0.800
ℓ 7, ℓ 8	0.123 λ, 60.0 Ω	11.48 x 1.24	0.452 x 0.049
ℓ 9, ℓ 10	0.258 λ, 50.9 Ω	23.88 x 1.65	0.940 x 0.065
ℓ 11	0.067 λ, 5.0 Ω	5.59 x 28.91	0.220 x 1.138
ℓ 12 (taper)	0.017 λ , 5.0 Ω / 7.2 Ω	1.42 x 28.91 / 19.51	0.056 x 1.138 / 0.768
ℓ 13 (taper)	0.024 λ, 7.2 Ω / 12.3 Ω	2.08 x 19.51 / 10.67	0.082 x 0.768 / 0.420
ℓ 14 (taper)	0.019 λ, 12.3 Ω / 41 Ω	1.78 x 10.67 / 2.29	0.070 x 0.420 / 0.090
ℓ 15	0.009 λ, 41.0 Ω	0.79 x 2.29	0.031 x 0.090
ℓ 16	0.021 λ, 41.0 Ω	1.85 x 2.29	0.073 x 0.090
ℓ 17	0.096 λ, 50.0 Ω	8.99 x 1.70	0.354 x 0.067

¹Electrical characteristics are rounded.

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Reference Circuit (cont.)



Reference circuit assembly diagram* (not to scale)

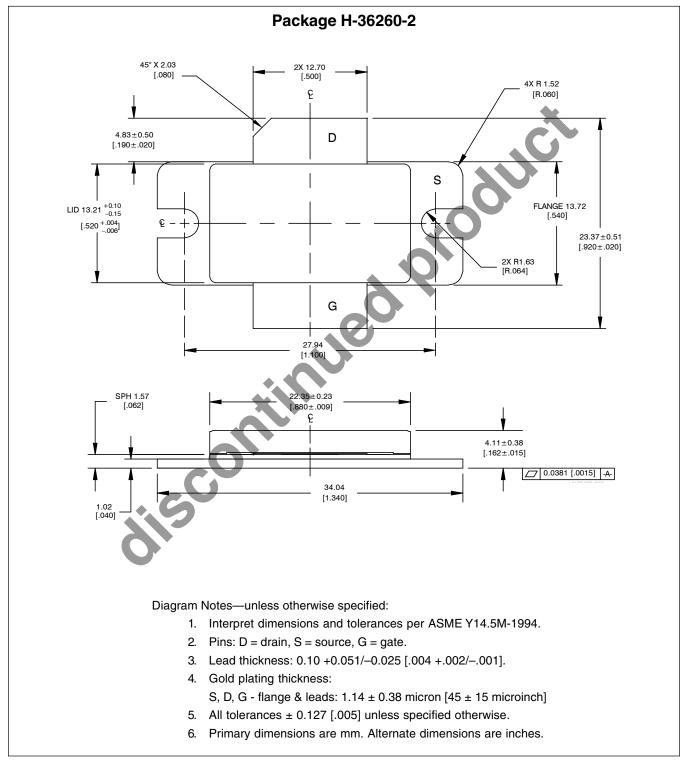
Component	Description	Suggested Manufacturer	P/N or Comment
C1, C2, C3	Capacitor, 0.001 µF	Digi-Key	PCC1772CT-ND
C4, C8	Capacitor, 4.7 µF, 16 V	Digi-Key	PCS3475CT-ND
C5, C9, C15, C21	Capacitor, 0.1 µF	Digi-Key	PCC104BCT-ND
C6, C10	Ceramic capacitor, 10 pF	ATC	100A 100
C7, C28	Ceramic capacitor, 10 pF	ATC	100B 100
C11, C17	Capacitor, 10 pF	AVX	08051J100GBTTR
C12, C13, C18, C19	Ceramic capacitor, 1 µF	Digi-Key	445-1411-1-ND
C14, C20	Capacitor, 2.2 μF	Digi-Key	445-1447-2-ND
C16, C22	Tantalum capacitor, 10 μF, 50 V	Garrett Electronics	TPSE106K050R0400
C23, C24, C25,	Capacitor, 0.7 pF	AVX	08051J0R7BBTTR
C26, C27			
L1, L2	Ferrite, 8.9 mm	Elna Magnetics	BDS 4.6/3/8.9-4S2
Q1	Transistor	Infineon Technologies	BCP56
QQ1	Voltage regulator	National Semiconductor	LM7805
R1	Chip resistor 1.2 k-ohms	Digi-Key	P1.2KGCT-ND
R2	Chip resistor 1.3 k-ohms	Digi-Key	P1.3KGCT-ND
R3, R5	Chip resistor 2 k-ohms	Digi-Key	P2KECT-ND
R6, R7	Chip resistor 5.1 k-ohms	Digi-Key	P5.1KECT-ND
R8	Potentiometer 2 k-ohms	Digi-Key	3224W-202ETR-ND

^{*}Gerber files for this circuit available on request

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Package Outline Specifications

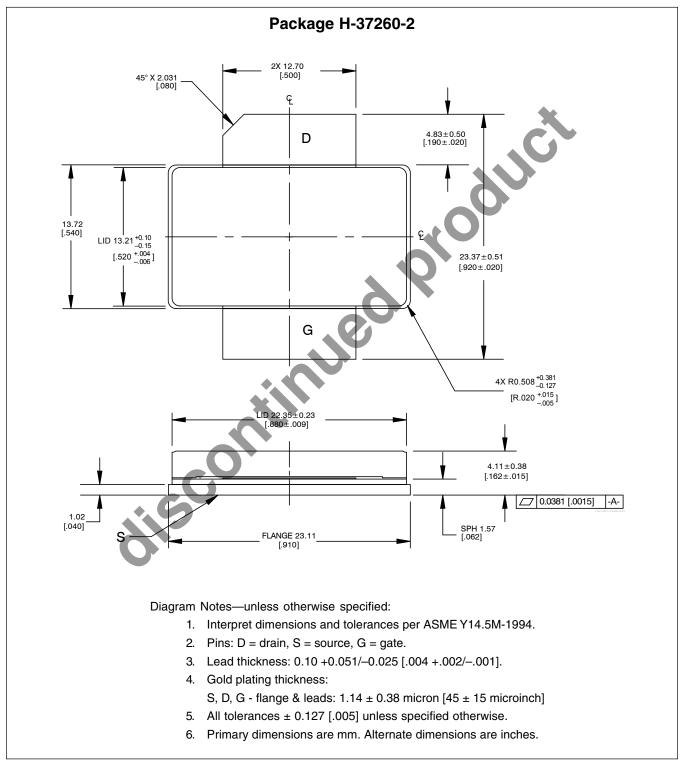


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Package Outline Specifications (cont.)



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PTFA192001E/F

Revision I	listory: 2017-07-19	Data Sheet
Previous V	ersion: 2015-03-04, Data Sheet	
Page	Subjects (major changes since last revision)	
All	Product discontinued	

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