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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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DC-DC CONVERTERS

POLA Non-isolated

NEW Product





- 22 A output current
- 5 V input voltage
- Wide-output voltage adjust (0.8 Vdc to 3.6 Vdc)
- Auto-track<sup>™</sup> sequencing\*
- Margin up/down controls
- Pre-bias start-up capability
- Efficiencies up to 96%
- Output ON/OFF inhibit
- Output voltage sense
- Point-of-Load-Alliance (POLA) compatible
- Available RoHS compliant

The PTH05020 is a next generation series of non-isolated dc-dc converters offering some of the most advanced POL features available in the industry. The primary new feature provides for sequencing between multiple modules, a function, which is becoming a necessity for powering advanced silicon including DSP's, FPGA's and ASIC's requiring controlled power-up and power-down Other industry leading features include margin up/down controls, pre-bias start-up capability and efficiencies up to 96%. The PTH05020 has an input voltage of 4.5 Vdc to 5.5 Vdc and offers a wide 0.8 Vdc to 3.6 Vdc output voltage range with up to 22 A output current, which allows for maximum design flexibility and a pathway for future upgrades.







All specifications are typical at nominal input, full load at 25 °C unless otherwise stated  $C_{in}$  = 1000  $\mu$ F,  $C_{out}$  = 0  $\mu$ F

**SPECIFICATIONS** 

# **OUTPUT SPECIFICATIONS**

Voltage adjustability	(See Note 4)	0.8-3.6 Vdc
Setpoint accuracy		±2.0% Vo
Line regulation		±5 mV typ.
Load regulation		±5 mV typ.
Total regulation		±3.0% Vo
Minimum load		0 A
Ripple and noise	20 MHz bandwidth	20 mV pk-pk
Temperature co-efficient	-40 °C to +85 °C	±0.5% Vo
Transient response (See Note 5)	Overshoot	70 µs recovery time /undershoot 120 mV
Margin adjustment		±5.0% Vo

# **INPUT SPECIFICATIONS**

Input voltage range	(See Note 3)	4.5-5.5 Vdc		
Input current	No load	10 mA typ.		
Remote ON/OFF	(See Note 1)	Positive logic		
Start-up time		1 V/ms		
Undervoltage lockout		3.7-4.3 Vdc typ.		
Track input voltage	Pin 8 (See Note 6, 7)	±0.3 Vin		

# EMC CHARACTERISTICS

Electrostatic discharge	EN61000-4-2, IEC801-2
Conducted immunity	EN61000-4-6
Radiated immunity	EN61000-4-3

## **GENERAL SPECIFICATIONS**

Efficiency	(See Efficiency Table)		96% max.
Insulation voltage			Non-isolated
Switching frequency		250	kHz to 340 kHz
Approvals and standards			EN60950 UL/cUL60950
Material flammability			UL94V-0
Dimensions	(L x W x H)		2.10 x 9.00 mm 0.870 x 0.354 in
Weight			7 g (0.25 oz)
MTBF	Telcordia SR-3	32 5	5,236,000 hours

## **ENVIRONMENTAL SPECIFICATIONS**

Thermal performance (See Note 2)	Operating ambient, temperature	-40 °C to +85 °C		
(See Note 2)	Non-operating	-40 °C to +125 °C		
MSL ('Z' suffix only)	JEDEC J-STD-020C	Level 3		

#### **PROTECTION**

Short-circuit	Auto reset	41 A typ.
Thermal		Auto recovery

\*Auto-track™ is a trade mark of Texas Instruments

#### **International Safety Standard Approvals**



UL/cUL CAN/CSA-C22.2 No.  $60950-1-03/UL\ 60950-1$ , File No. E174104



TÜV Product Service (EN60950) Certificate No. B 04 06 38572 044 CB Report and Certificate to IEC60950, Certificate No. US/8292/UL





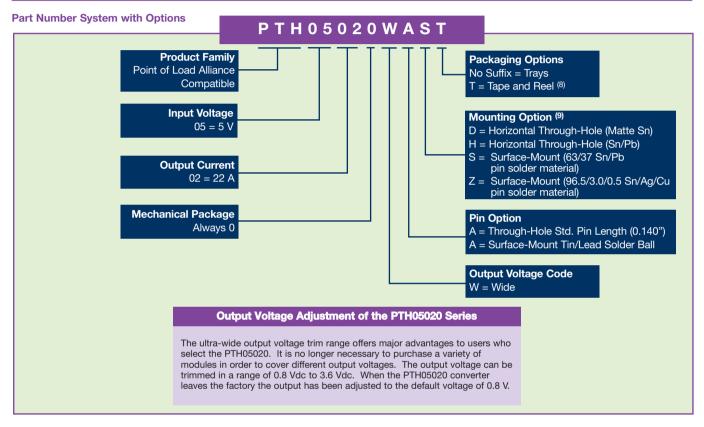


DC-DC CONVERTERS POLA Non-isolated

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**NEW Product** 

OUTPUT POWER	INPUT	OUTPUT	OUTPUT	OUTPUT CURRENT	EFFICIENCY	REGU	LATION	MODEL
(MAX.)	VOLTAGE	VOLTAGE	(MIN.)	(MAX.)	(MAX.)	LINE	LOAD	NUMBER (9,10)
79.2 W	4.5-5.5 Vdc	0.8-3.6 Vdc	0 A	22 A	96%	±5 mV	±5 mV	PTH05020



#### **Notes**

Remote ON/OFF. Positive Logic

Pin 3 open; or V > Vin - 0.5 V Pin 3 GND; or V < 0.8 V (min - 0.2 V).

See Figure 1 for safe operating curve.

- A 1,000 µF electrolytic input capacitor is required for proper operation. The capacitor must be rated for a minimum of 700 mA rms of ripple
- An external output capacitor is not required for basic operation. Adding 330 µF of distributed capacitance at the load will improve the transient response.
- 1 A/µs load step, 50 to 100%  $I_{omax}$ ,  $C_{out}$  = 330 µF. If utilized Vout will track applied voltage by ±0.3 V (up to Vo set point). The pre-bias start-up feature is not compatible with Auto-Track™. This is because when the module is under Auto-Track™ control, it is fully active and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track<sup>™</sup> function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 156 for more details.
- Tape and reel packaging only available on the surface-mount versions. To order Pb-free (RoHS compatible) surface-mount parts replace the mounting option 'S' with 'Z', e.g. PTH05020WAZ. To order Pb-free (RoHS compatible) through-hole parts replace the mounting option 'H' with 'D', e.g. PTH05020WAD.
- 10 NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at http://www.artesyn.com/powergroup/products.htm to find a suitable alternative

EFFICIENCY TABLE (I <sub>O</sub> = 10 A)				
OUTPUT VOLTAGE	EFFICIENCY			
Vo = 1.0 V	88%			
Vo = 1.2 V	90%			
Vo = 1.5 V	91%			
Vo = 1.8 V	92%			
Vo = 2.0 V	93%			
Vo = 2.5 V	94%			
Vo = 3.3 V	96%			
Vo = 2.5 V	94%			







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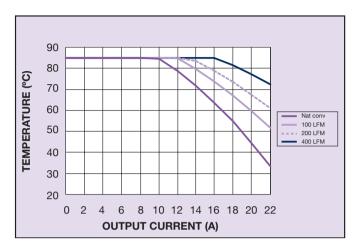


Figure 1 - Safe Operating Area
Vin = 5 V, Output Voltage = 3.3 V (See Note A)

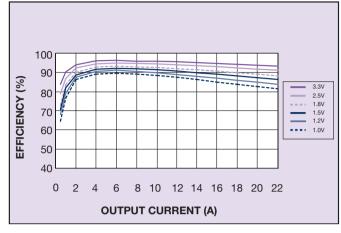


Figure 2 - Efficiency vs Load Current Vin = 5 V (See Note B)

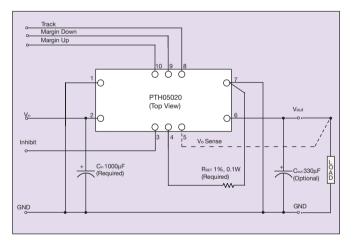


Figure 3 - Standard Application

#### **Notes**

- A SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
   B Characteristic data has been developed from actual products tested at
- B Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.







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**NEW Product** 

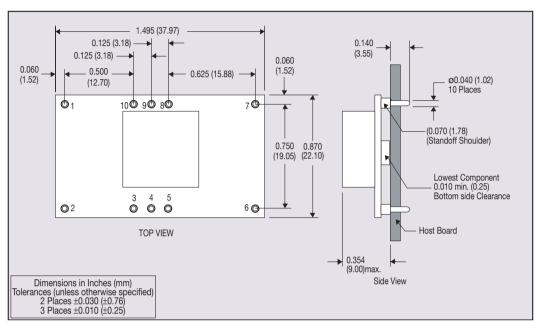
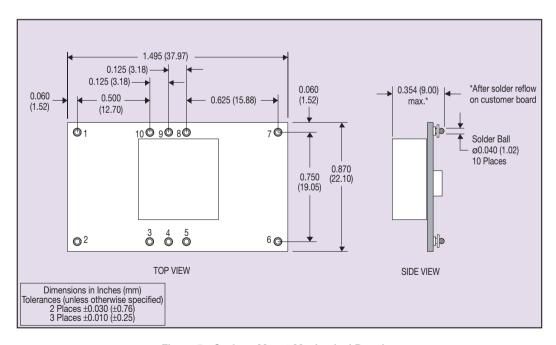


Figure 4 - Plated Through-Hole Mechanical Drawing



PIN CONNECTIONS PIN NO. **FUNCTION** 1 Ground 2 Vin 3 Inhibit\* 4 Vo adjust 5 Vo sense 6 Vout 7 Ground 8 Track 9 Margin down\* 10 Margin up\*

\*Denotes negative logic: Open = Normal operation Ground = Function active

Figure 5 - Surface-Mount Mechanical Drawing

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Please consult our website for the following items: 

Application Note

www.artesyn.com