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## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



**Series PVD33N & PbF**

Microelectronic Power IC  
 HEXFET® Power MOSFET Photovoltaic Relay  
 Single-Pole, Normally-Open  
 0-300V DC, 240mA

**General Description**

The PVD33 Series DC Relay (PVD) is a single-pole, normally open, solid-state replacement for electromechanical relays used for general purpose switching of analog signals. It utilizes International Rectifier's HEXFET power MOSFET as the output switch, driven by an integrated circuit photovoltaic generator of novel construction. The output switch is controlled by radiation from a GaAlAs light emitting diode (LED), which is optically isolated from the photovoltaic generator.

The PVD33 Series overcomes the limitations of both conventional electromechanical and reed relays by offering the solid state advantages of long life, fast operating speed, low pick up power, bounce-free operation, low thermal offset voltages and miniature package. These advantages allow product improvement and design innovations in many applications such as process control, multiplexing, automatic test equipment and data acquisition.

The PVD33 can switch analog signals from thermocouple level to 300 Volts peak DC. Signal frequencies into the RF range are easily controlled and switching rates up to 500Hz are achievable. The extremely small thermally generated offset voltages allow increased measurement accuracies.

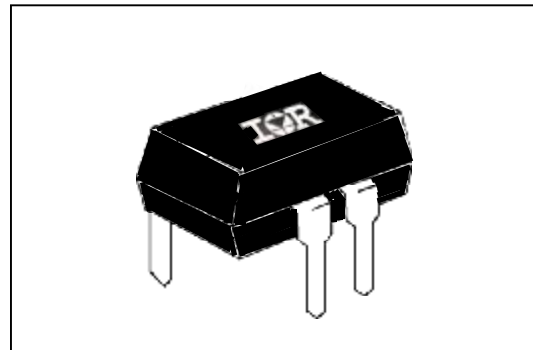
These relays are packaged in 8-pin, molded DIP packages and available with either thru-hole or surface-mount ("gull-wing") leads, in plastic shipping tubes.

**Applications**

- § Process Control
- § Data Acquisition
- § Test Equipment
- § Multiplexing and Scanning

**Features**

- § Bounce-Free Operation
- §  $10^{10}$  Off-State Resistance
- § 1,000 V/ $\mu$ sec dv/dt
- § 5 mA Input Sensitivity
- § 4,000 V<sub>RMS</sub> I/O Isolation
- § Solid-State Reliability
- § UL Recognized
- § ESD Tolerance:  
     4000V Human Body Model  
     500V Machine Model



**Part Identification**

|                 |               |
|-----------------|---------------|
| PVD2352N & PbF  | thru-hole     |
| PVD3354N & PbF  |               |
| PVD2352NS & PbF | surface-mount |
| PVD3354NS & PbF | (gull-wing)   |

*(HEXFET is the registered trademark for International Rectifier Power MOSFETs)*

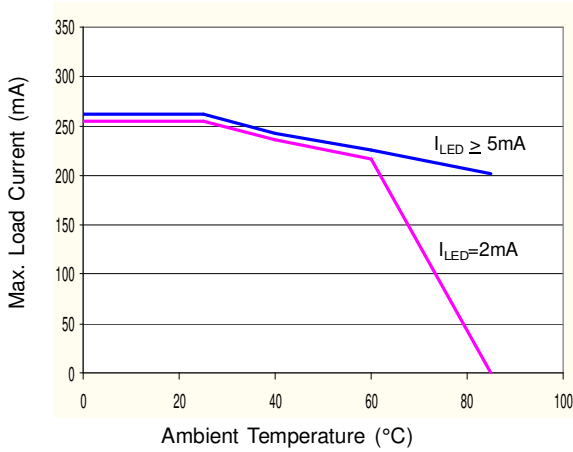
**Electrical Specifications** ( $-40^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$  unless otherwise specified)

| <b>INPUT CHARACTERISTICS</b>   | <b>PVD2352N</b> | <b>PVD3354N</b> | <b>Units</b>                        |
|--|-----------------|-----------------|-------------------------------------|
| Minimum Control Current (see figures 1 and 2)<br>For 250mA Continuous Load Current<br>For 240mA Continuous Load Current<br>For 200mA Continuous Load Current | 2<br>5<br>5     |                 | DC<br>mA@25°C<br>mA@40°C<br>mA@85°C |
| Maximum Control Current for Off-State Resistance at 25°C   | 10              |                 | μA(DC)                              |
| Control Current Range (Caution: current limit input LED. See figure 6)   | 2.0 to 25       |                 | mA(DC)                              |
| Maximum Reverse Voltage  | 6.0             |                 | V(DC)                               |

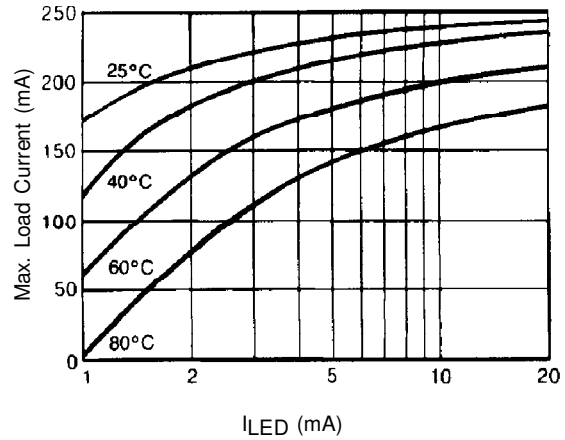
| <b>OUTPUT CHARACTERISTICS</b>  | <b>PVD2352N</b>          | <b>PVD3354N</b>           | <b>Units</b>        |
|--|--------------------------|---------------------------|---------------------|
| Operating Voltage Range  | 200                      | 300                       | V <sub>(peak)</sub> |
| Maximum Load Current 40°C I LED 5mA  | 240                      |                           | mA(DC)              |
| Response Time @25°C (see figures 7 and 8)<br>Max. T <sub>(on)</sub> @ 12mA Control, 50 mA Load, 100 VDC<br>Max. T <sub>(off)</sub> @ 12mA Control, 50 mA Load, 100 VDC | 100<br>110               |                           | μs                  |
| Max. On-state Resistance 25°C (Pulsed) (fig. 4) 50 mA Load, 5mA Control  | 6                        |                           | Ω                   |
| Min. Off-state Resistance 25°C (see figure 5)  | 10 <sup>8</sup> @ 160VDC | 10 <sup>10</sup> @ 240VDC | Ω                   |
| Max. Thermal Offset Voltage @ 5.0mA Control  | 0.2                      |                           | μvolts              |
| Min. Off-State dv/dt   | 1000                     |                           | V/μs                |
| Typical Output Capacitance (see figure 9)  | 10                       |                           | pF @ 50VDC          |

| <b>GENERAL CHARACTERISTICS</b>  | <b>(PVD2352N and PVD3354N)</b>   |             | <b>Units</b>     |
|---|----------------------------------|-------------|------------------|
| Dielectric Strength: Input-Output   | 4000                             |             | V <sub>RMS</sub> |
| Insulation Resistance: Input-Output @ 90V <sub>DC</sub>                     | 10 <sup>12</sup> @ 25°C - 50% RH |             | Ω                |
| Maximum Capacitance: Input-Output   | 1.0                              |             | pF               |
| Max. Pin Soldering Temperature (1.6mm below seating plane, 10 seconds max.) | +260                             |             | °C               |
| Ambient Temperature Range:  | Operating                        | -40 to +85  |                  |
|   | Storage                          | -40 to +100 |                  |

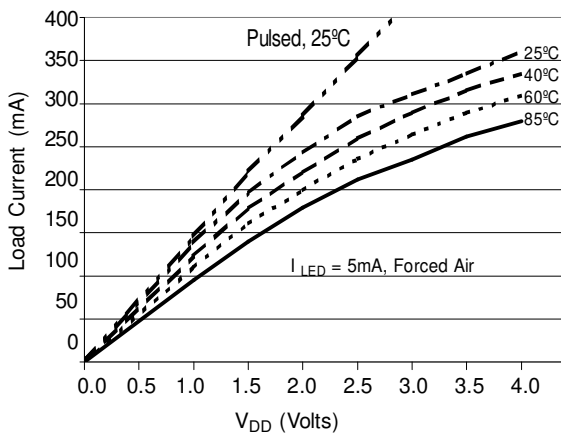
International Rectifier does not recommend the use of this product in aerospace, avionics, military or life support applications. Users of this International Rectifier product in such applications assume all risks of such use and indemnify International Rectifier against all damages resulting from such use.



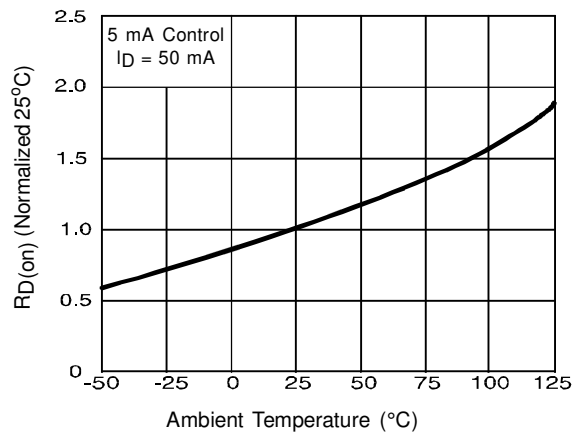
**Figure 1. Current Derating Curves**



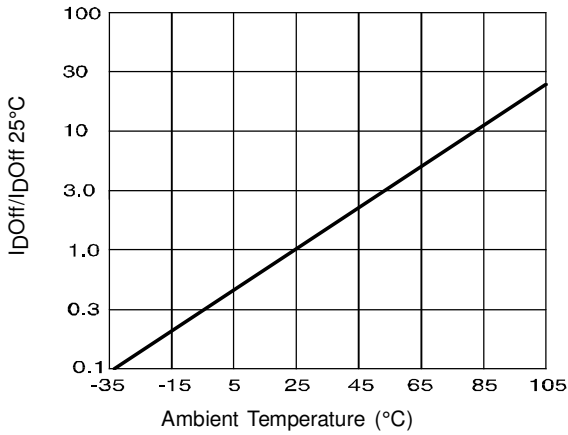
**Figure 2. Typical Control Current Requirements**



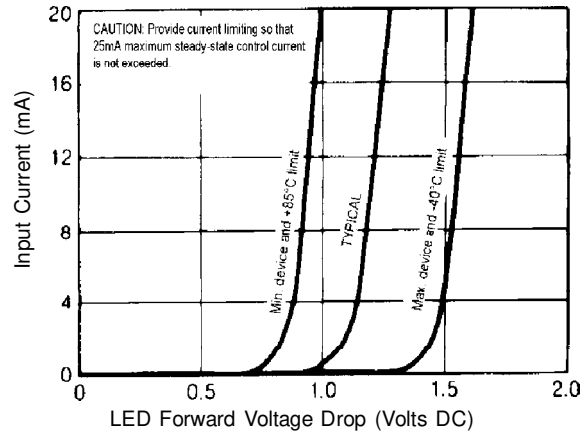
**Figure 3. Typical On Characteristics**



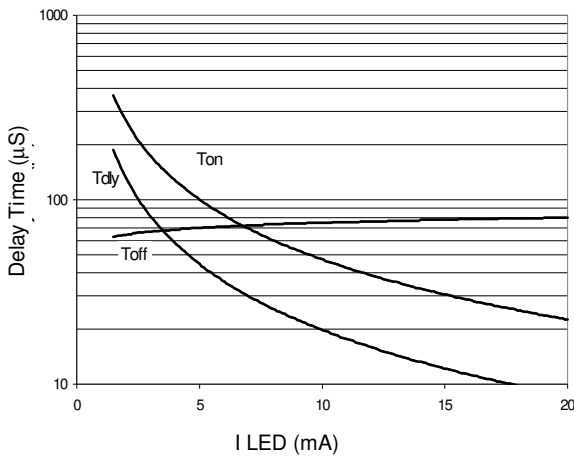
**Figure 4. Typical On-Resistance**



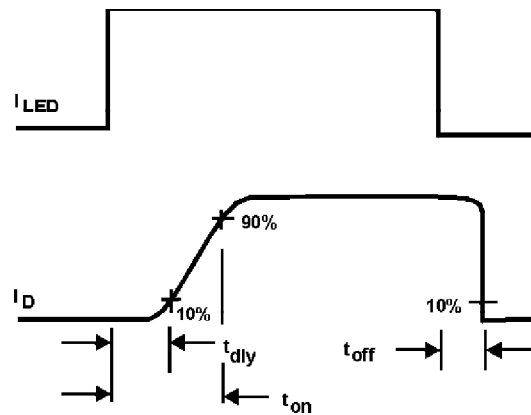
**Figure 5. Normalized Off-State Leakage**



**Figure 6. Input Characteristics (Current Controlled)**



**Figure 7. Typical Delay Times**



**Figure 8. Delay Time Definitions**

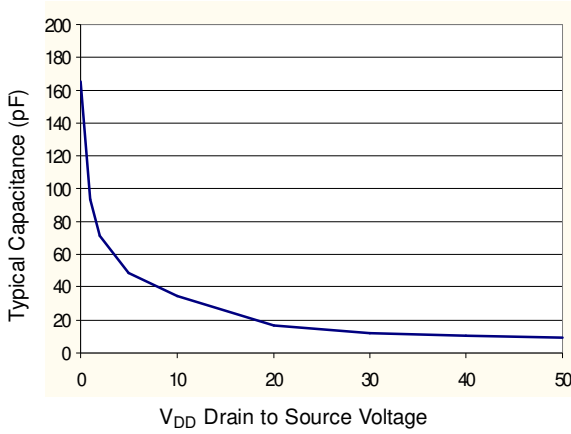
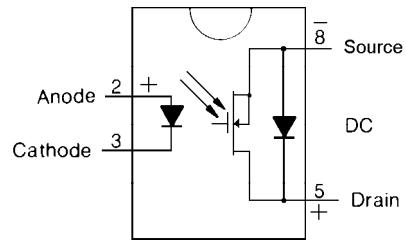
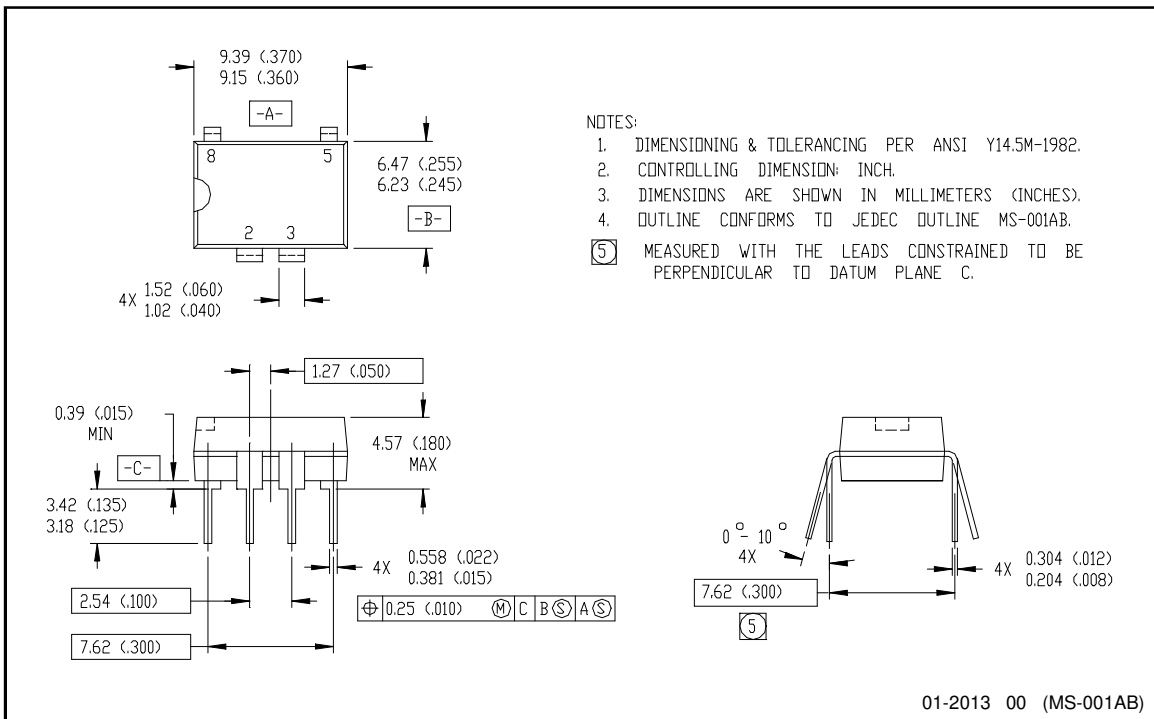


Figure 9. Typical Output Capacitance

Wiring Diagram



Case Outline



**Case Outline**

