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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







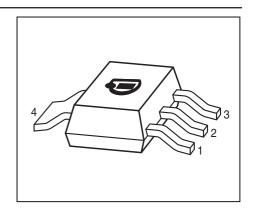


NPN Silicon Darlington Transistor

- For general AF applications
- High collector current
- High current gain
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101







Туре	Marking	Pin Configuration					Package	
PZTA14	PZTA14	1=B	2=C	3=E	4=C	-	-	SOT223

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CES}	30	V
Collector-base voltage	V_{CBO}	30	
Emitter-base voltage	V _{EBO}	10	
Collector current	I _C	300	mA
Peak collector current, $t_p \le 10 \text{ ms}$	I _{CM}	500	
Base current	I _B	100	
Peak base current	I _{BM}	200	
Total power dissipation-	P _{tot}	1.5	W
<i>T</i> _S ≤ 124 °C			
Junction temperature	T_{i}	150	°C
Storage temperature	T _{stq}	-65 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ¹⁾	R _{thJS}	≤ 17	K/W

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 $^{^{1}}$ For calculation of R_{thJA} please refer to Application Note AN077 (Thermal Resistance Calculation)



Electrical Characteristics at T_A = 25°C, unless otherwise specified

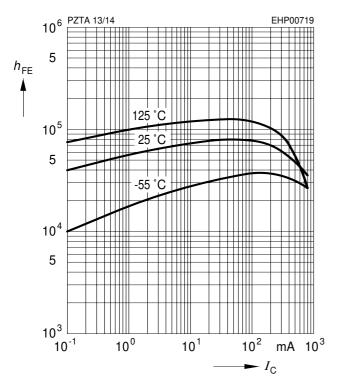
Parameter	Symbol		Values		
			typ.	max.	1
DC Characteristics	•			•	
Collector-base breakdown voltage	V _{(BR)CBO}	30	-	-	V
$I_{\rm C} = 100 \mu \text{A}, I_{\rm E} = 0$, ,				
Collector-emitter breakdown voltage	V _{(BR)CES}	30	-	-	
$I_{\rm C}$ = 100 μ A, $V_{\rm BE}$ = 0					
Emitter-base breakdown voltage	$V_{(BR)EBO}$	10	-	_	
$I_{\rm E}$ = 10 μ A, $I_{\rm C}$ = 0					
Collector-base cutoff current	I _{CBO}				μA
$V_{CB} = 30 \text{ V}, I_{E} = 0$		-	-	0.1	
$V_{\rm CB}$ = 30 V, $I_{\rm E}$ = 0 , $T_{\rm A}$ = 150 °C		-	-	10	
Emitter-base cutoff current	I _{EBO}	-	-	100	nA
$V_{\rm EB} = 10 \rm V, I_{\rm C} = 0$					
DC current gain ¹⁾	h _{FE}				-
$I_{\rm C}$ = 10 mA, $V_{\rm CE}$ = 5 V		10000	-	-	
$I_{\rm C}$ = 100 mA, $V_{\rm CE}$ = 5 V		20000	-	-	
Collector-emitter saturation voltage ¹⁾	V _{CEsat}	-	-	1.5	V
$I_{\rm C}$ = 100 mA, $I_{\rm B}$ = 0.1 mA					
Base emitter saturation voltage ¹⁾	V _{BEsat}	-	-	2	
$I_{\rm C}$ = 100 mA, $I_{\rm B}$ = 0.1 mA					
AC Characteristics					
Transition frequency	f_{T}	125	-	-	MHz
$I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 5 V, f = 20 MHz					
Collector-base capacitance	C _{cb}	-	3	-	pF
$V_{CB} = 10 \text{ V}, f = 1 \text{ MHz}$					

¹Pulse test: $t < 300 \mu s$; D < 2%



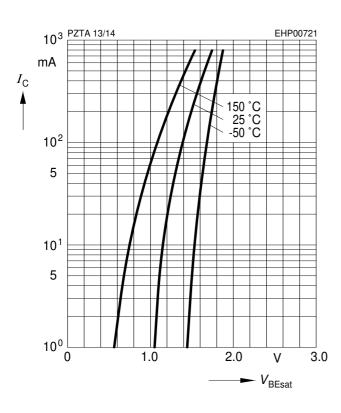
DC current gain $h_{FE} = f(I_C)$

$$V_{CE} = 5 \text{ V}$$



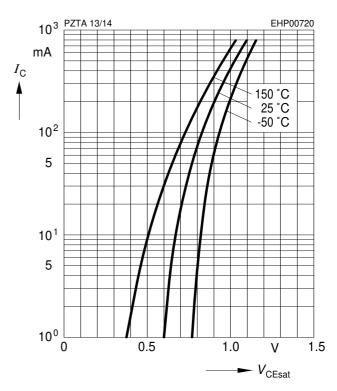
Base-emitter saturation voltage

$$I_{\rm C} = f(V_{\rm BEsat}), h_{\rm FE} = 1000$$



Collector-emitter saturation voltage

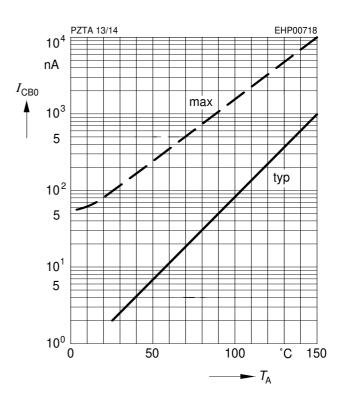
$$I_{\rm C} = f(V_{\rm CEsat}), h_{\rm FE} = 1000$$



Collector cutoff current $I_{CBO} = f(T_A)$

$$V_{\rm CBO}$$
 = 30 V

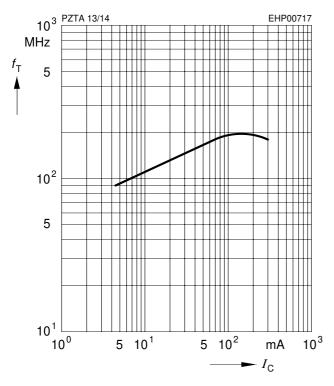
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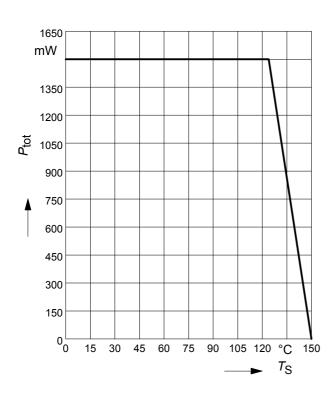


Transition frequency $f_T = f(I_C)$

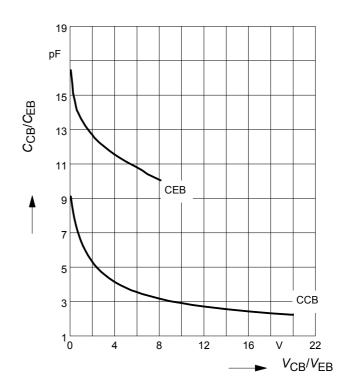
 $V_{CE} = 5 \text{ V}, f = 200 \text{ MHz}$



Total power dissipation $P_{\text{tot}} = f(T_{\text{S}})$

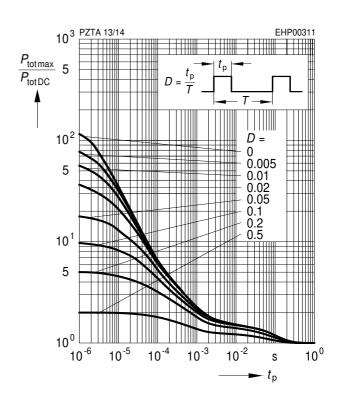


Collector-base capacitance $C_{\text{cb}} = f(V_{\text{CB}})$ Emitter-base capacitance $C_{\text{eb}} = f(V_{\text{EB}})$



Permissible Pulse Load

 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{\text{p}})$





Package Outline 1.6±0.1 6.5 ± 0.2 0.1 MAX 3±0.1 $\tilde{\Omega}$ 3.5 ± 0.2 7±0.3 2 2.3 0.7±0.1 0.28 ±0.04 4.6 0...10° ⊕ 0.25 M A = 0.25 M B Foot Print 3.5 1.2 1.1 Marking Layout (Example) **(**infineon Manufacturer 2005, 24 CW Date code (YYWW) 0524 16 BCP52-16 Type code Pin 1 Packing Reel ø180 mm = 1.000 Pieces/Reel Reel ø330 mm = 4.000 Pieces/Reel 0.3 MAX. \oplus

1.75

7.55

6.8



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