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April 1st, 2010
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User's Manual

QB-78K0RKX3

In-Circuit Emulator

Target Devices

78K0R/KE3

78K0R/KF3

78K0R/KG3

78K0R/KH3

78K0R/KJ3

Document No. U17866EJ4V0UM00 (4th edition)

Date Published April 2008 NS

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Printed in Japan

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[MEMO]

General Precautions for Handling This Product

1. Circumstances not covered by product guarantee

- If the product was disassembled, altered, or repaired by the customer
- If it was dropped, broken, or given another strong shock
- Use at overvoltage, use outside guaranteed temperature range, storing outside guaranteed temperature range
- If power was turned on while connection to the AC adapter, USB interface cable, or target system was in an unsatisfactory state
- If the cable of the AC adapter, the USB interface cable, the emulation probe, or the like was bent or pulled excessively
- If an AC adapter other than the supplied product was used
- If the product got wet
- If this product is connected to the target system when there is a potential difference between the GND of this product and GND of the target system.
- If the connectors or cables are plugged/unplugged while this product is in the power-on state.
- If excessive load is applied to the connectors or sockets (As for handling, please see **2.5 Mounting and Connecting Connectors**).
- If a metal part of the power switch, cooling fan, or another such part comes in contact with an electrostatic charge.
- If the product is used or stored in an environment where an electrostatic or electrical noise is likely to occur.

2. Safety precautions

- If used for a long time, the product may become hot (50°C to 60°C). Be careful of low temperature burns and other dangers due to the product becoming hot.
- Be careful of electrical shock. There is a danger of electrical shock if the product is used as described above in **1. Circumstances not covered by product guarantee**.
- The AC adapter supplied with the product is exclusively for this product, so do not use it with other products.

INTRODUCTION

Readers This manual is intended for users who wish to perform debugging using the QB-78K0RKX3. The readers of this manual are assumed to be familiar with the device functions and usage, and to have knowledge of debuggers.

Purpose This manual is intended to give users an understanding of the basic specifications and correct usage of the QB-78K0RKX3.

Organization This manual is divided into the following sections.

- General
- Setup procedure
- Settings at product shipment
- Cautions
- Characteristics of target interface

How to Read This Manual It is assumed that the readers of this manual have general knowledge in the fields of electrical engineering, logic circuits, and microcontrollers.

This manual describes the basic setup procedures and how to set switches.

To understand the overall functions and usages of the QB-78K0RKX3

→Read this manual in the order of the **CONTENTS**. The mark “<R>” shows major revised points. The revised points can be easily searched by copying an “<R>” in the PDF file and specifying it in the “Find what:” field.

To know the manipulations, command functions, and other software-related settings of the QB-78K0RKX3

→See the user’s manual of the debugger (supplied with the QB-78K0RKX3) to be used.

Conventions

Note: Footnote for item marked with **Note** in the text

Caution: Information requiring particular attention

Remark: Supplementary information

Numeric representation: Binary ... xxxx or xxxxB

Decimal ... xxxx

Hexadecimal ... xxxxH

Prefix indicating power of 2

(address space, memory

capacity):

K (kilo): $2^{10} = 1,024$

M (mega): $2^{20} = 1,024^2$

Terminology

The meanings of the terms used in this manual are described in the table below.

Term	Meaning
Target device	This is the device to be emulated.
Target system	This is the system to be debugged. This includes the target program and the hardware provided by the user.
78K0R/Kx3	Generic name indicating 78K0R/KE3, 78K0R/KF3, 78K0R/KG3, 78K0R/KH3 and 78K0R/KJ3.
IECUBE™	Generic name for NEC Electronics' high-performance/compact in-circuit emulator.

Related Documents

Please use the following documents in conjunction with this manual.

The related documents listed below may include preliminary versions. However, preliminary versions are not marked as such.

<R>

Documents Related to Development Tools (User's Manuals)

Document Name		Document Number
QB-78K0RKX3 In-Circuit Emulator		This manual
RA78K0R Ver. 1.20 Assembler Package	Operation	U18547E
	Language	U18546E
CC78K0R Ver. 2.00 C Compiler	Operation	U18549E
	Language	U18548E
ID78K0R-QB Ver. 3.20 Integrated Debugger	Operation	U17839E
PM+ Ver. 6.30 Project Manager		U18416E

Caution The related documents listed above are subject to change without notice. Be sure to use the latest version of each document for designing, etc.

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CHAPTER 1 GENERAL

The QB-78K0RKX3 is an in-circuit emulator for emulating the 78K0R/Kx3.

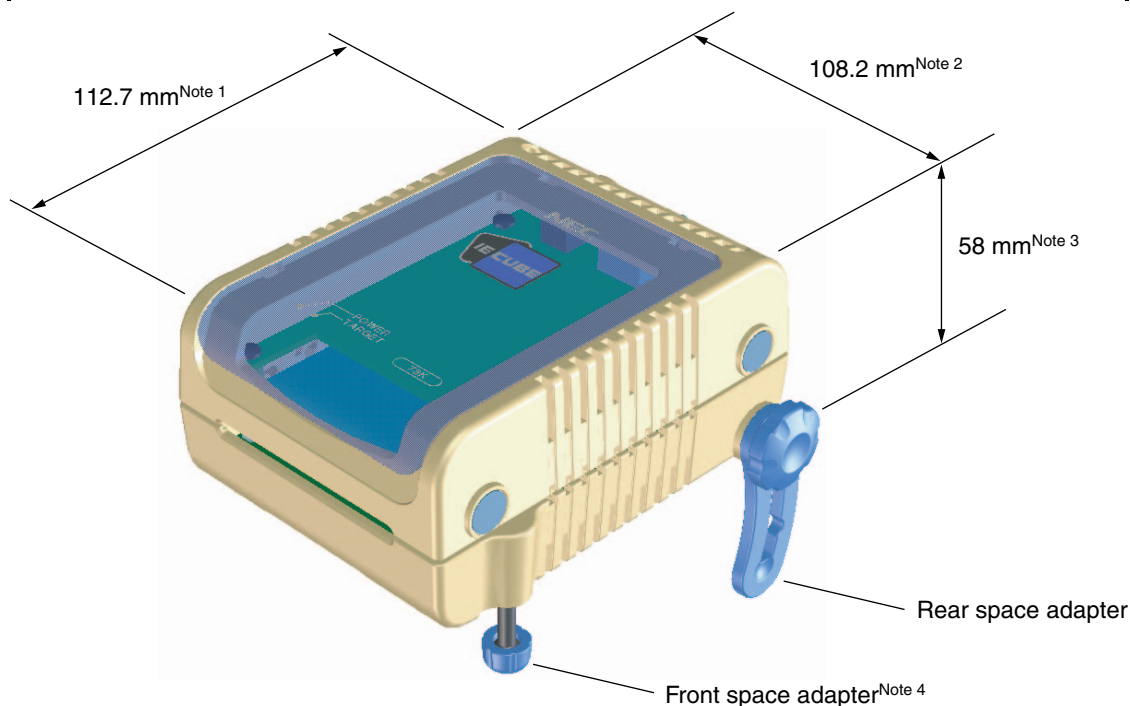
Hardware and software can be debugged efficiently in the development of systems in which the 78K0R/Kx3 is used. This manual describes basic setup procedures, hardware specifications, system specifications, and how to set switches.

1.1 Hardware Specifications

Table 1-1. QB-78K0RKX3 Hardware Specifications

Parameter		Specification	
Target device		78K0R/KE3, 78K0R/KF3, 78K0R/KG3, 78K0R/KH3, 78K0R/KJ3	
Operating voltage		1.8 to 5.5 V	
Operating frequency		High-speed system clock $2.7\text{ V} \leq V_{DD} \leq 5.5\text{ V}$: 2 to 20 MHz $1.8\text{ V} \leq V_{DD} \leq 2.7\text{ V}$: 2 to 5 MHz	
		Internal high-speed oscillation clock	An 8 MHz clock is supplied from the oscillator in the QB-78K0RKX3
		Internal low-speed oscillation clock	A 240 kHz clock is supplied from the oscillator in the QB-78K0RKX3
		Subsystem clock	A 32.768 kHz clock is supplied from the oscillator in the QB-78K0RKX3
Operating temperature range		0 to 40°C (No condensation)	
Storage temperature range		-15 to 60°C (No condensation)	
External dimensions		See figure below	
Power consumption	AC adapter for QB-78K0RKX3	Output: DC15 V, 1 A Input: AC100 to 240 V	
	Target system power supply	Voltage: 1.8 to 5.5 V Current: approx. 4.1 mA MAX.	
Weight		Approx. 400 g	
Host interface		USB interface (1.1, 2.0)	

<R>



- Notes**
- Does not include projection of power switch
 - Includes projection of screw that fixes rear space adapter
 - Rear space adapter can adjust the height from 30 mm (longest) to 0 mm (shortest)
 - Front space adapter can adjust the height from 20 mm (longest) to 5 mm (shortest)

1.2 System Specifications

This section shows the QB-78K0RKX3 system specifications.

Table 1-2. QB-78K0RKX3 System Specifications (1/2)

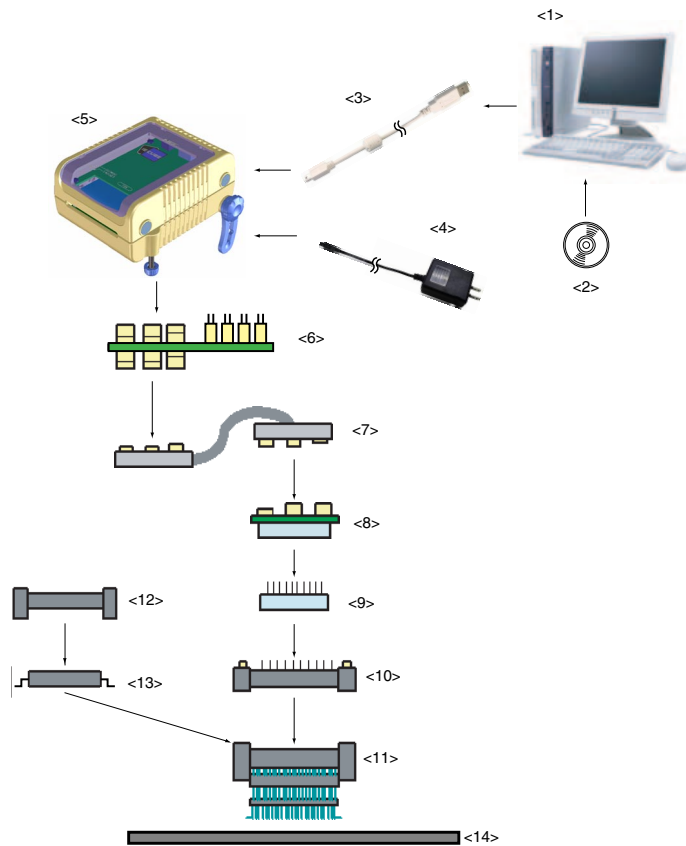
Parameter		Specification
Emulation memory capacity	Internal ROM	512 KB (MAX.)
	Internal RAM	61.75 KB (MAX.)
Program execution functions	Real-time execution function	Go, Start from Here, Come Here, Restart, Return Out, Ignore break points and Go
	Non-real-time execution function	Step In, Next Over, Slowmotion, Go & Go
Memory manipulation		Available (initialize, copy, compare)
Register manipulation		Available (general-purpose registers, control registers, SFRs)
Disassemble function		Available
Local variable view		Local variables
Watch data view		Local variables, global variables, or else
Stack trace view		Available
Break functions	Event break	Execution: 8 points Access: 8 points
	Software break	2000 points
	Pre-execution break	4 to 8 points ^{Note}
	Fail-safe break	Non-map, write protect, SFR illegal access, stack overflow, or else
	Other	Forcible break, trace full break, trace delay break, timeout break, timer overflow break
Trace functions	Trace data types	Program address, program data, access address, access data, status, time tag
	Trace modes	Unconditional trace, section trace, qualify trace, delay trigger trace
	Trace functions	Non-stop, full stop, full break, delay trigger stop, delay trigger break
	Memory capacity	128K frames
Real-time RAM monitoring function		All internal RAM spaces
Time measurement functions	Measurement clock	60 MHz
	Measurement objects	Start through end of program execution Start event through end event
	Maximum measurement time	Approx. 40 hours and 43 minutes (Resolution: 17 ns)
	Number of timers for measurement	Start through end of program execution: 1 Start event through end event: 2
	Measurement results	Execution time (start through end of execution) Maximum, minimum, average, total, pass count (between events)
	Other	Timer overflow break function, timeout break function
Other functions		Command functions set in the console, mapping function, event function, coverage function, snapshot function, DMM function, power-off emulation function, pin mask function, flash self programming emulation function

Note The number of breaks that can be set varies depending on the location where the break is set.

1.3 System Configuration

This section shows the system configuration when using the QB-78K0RKX3 connected to a PC (Windows PC (Windows2000 and WindowsXP), PC/AT™ compatible). Connection is possible even without optional products.

Figure 1-1. System Configuration



<1> Host machine	: Windows PC (Windows2000 and WindowsXP), IBM PC/AT compatible can be used
<2> ID78K0R-QB Disk/Accessory Disk	: Debugger, USB drivers, manual, etc.
<3> USB interface cable	: Cable connecting QB-78K0RKX3 to host machine
<4> AC adapter	: Support input AC100 to 240 V
<5> QB-78K0RKX3	: This product
<6> Check pin adapter (optional)	: Adapter used for monitoring waveforms with oscilloscope
<7> Emulation probe	: High-characteristic FPC type emulation probe
<8> Exchange adapter	: Adapter that performs pin conversion
<9> Space adapter (optional)	: Adapter used for height adjustment
<10> YQ connector	: Connector that connects exchange adapter to target connector
<11> Target connector	: Connector soldered to target system
<12> Mount adapter (optional)	: Adapter used for mounting target device into socket
<13> Device	: Target device
<14> Target system	

Remarks 1. Obtain device files from the NEC Electronics website.

<http://www.necel.com/micro/ods/eng/>

2. Refer to **1.5 Package Contents** for the purchase forms of the above products.

3. As for handling of connectors, refer to **2.5 Mounting and Connecting Connectors**.

1.4 System Configuration for Each Target Device

The following table lists the system configuration for each target device of the QB-78K0RKX3.

<R>

Table 1-3. Adapters and Connectors for Each Target Device

Target Device	Package	Exchange Adaptor	Space Adaptor	YQ Connector	Target Connector	Mount Adaptor
78K0R/KE3	64GB	QB-64GB-EA-08T	QB-64GB-YS-01T	QB-64GB-YQ-01T	QB-64GB-NQ-01T	QB-64GB-HQ-01T
	64GK	QB-64GK-EA-06T	QB-64GK-YS-01T	QB-64GK-YQ-01T	QB-64GB-NQ-01T	QB-64GK-HQ-01T
78K0R/KF3	80GC	QB-80GC-EA-06T	QB-80GC-YS-01T	QB-80GC-YQ-01T	QB-80GC-NQ-01T	QB-80GC-HQ-01T
	80GK	QB-80GK-EA-06T	QB-80GK-YS-01T	QB-80GK-YQ-01T	QB-80GK-NQ-01T	QB-80GK-HQ-01T
78K0R/KG3	100GC	QB-100GC-EA-01T	QB-100GC-YS-01T	QB-100GC-YQ-01T	QB-100GC-NQ-01T	QB-100GC-HQ-01T
	100GF	QB-100GF-EA-04T	QB-100GF-YS-01T	QB-100GF-YQ-01T	QB-100GF-NQ-01T	QB-100GF-HQ-03T
78K0R/KH3	128GF	QB-128GF-EA-01T	QB-128GF-YS-01T	QB-128GF-YQ-01T	QB-128GF-NQ-01T	QB-128GF-HQ-01T
78K0R/KJ3	144GJ	QB-144GJ-EA-05T	QB-144GJ-YS-01T	QB-144GJ-YQ-01T	QB-144GJ-NQ-01T	QB-144GJ-HQ-01T

<R>

Table 1-4. Common Probe and Adapter

Name	Part Number
Check pin adapter	QB-144-CA-01
Emulation probe	QB-144-EP-02S

<R>

The adapter and connector for each device, and common probe and adapter are sold separately. An exchange adapter, a YQ connector, a target connector, and an emulation probe are included, depending on the order product name. For details, refer to **1.5 Package Contents**.

Remark For the package drawings of the connector, adapter, and probe, refer to the following URL.
<http://www.necel.com/micro/en/development/asia/Emulator/IE/iecube.html>

1.5 Package Contents

<R> The included products are described for each order product name.

Products supplied with QB-78K0RKX3-ZZZ

- 1: QB-78K0RKX3
- 2: AC adapter
- 3: USB interface cable (2 meters)
- 4: Probe holder
- 5: Online user registration card (warranty card and software contract in one)
- 6: ID78K0R-QB Disk (CD-ROM)
- 7: Accessory Disk (CD-ROM)
- 8: IECUBE Setup Manual (Japanese/English)
- 9: Packing list
- 10: QB-MINI2

Products supplied with QB-78K0RKX3-T144GJ

- 1 to 10
- 11: Emulation probe QB-144-EP-02S
 - 12: Exchange adapter QB-144GJ-EA-05T
 - 13: YQ connector QB-144GJ-YQ-01T
 - 14: Target connector QB-144GJ-NQ-01T

Products supplied with QB-78K0RKX3-T128GF

- 1 to 10
- 11: Emulation probe QB-144-EP-02S
 - 12: Exchange adapter QB-128GF-EA-01T
 - 13: YQ connector QB-128GF-YQ-01T
 - 14: Target connector QB-128GF-NQ-01T

Products supplied with QB-78K0RKX3-T100GC

- 1 to 10
- 11: Emulation probe QB-144-EP-02S
 - 12: Exchange adapter QB-100GC-EA-01T
 - 13: YQ connector QB-100GC-YQ-01T
 - 14: Target connector QB-100GC-NQ-01T

Products supplied with QB-78K0RKX3-T100GF

- 1 to 10
- 11: Emulation probe QB-144-EP-02S
 - 12: Exchange adapter QB-100GF-EA-04T
 - 13: YQ connector QB-100GF-YQ-01T
 - 14: Target connector QB-100GF-NQ-01T

Products supplied with QB-78K0RKX3-T80GC

- 1 to 10
- 11: Emulation probe QB-144-EP-02S
 - 12: Exchange adapter QB-80GC-EA-06T

- 13: YQ connector QB-80GC-YQ-01T
- 14: Target connector QB-80GC-NQ-01T

Products supplied with QB-78K0RKX3-T80GK

1 to 10

- 11: Emulation probe QB-144-EP-02S
- 12: Exchange adapter QB-80GK-EA-06T
- 13: YQ connector QB-80GK-YQ-01T
- 14: Target connector QB-80GK-NQ-01T

Products supplied with QB-78K0RKX3-T64GB

1 to 10

- 11: Emulation probe QB-144-EP-02S
- 12: Exchange adapter QB-64GB-EA-08T
- 13: YQ connector QB-64GB-YQ-01T
- 14: Target connector QB-64GB-NQ-01T

Products supplied with QB-78K0RKX3-T64GK

1 to 10

- 11: Emulation probe QB-144-EP-02S
- 12: Exchange adapter QB-64GK-EA-06T
- 13: YQ connector QB-64GK-YQ-01T
- 14: Target connector QB-64GK-NQ-01T

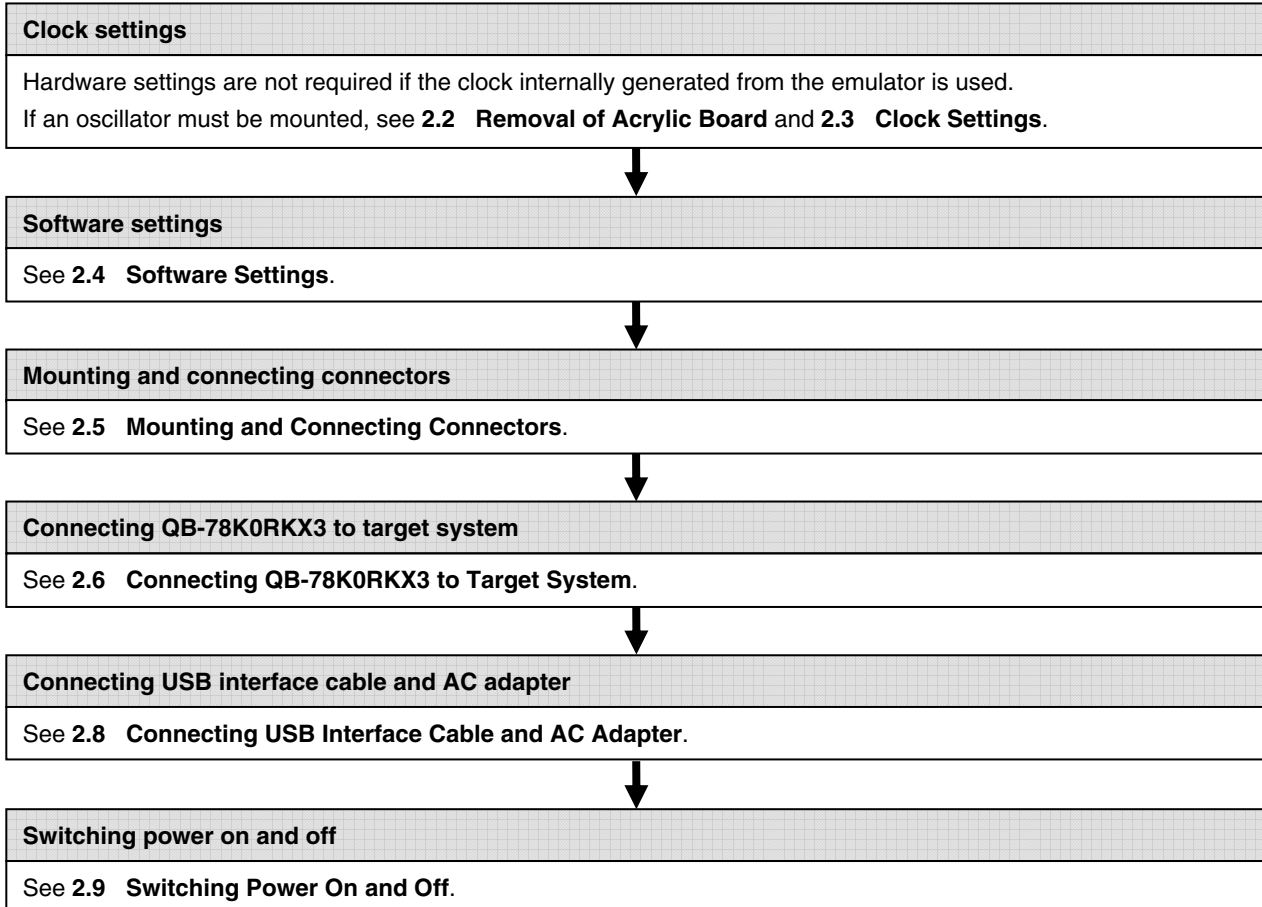
CHAPTER 2 SETUP PROCEDURE

This chapter explains the QB-78K0RKX3 setup procedure.

Setup can be completed by performing installation setup in the order in which it appears in this chapter.

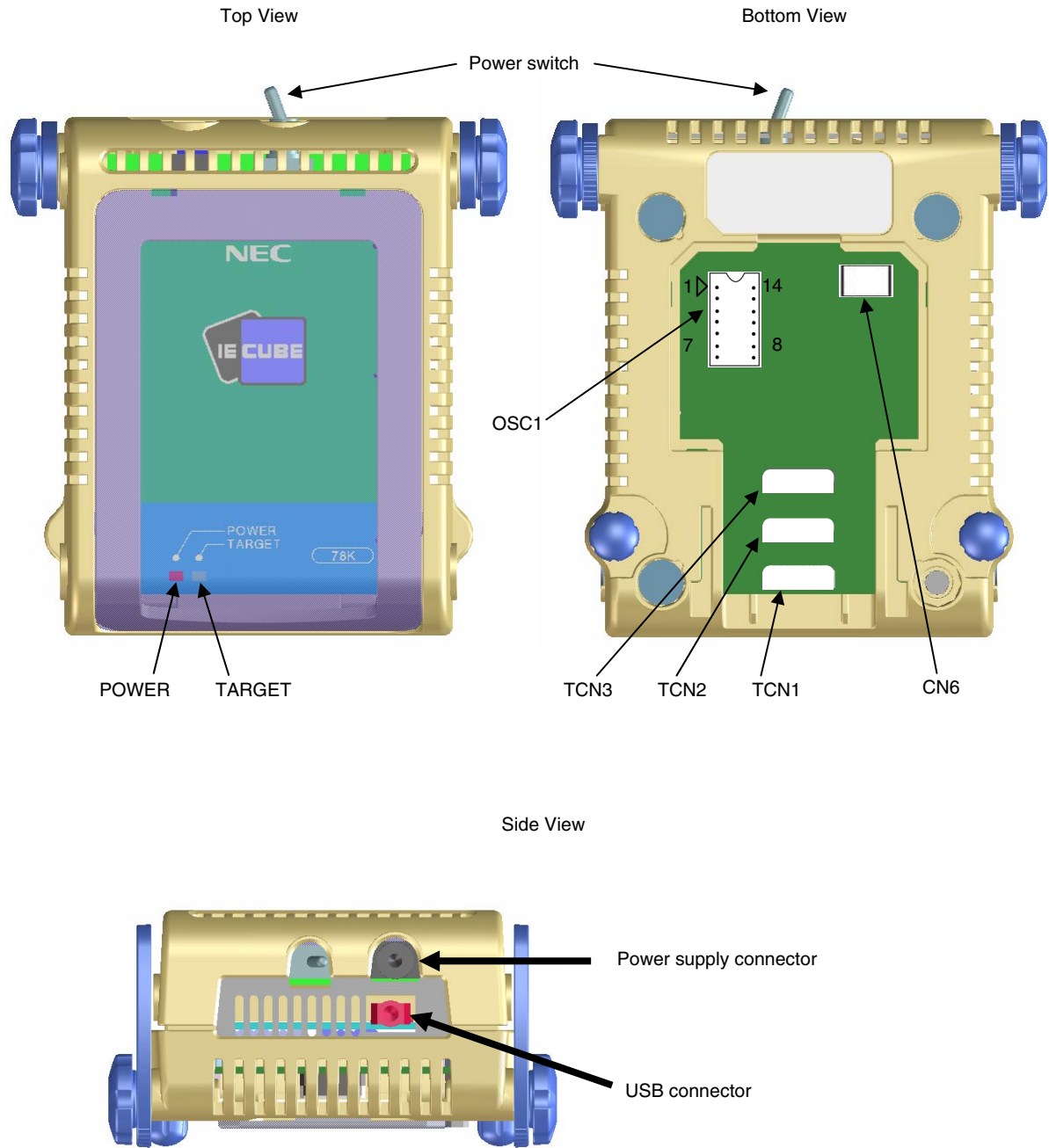
Perform setup along the lines of the following procedure.

See **2.1 Names and Functions of Hardware** for clock positions.



2.1 Names and Functions of Hardware

Figure 2-1. Names of Parts of QB-78K0RKX3



(1) TCN1, TCN2, TCN3

These are connectors for connecting a check pin adapter or emulation probe.

(2) OSC1

This is a socket for mounting the oscillator.

(3) CN6

This is a connector for the shipment inspection. It is not something that the user will need.

(4) POWER (Red LED)

This is an LED that shows whether the power supply of the QB-78K0RKX3 is switched on.

LED State	QB-78K0RKX3 State
Lit	Power switch ON
Not lit	Power switch OFF or AC adapter not connected to QB-78K0RKX3
Blinking	Internal error occurred (Contact an NEC Electronics sales representative or distributor)

(5) TARGET (Green LED)

This is an LED that shows whether the power supply of the target system is switched on.

LED State	Target System State
Lit	Target system power supply ON
Not lit	Target system power supply OFF or target system not connected

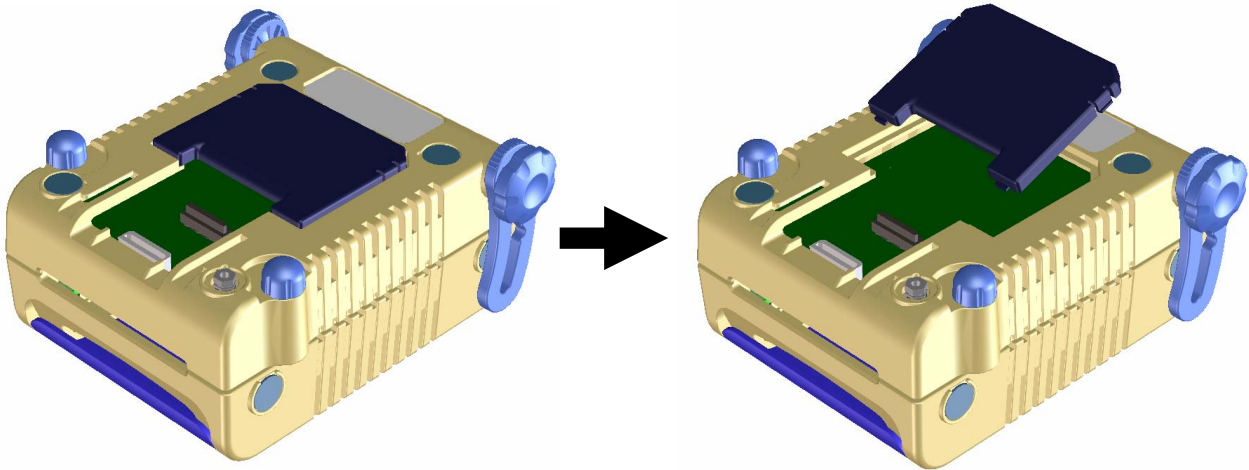
(6) Power switch

This is the power switch of the QB-78K0RKX3.
It is OFF at shipment.

2.2 Removal of Acrylic Board

To modify the clock setting, the acrylic board on the bottom of the QB-78K0RKX3 must be removed. The acrylic board can be removed by lifting it up.

Figure 2-2. Acrylic Board Removal Method



<R> 2.3 Clock Settings**2.3.1 Overview of clock settings**

The following four types of clock settings are available.

Each clock setting is listed below.

Clock Used	Clock Supply	Debugger Setting (in Configuration Dialog)
(1) High-speed system clock ^{Note 1} (X1 oscillator or External input)	(a) When the clock generated within the emulator is used	System
	(b) When the clock is supplied from the target system	External
	(c) When the oscillator (OSC1) mounted onto the emulator is used	Clock Socket
(2) Internal high-speed oscillation clock	Uses the clock internally generated from the emulator	–
(3) Internal low-speed oscillation clock	Uses the clock internally generated from the emulator	–
(4) Subsystem clock ^{Note 2} (XT1 oscillator)	(a) When the clock generated within the emulator is used	System
	(b) When the clock is supplied from the target system	External

Notes 1. First, select “System” in the debugger settings (refer to (a) When the clock generated within the emulator is used, in (1) High-speed system clock).

If there is no clock that can be selected, follow the descriptions below.

- If the target system clock can supply a square wave for the emulator:

Select “External” in the debugger settings (refer to (b) When the clock is supplied from the target system, in (1) High-speed system clock).

- If the target system clock cannot supply a square wave for the emulator:

Mount onto the emulator the oscillator of the clock to be used and select “Clock Socket” in the debugger settings (refer to (c) When the oscillator (OSC1) mounted onto the emulator is used, in (1) High-speed system clock).

2. First, select “System” in the debugger settings (refer to (a) When the clock generated within the emulator is used, in (4) Subsystem clock).

If there is no clock that can be selected, it can be supplied from the target system clock. A square wave, however, must be supplied (refer to (b) When the clock is supplied from the target system, in (4) Subsystem clock).

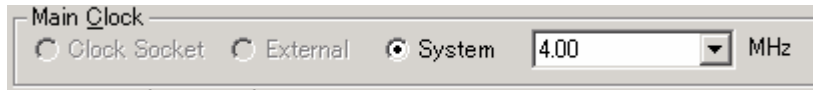
Oscillation with the resonator on the target system is not supported. Therefore, the in-circuit emulator cannot emulate the oscillation operation of the clock on the target system.

(1) High-speed system clock

The clock settings are listed below.

Table 2-1. Settings for High-Speed System Clock

Type of Clock to Be Used	OSC1	Debugger Setting
(a) When the clock generated within the emulator is used	–	System
(b) When the clock is supplied from the target system ^{Note}	–	External
(c) When the oscillator (OSC1) mounted onto the emulator is used	Oscillator mounted	Clock Socket



Note This setting is not possible when TARGET LED is not lit.

Remarks 1. Settings other than the above are prohibited.

2. Selection of (a) or (b) is possible regardless of whether the oscillator is not mounted in the OSC1 socket.

(a) When the clock generated within the emulator is used

Select the “System” in the debugger and select the desired frequency from the drop-down list.

The following frequencies are selectable.

2.00, 3.00, 3.57, 4.00, 4.19, 4.91, 5.00, 6.00, 8.00, 8.38, 10.00, 12.00, 16.00, 20.00 [MHz]

(b) When the clock is supplied from the target system

Select the “External” in the debugger. The clock input from the target system is then used.

Oscillation with the resonator on the target system is not supported. To input a clock from the target system, input to the clock pin (X2) the square-wave signal with the same voltage potential as that of the target device supply voltage (V_{DD}). Inputting the inverted signal to X1 is not necessary.

The selectable frequencies are same as those of the target device.

(c) When the oscillator (OSC1) mounted onto the emulator is used

Mount an oscillator in the OSC1 socket in the emulator and then select the “Clock socket” in the debugger. The clock generated from the oscillator mounted on the emulator is used.

The selectable frequencies are same as those of the target device.

As an oscillator^{Note} to be mounted in the OSC1 socket in the emulator, use the one that satisfies the following specifications.

- Supply voltage: 5 V
- Output level: CMOS

Note An oscillator that uses a resonator cannot be used.

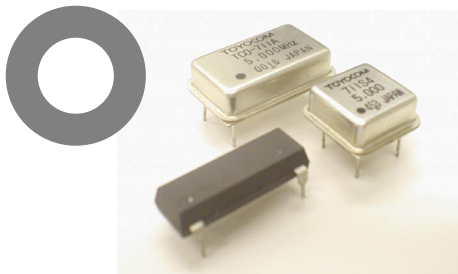


Figure 2-3. Oscillator Shape

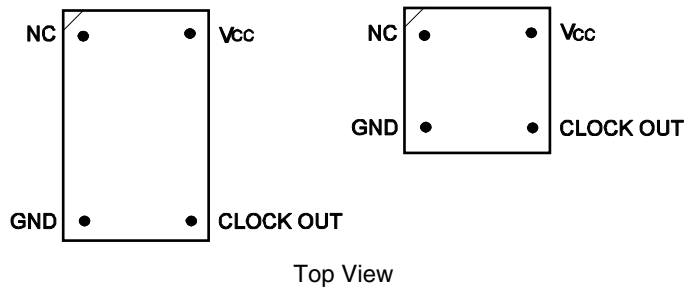
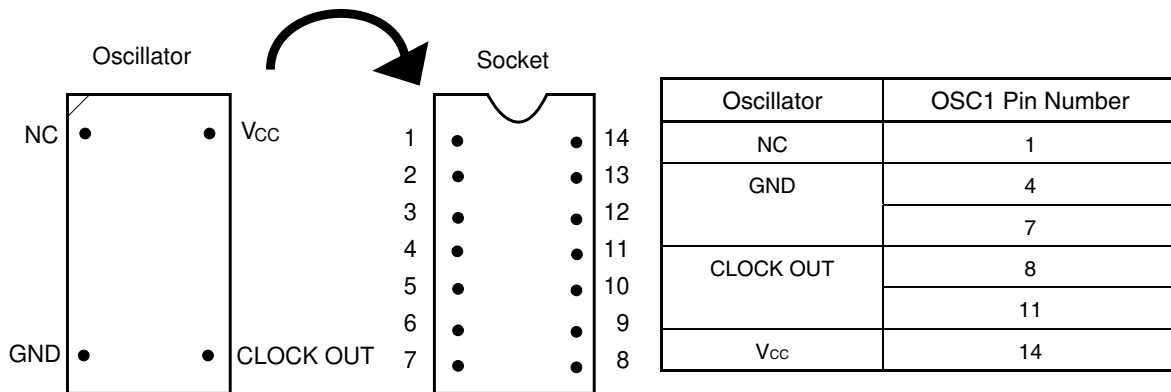


Figure 2-4. Mapping of Oscillator to Socket



Remark Insert the oscillator into the socket, take care for the pin 1 position.

(2) Internal high-speed oscillation clock

The debugger setting is not necessary.

The use of the internal high-speed oscillation clock can be specified in the user program.

(3) Internal low-speed oscillation clock

The debugger setting is not necessary.

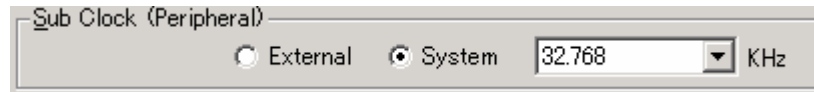
The use of the internal low-speed oscillation clock can be specified in the user program.

(4) Subsystem clock

The clock settings are listed below.

Table 2-2. Settings for Subsystem Clock

Type of Clock to Be Used	Debugger Setting
(a) When the clock generated within the emulator is used	System
(b) When the clock is supplied from the target system ^{Note}	External



Note This setting is not possible when TARGET LED is not lit.

Remark Settings other than above are prohibited.

(a) When the clock generated within the emulator is used

Select the “System” in the debugger and select “32.768” [kHz] as the frequency from the drop-down list.

32.768 [kHz]

Remark “38.400” [kHz] can also be selected from the list, but do not select this frequency; it is not supported by the device.

(b) When the clock is supplied from the target system

Select the “External” in the debugger. The clock input from the target system is then used.

Oscillation with the resonator on the target system is not supported. To input a clock from the target system, input to the clock pin (XT2) the square-wave signal with the same voltage potential as that of the target device supply voltage (V_{DD}). Inputting the inverted signal to XT1 is not necessary.

The selectable frequencies are same as those of the target device.

2.4 Software Settings

For details, see the **ID78K0R-QB Ver. 3.20 Integrated Debugger Operation User's Manual** (U17839E).