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QH03TZ600 Qspeed[™] Family

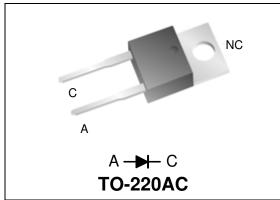


600 V, 3 A H-Series PFC Diode

Product Summary

I _{F(AVG)}	3	Α
V_{RRM}	600	V
Q _{RR} (Typ at 125 °C)	14.8	nC
I _{RRM} (Typ at 125 °C)	1.35	Α
Softness t _b /t _a (Typ at 125 °C)	1.1	

Pin Assignment



RoHS Compliant

Package uses Lead-free plating and Green mold compound. Halogen free per IEC 61249-2-21.

General Description

This device has the lowest Q_{RR} of any 600V Silicon diode. Its recovery characteristics increase efficiency, reduce EMI and eliminate snubbers.

Applications

- Power Factor Correction (PFC) Boost Diode
- Motor drive circuits
- DC-AC Inverters

Features

- Low Q_{RR}, Low I_{RRM}, Low t_{RR}
- High dl_F/dt capable (1000A/μs)
- Soft recovery

Benefits

- Increases efficiency
 - Eliminates need for snubber circuits
 - Reduces EMI filter component size & count
- · Enables extremely fast switching

Absolute Maximum Ratings

Absolute maximum ratings are the values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Symbol	Parameter	Conditions	Rating	Units
V _{RRM}	Peak repetitive reverse voltage		600	V
I _{F(AVG)}	Average forward current	$T_J = 150 ^{\circ}\text{C}, T_C = 120 ^{\circ}\text{C}$	3	Α
I _{FSM}	Non-repetitive peak surge current	60 Hz, ½ cycle	35	Α
I _{FSM}	Non-repetitive peak surge current	½ cycle of t=28 μs Sinusoid, T _C =25 °C	350	Α
$T_{J(MAX)}$	Maximum junction temperature		150	°C
T _{STG}	Storage temperature		-55 to 150	°C
	Lead soldering temperature	Leads at 1.6 mm from case, 10 sec	300	°C
V _{ISOL}	Isolation voltage (leads-to-tab)	AC	2500	V
P _D	Power dissipation	T _C = 25 °C	30.4	W

Thermal Resistance

Symbol	Resistance from:	Conditions	Rating	Units
$R_{\theta JA}$	Junction to ambient	TO-220	62	°C/W
R _{eJC}	Junction to case	TO-220	4.1	°C/W

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Electrical Specifications at T_J= 25 °C (unless otherwise specified)

Symbol	Parameter	Conditions		Min	Тур	Max	Units
DC Chara	acteristics	•	<u>.</u>				
I _R	Reverse current	$V_R = 600 \text{ V}, T_J = 25$	5 °C	-	-	250	μΑ
		$V_R = 600 \text{ V}, T_J = 12$	25 °C	-	0.22	-	mA
V _F	Forward voltage	I _F = 3 A, T _J = 25 °C		-	2.52	3.0	V
		I _F = 3 A, T _J = 150 °C	С	-	2.1	-	٧
CJ	Junction capacitance	V _R = 10 V, 1 MHz		-	11	-	pF
Dynamic	Characteristics						
t _{RR}	Reverse recovery time	dI/dt =200 A/μs	T _J =25 °C	-	9.8	-	ns
		V _R =400 V, I _F =3 A	T _J =125 °C	-	16.1	-	ns
Q _{RR}	Reverse recovery charge	dI/dt =200 A/μs	T _J =25 °C	-	5.8	10	nC
		$V_R=400 \text{ V}, I_F=3 \text{ A}$	T _J =125 °C	-	14.8	-	nC
I _{RRM}	Maximum reverse	dI/dt =200 A/μs	T _J =25 °C	-	0.93	1.4	Α
	recovery current	V _R =400 V, I _F =3 A	T _J =125 °C	-	1.35	-	Α
S	- th	dI/dt =200 A/μs V _R =400 V, I _F =3 A	T _J =25 °C	-	0.92	-	
	Softness factor = $\frac{t_b}{t_a}$		T _J =125 °C	-	1.1	-	

Note to component engineers: H-Series diodes employ Schottky technologies in their design and construction. Therefore, Component Engineers should plan their test setups to be similar to those for traditional Schottky test setups. (For additional details, see Application Note AN-300.)

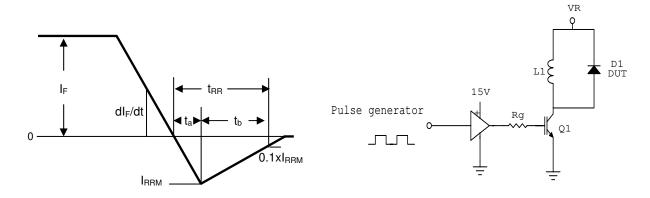
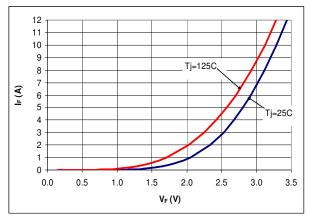


Figure 1. Reverse Recovery Definitions

Figure 2. Reverse Recovery Test Circuit

Electrical Specifications at T_J= 25 °C (unless otherwise specified)



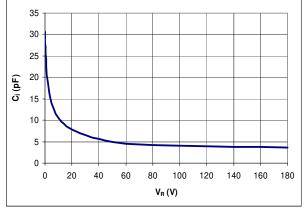
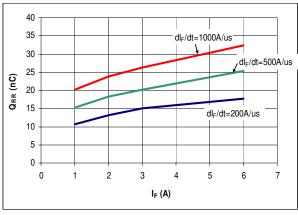


Figure 3. Typical I_F vs V_F

Figure 4. Typical Typical C_i vs V_R



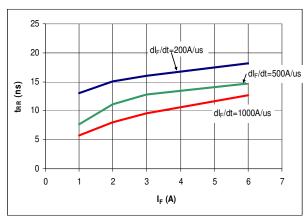
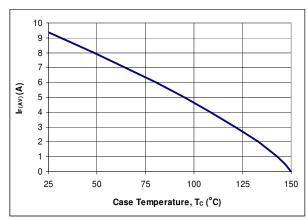


Figure 5. Typical Q_{RR} vs I_F at $T_J = 125$ °C

Figure 6. Typical t_{RR} vs I_F at T_J = 125 °C



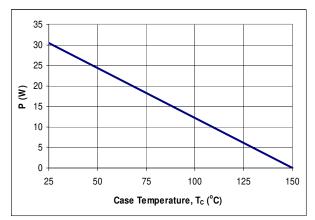


Figure 7. DC Current Derating Curve

Figure 8. Power Derating Curve

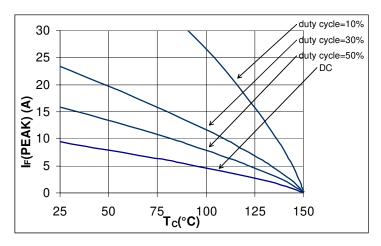


Figure 9. $I_{\rm F}({\rm PEAK})$ vs ${\rm T_c}$, f=70 kHz

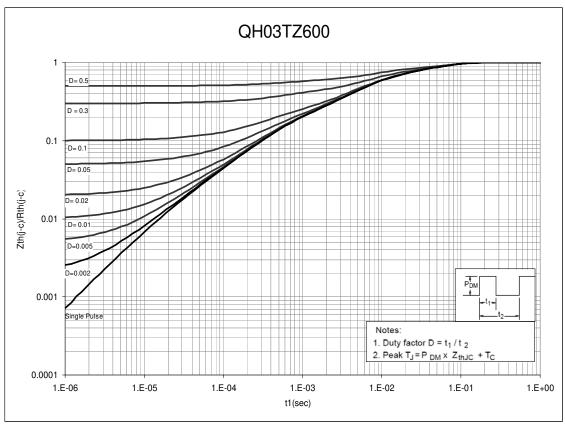
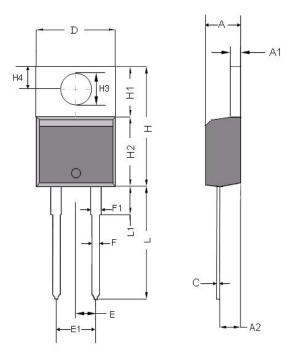


Figure 10. Normalized Maximum Transient Thermal Impedance

Dimensional Outline Drawings



	Millimeters		
Dim	MIN	MAX	
Α	4.32	4.70	
A 1	1.14	1.40	
A 2	2.03	2.79	
С	0.34	0.610	
D	9.65	10.67	
E	2.49	2.59	
E1	4.98	5.18	
F	0.508	1.016	
F1	1.14	1.78	
Н	14.71	16.51	
H1	5.84	6.795	
H2	8.40	9.00	
Н3	3.53	3.96	
H4	2.54	3.05	
L	12.70	14.22	
L1	-	6.35	

Mechanical Mounting Method	Maximum Torque / Pressure specification	
Screw through hole in package tab	1 Newton Meter (nm) or 8.8 inch-pounds (lb-in)	
Clamp against package body	12.3 kilogram-force per square centimeter (kgf/cm²) or 175 lbf/in²	

Soldering time and temperature: This product has been designed for use with high-temperature, lead-free solder. The component leads can be subjected to a maximum temperature of 300 °C, for up to 10 seconds. See Application Note AN-303, for more details.

Ordering Information

Part Number	Package	Packing
QH03TZ600	TO-220AC	50 units/tube

The information contained in this document is subject to change without notice.

QH03TZ600

Revision	Notes	Date
1.0	Released by Qspeed	01/10
1.1	Converted to Power Integrations Document	01/11



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