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2.5V Drive Nch+Pch MOSFET

QS6M4

●Structure

Silicon P-channel MOSFET Silicon N-channel MOSFET

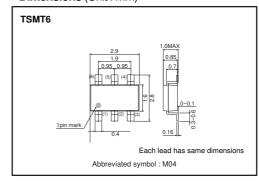
● Features

- 1) The QS6M4 combines Pch MOSFET with a Nch MOSFET in a single TSMT6 package.
- 2) Low on-state resistance with a fast switching.
- 3) Low voltage drive (2.5V).

Applications

Load switch, inverter

●Dimensions (Unit:mm)



Packaging specifications

	Package	Taping
Type	Code	TR
	Basic ordering unit (pieces)	3000
QS6M4		0

● Absolute maximum ratings (Ta=25°C)

Parameter		Currente el	Lin	Unit	
		Symbol	Nchannel	Nchannel Pchannel	
Drain-source voltage		V _{DSS}	30	-20	V
Gate-source voltage		V_{GSS}	±12	±12	V
Drain current	Continuous	lσ	±1.5	±1.5	Α
Drain current	Pulsed	I _{DP} *1	±6.0	±6.0	Α
Source current	Continuous	Is	0.8	-0.75	Α
(Body diode)	Pulsed	Isp *1	6.0 –6.0		Α
Total power dissipation		D- *2	Pp.*2 1.25		
		PD '-	0	W / ELEMENT	
Channel temperature		Tch	150		°C
Storage temperature		Tstg	-55 to +150		°C

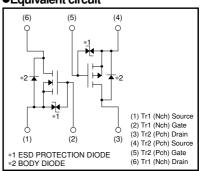
^{*1} Pw≤10µs, Duty cycle≤1% *2 Mounted on a ceramic board

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Dth (oh o)*	100	°C / W / TOTAL
Chame to ambient	Rth (ch-a)	139	°C / W / FI FMFNT

^{*} Mounted on a ceramic board

●Equivalent circuit



●Electrical characteristics (Ta=25°C)

<Tr1. N-ch MOSFET>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±10	μΑ	V _{GS} =±12V / V _{DS} =0V
Drain-source breakdown voltage	V(BR) DSS	30	_	_	٧	I _D =1mA / V _{GS} =0V
Zero gate voltage drain current	IDSS	_	_	1	μΑ	V _{DS} =30V / V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	0.5	-	1.5	٧	V _{DS} =10V / I _D =1mA
Obstitution and a second state	*	-	170	230		I _D =1.5A / V _{GS} =4.5V
Static drain-source on-state resistance	R _{DS (on)}	_	180	245	mΩ	I _D =1.5A / V _{GS} =4.0V
- I esistance		-	260	360		I _D =1.0A / V _{GS} =2.5V
Forward transfer admittance	Y _{fs} *	1.0	-	_	S	V _{DS} =10V / I _D =1.0A
Input capacitance	Ciss	_	80	_	рF	V _{DS} =10V
Output capacitance	Coss	_	25	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	-	15	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	_	7	_	ns	I _D =1A, V _{DD} ≒15V
Rise time	tr *	_	18	_	ns	V _{GS} =4.5V
Turn-off delay time	t _{d (off)} *	_	15	_	ns	$R_L=15\Omega / R_G=10\Omega$
Fall time	t _f *	-	15	_	ns	
Total gate charge	Qg *	-	1.6	_	nC	V _{DD} ≒15V R _L =10Ω
Gate-source charge	Q _{gs} *	-	0.5	_	nC	V _{GS} =4.5V R _G =10Ω
Gate-drain charge	Q _{gd} *	1	0.9	_	nC	I _D =1.5A

^{*}Pulsed

●Body diode characteristics (Source-Drain)

<Tr1. N-ch MOSFET>

Parameter	Symbol	Min.	Тур.	Мах.	Unit	Conditions
Forward voltage	V _{SD} *	_	_	1.2	V	Is=3.2A / Vgs=0V

^{*}Pulsed



●Electrical characteristics (Ta=25°C)

<Tr2. P-ch MOSFET>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±10	μΑ	V _{GS} = ±12V / V _{DS} =0V
Drain-source breakdown voltage	V _{(BR) DSS}	-20	-	_	٧	I _D =-1mA / V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	-	-	-1	μΑ	V _{DS} = -20V / V _{GS} =0V
Gate threshold voltage	VGS (th)	-0.7	_	-2.0	٧	V _{DS} = -10V / I _D =-1mA
Otatio during a superior atata		_	155	215		$I_D = -1.5A / V_{GS} = -4.5V$
Static drain-source on-state resistance	R _{DS (on)} *	_	170	235	mΩ	I _D = -1.5A / V _G S= -4.0V
resistance		-	310	430		I _D = -0.75A / V _G S= -2.5V
Forward transfer admittance	Y _{fs} *	1.0	_	_	S	V _{DS} = -10V / I _D = -0.75A
Input capacitance	Ciss	_	270	_	pF	V _{DS} = -10V
Output capacitance	Coss	_	40	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	_	35	_	pF	f=1MHz
Turn-on delay time	td (on) *	_	10	_	ns	I _D = −0.75A, V _D D = −15V
Rise time	tr *	_	12	_	ns	V _{GS} = -4.5V
Turn-off delay time	td (off) *	_	45	_	ns	$R_L=20\Omega / R_G=10\Omega$
Fall time	t _f *	-	20	_	ns	
Total gate charge	Qg *	_	3.0	_	nC	V _{DD} ≒ −15V R _L =10Ω
Gate-source charge	Q _{gs} *		0.8		nC	$V_{GS}=-4.5V$ $R_{G}=10\Omega$
Gate-drain charge	Q _{gd} *	_	0.85	_	nC	I _D = -1.5A

^{*}Pulsed

●Body diode characteristics (Source-Drain)

<Tr2. P-ch MOSFET>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp	_	_	-1.2	V	Is= -0.75A / Vgs=0V

N-ch •Electrical characteristic curves

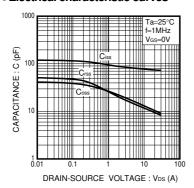


Fig.1 Typical Capacitance vs. Drain-Source Voltage

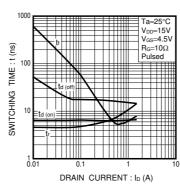


Fig.2 Switching Characteristics

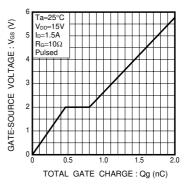


Fig.3 Dynamic Input Characteristics

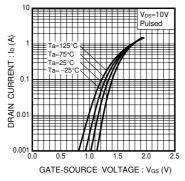


Fig.4 Typical Transfer Characteristics

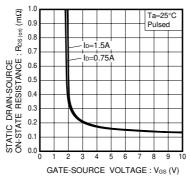


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

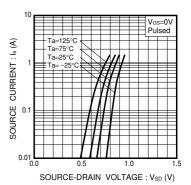


Fig.6 Source Current vs. Source-Drain Voltage

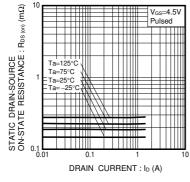


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

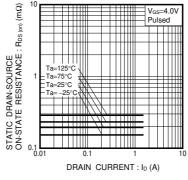


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

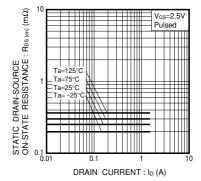


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

P-ch •Electrical characteristic curves

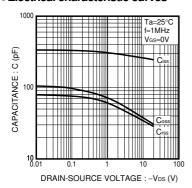


Fig.1 Typical Capacitance vs. Drain-Source Voltage

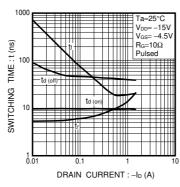


Fig.2 Switching Characteristics

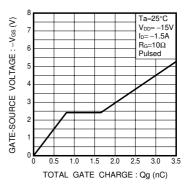


Fig.3 Dynamic Input Characteristics

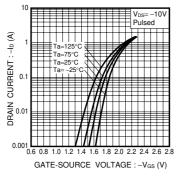


Fig.4 Typical Transfer Characteristics

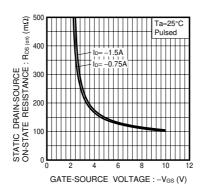


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

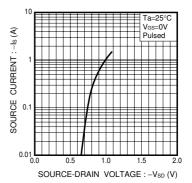


Fig.6 Source Current vs. Source-Drain Voltage

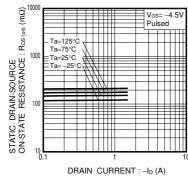


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

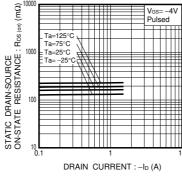


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

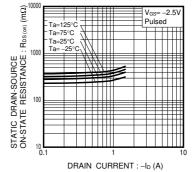


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

Rev.B

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