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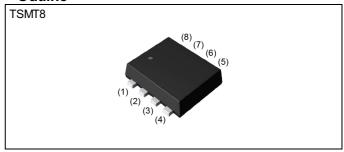
#### -12V Pch+Pch Middle Power MOSFET

V <sub>DSS</sub>	-12V
R <sub>DS(on)</sub> (Max.)	22mΩ
I <sub>D</sub>	±5.5A
P <sub>D</sub>	1.5W

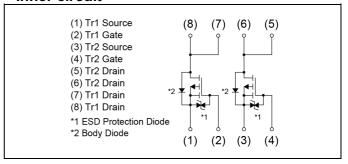
#### Features

- 1) Low on resistance.
- 2) Small Surface Mount Package .
- 3) Pb-free lead plating; RoHS compliant.
- 4) Halogen Free.

#### Outline



### ●Inner circuit



Packaging specifications

	- r working opcomoducite							
	Packing	Embossed Tape						
	Reel size (mm)	180						
Туре	Tape width (mm)	8						
	Basic ordering unit (pcs)	3000						
	Taping code	TR						
	Marking	J13						

# Application

Switching

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C) < It is the same ratings for the Tr1 and Tr2>

Parameter	Symbol	Value	Unit		
Drain - Source voltage		$V_{DSS}$	-12	V	
Continuous drain current		I <sub>D</sub> *1	±5.5	Α	
Pulsed drain current	I <sub>D,pulse</sub> *2	±18	Α		
Gate - Source voltage	V <sub>GSS</sub>	0~-8	V		
Dougs dissipation	total	P <sub>D</sub> *3	1.5	W	
Power dissipation	element	- P <sub>D</sub> °	1.25		
Junction temperature	T <sub>j</sub>	150	°C		
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C		

### ●Thermal resistance

Parameter		Cymbol	Values			l limit
		Symbol	Min.	Тур.	Max.	Unit
Thermal registance investign, ambient	total	D *3	-	83.3	-	°C/W
Thermal resistance, junction - ambient	element	R <sub>thJA</sub> *3	-	100	-	C/VV

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < It is the same characteristics for the Tr1 and Tr2>

	0 1 1	0 19		Values			
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = -1mA$	-12	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-5.0	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -12V, V <sub>GS</sub> = 0V	-	-	-10	μA	
Gate - Source leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = -8V	-	-	-10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = -6V, I_{D} = -1mA$	-0.3	-	-1.0	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{\text{GS(th)}}}{\DeltaT_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	2.7	-	V/°C	
		$V_{GS}$ = -4.5V, $I_{D}$ = -5.5A	-	15	22		
Static drain - source	D *4	$V_{GS}$ = -2.5V, $I_D$ = -2.7A	1	19	28	m0	
on - state resistance	R <sub>DS(on)</sub> *4	$V_{GS} = -1.8V, I_D = -2.7A$	-	24	38	mΩ	
		V <sub>GS</sub> = -1.5V, I <sub>D</sub> = -1.1A	-	29	58		
Transconductance	g <sub>fs</sub> *4	$V_{DS} = -6V, I_{D} = -5.5A$	8.5	-	-	S	

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> Mounted on a ceramic board.

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < It is the same characteristics for the Tr1 and Tr2>

Darameter	Cumb of	Conditions		Unit			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uniil	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	6300	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -6V	-	750	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	750	-		
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq -6V, V_{GS} = -4.5V$	-	13	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -2.7A	-	100	-		
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L = 2.2\Omega$	-	400	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	200	-		

# ● Gate charge characteristics (T<sub>a</sub> = 25°C) < It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol Conditions -	Conditions	Values			l leit
		Min.	Тур.	Max.	Unit	
Total gate charge	$Q_g^{*4}$	$V_{DD} \simeq -6V$ , $I_{D} = -5.5A$ $V_{GS} = -4.5V$	-	60	-	
Gate - Source charge	Q <sub>gs</sub> *4		-	10	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4		-	9	-	

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

<It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol			Unit		
- Farameter	arameter Symbol Conditions	Min.	Тур.	Max.	Unit	
Body diode continuous forward current	I <sub>S</sub> *1	T <sub>a</sub> = 25°C	-	-	-1	^
Body diode pulse current	l <sub>SP</sub> *2		-	-	-18	Α
Forward voltage	V <sub>SD</sub> *4	$V_{GS} = 0V, I_S = -5.5A$	-	-	-1.2	V

Fig.1 Power Dissipation Derating Curve

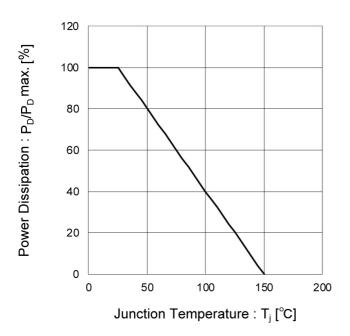
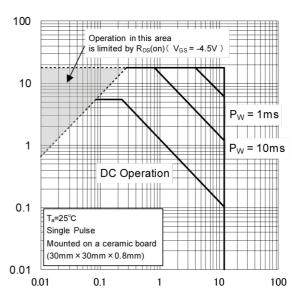


Fig.2 Maximum Safe Operating Area



Drain Current: -l<sub>D</sub> [A]

Drain - Source Voltage: -VDS [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

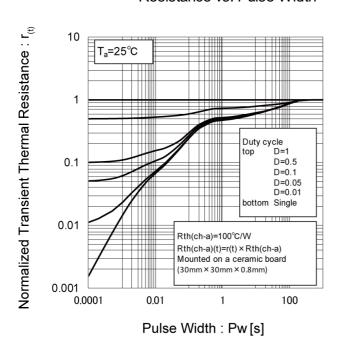
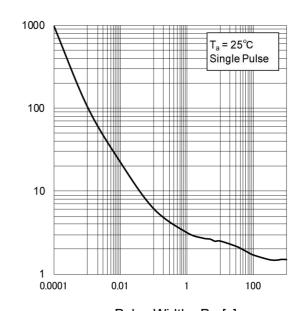


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width : Pw [s]

Peak Transient Power: P(W)

Drain Current : -I<sub>D</sub> [A]

#### • Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

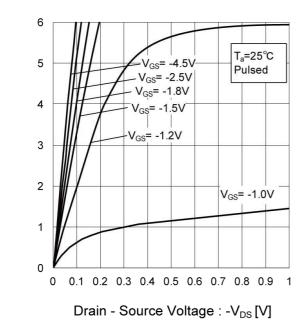
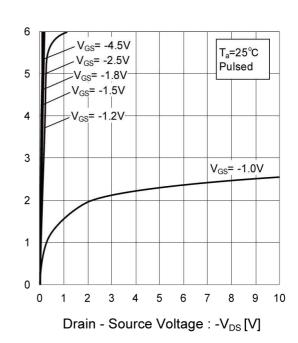


Fig.6 Typical Output Characteristics(II)



Drain Current : -I<sub>D</sub> [A]

Fig.7 Breakdown Voltage vs. Junction Temperature

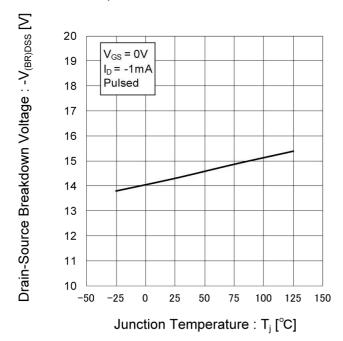


Fig.8 Typical Transfer Characteristics

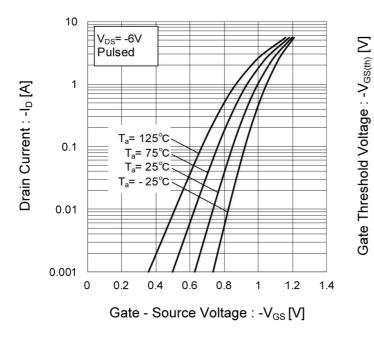


Fig.9 Gate Threshold Voltage vs. Junction Temperature

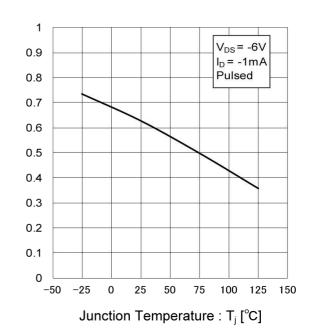


Fig.10 Tranceconductance vs. Drain Current

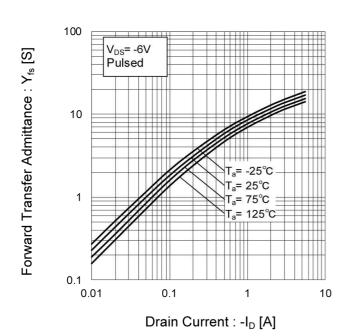


Fig.11 Drain Current Derating Curve

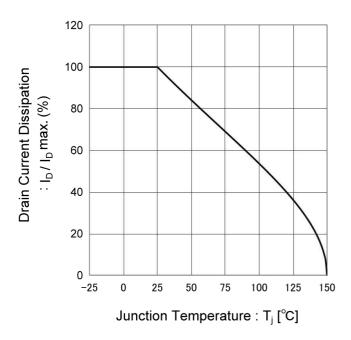


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

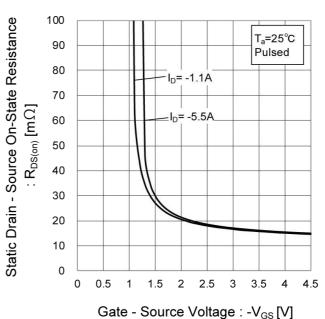


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

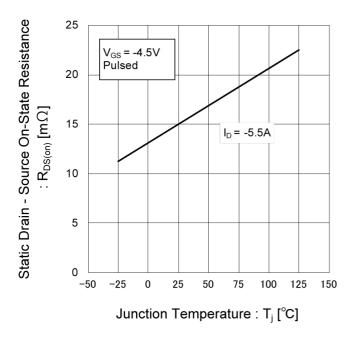


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

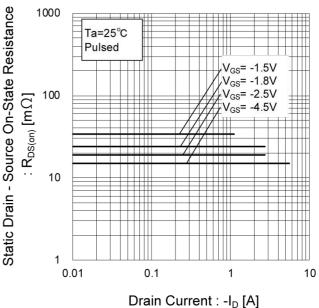


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

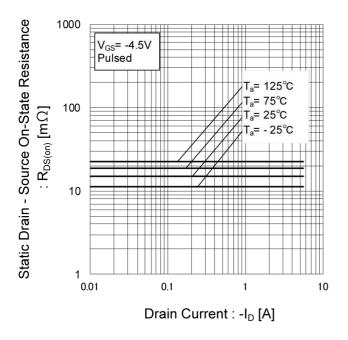


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

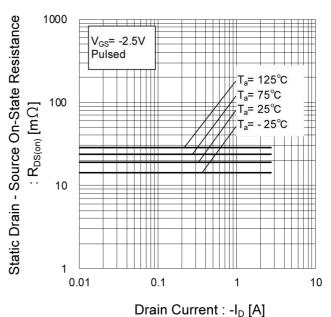


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

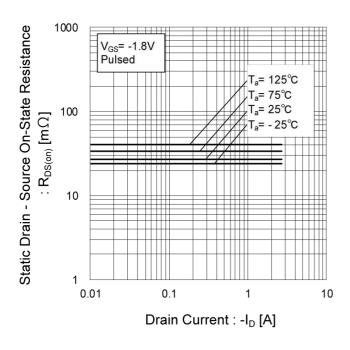


Fig.18 Static Drain - Source On - State Resistance vs. Drain Current(V)

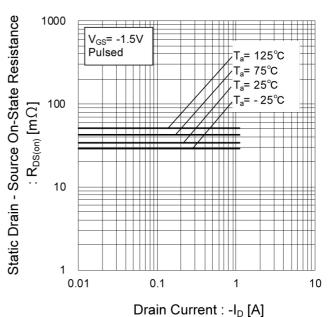


Fig.19 Typical Capacitance vs. Drain - Source Voltage

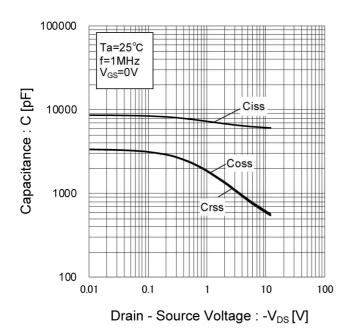


Fig.20 Switching Characteristics

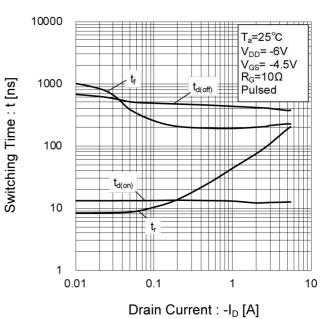


Fig.21 Dynamic Input Characteristics

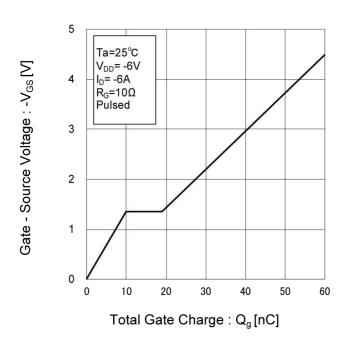
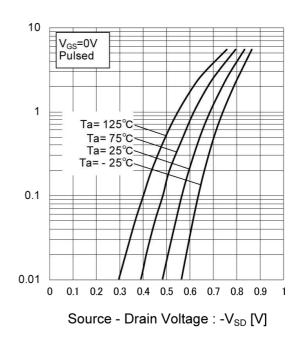


Fig.22 Source Current vs. Source Drain Voltage



Source Current : -I<sub>s</sub> [A]

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

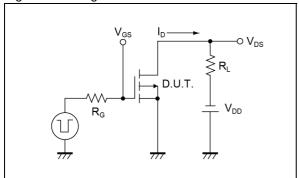


Fig.2-1 Gate Charge Measurement Circuit

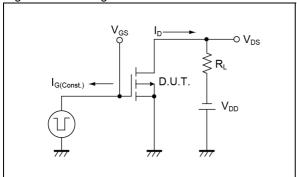


Fig.1-2 Switching Waveforms

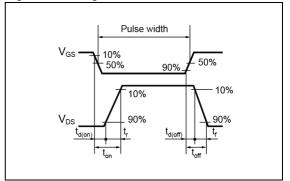
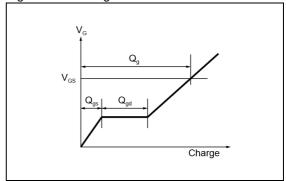
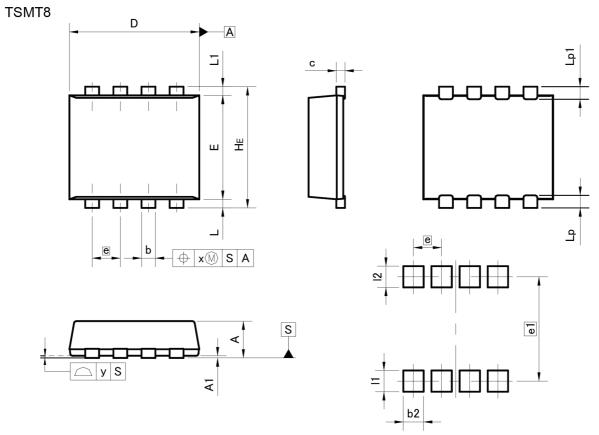


Fig.2-2 Gate Charge Waveform



### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.75	0.85	0.030	0.033
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
С	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	2.30	2.50	0.091	0.098
е	0.	65	0.0	26
HE	2.70	2.90	0.106	0.114
L	0.10	0.30	0.004	0.012
L1	0.10	0.30	0.004	0.012
Lp	0.19	0.39	0.007	0.015
Lp1	0.19	0.39	0.007	0.015
х	-	0.10	_	0.004
У	-	0.10	-	0.004

	DIM	MILIMI	ETERS	INC	HES
		MIN	MAX	MIN	MAX
	b2	-	0.47	Ī	0.019
	e1	2.4	41	0.0	95
	11	1	0.49		0.019
	12	-	0.49	-	0.019

Dimension in mm/inches



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