imall

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QS8K13

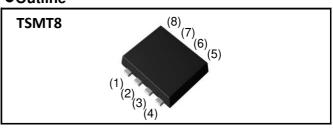
Dual Nch 30V 6.0A Power MOSFET

V _{DSS}	30V
R _{DS(on)} (Max.)	$28 \text{m}\Omega$
I _D	6A
P _D	1.5W

Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT8).
- 4) Pb-free lead plating ; RoHS compliant

Outline



Inner circuit

	(8) (7) (6) (5)
(1) Tr1 Source (5) Tr2 Drain (2) Tr1 Gate (6) Tr2 Drain (3) Tr2 Source (7) Tr1 Drain (4) Tr2 Gate (8) Tr1 Drain	
*1 ESD PROTECTION DIODE *2 BODY DIODE	(1) (2) (3) (4)

Packaging specifications

	Packaging	Taping
	Reel size (mm)	180
Type	Tape width (mm)	8
Туре	Basic ordering unit (pcs)	3,000
	Taping code	TR
	Marking	K13

Application

DC/DC converters

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	30	V
Continuous drain current	ا _D ^{*1}	±6	А
Pulsed drain current	I _{D,pulse} *2	±18	A
Gate - Source voltage	V _{GSS}	±20	V
Dower disainstian	P _D *3	1.5	W
Power dissipation	P _D ^{*4}	0.55	W
Junction temperature	Tj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

Thermal resistance

Parameter	Symbol	Values			Unit
Farameter	Symbol	Min.	Тур.	Max.	Onit
Thermal resistance, junction - ambient	R_{thJA} *3	-	-	83.3	°C/W
Thermal resistance, junction - ambient	R_{thJA} *4	-	-	227	°C/W

●Electrical characteristics(T_a = 25°C)

<It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
Farameter			Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 1mA$	30	-	-	v
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	35	-	mV/°C
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 30V, \ V_{GS} = 0V$	-	-	1	μA
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, \ V_{DS} = 0V$	-	-	±10	μA
Gate threshold voltage	$V_{GS\ (th)}$	$V_{DS} = 10V, I_D = 1mA$	1.0	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	-3.3	-	mV/°C
		V _{GS} =10V, I _D =6A	-	20	28	
Static drain - source	D *5	V _{GS} =4.5V, I _D =6A	-	25	35	
on - state resistance	R _{DS(on)} 5	V _{GS} =4V, I _D =6A	-	28	39	mΩ
		V_{GS} =10V, I _D =6A, T _j =125°C	-	30	42	
Gate input resistannce	R _G	f = 1MHz, open drain	-	0.6	-	Ω
Transconductance	9 _{fs} *5	$V_{DS} = 10V, I_{D} = 6A$	3.0	8.4	-	S

*1 Limited only by maximum temperature allowed.

*2 Pw \leq 10 $\mu s,$ Duty cycle \leq 1%

- *3 Mounted on a ceramic boad (30×30×0.8mm)
- *4 Mounted on a FR4 (20×20×0.8mm)

*5 Pulsed

•Electrical characteristics($T_a = 25^{\circ}C$)

<It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Conditions		Unit		
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	390	-	
Output capacitance	C _{oss}	V _{DS} = 10V	-	150	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	70	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 15V, V_{GS} = 10V$	-	8	-	
Rise time	t _r *5	I _D = 3A	-	40	-	20
Turn - off delay time	t _{d(off)} *5	$R_L = 5\Omega$	-	35	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	7	-	

•Gate Charge characteristics($T_a = 25^{\circ}C$)

<lt is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit		
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit		
Total gata charge	Q_q^{*5}	• ^{*5}	O *5	$V_{DD} \simeq 15V, I_D = 6A$ $V_{GS} = 5V$	-	5.5	13.6	
Total gate charge		$V_{DD} \simeq 15V, I_D = 6A$ $V_{GS} = 10V$	-	10	20	nC		
Gate - Source charge	Q _{gs} ^{*5}	$V_{DD} \simeq 15V, I_D = 6A$	-	1.5	-			
Gate - Drain charge	Q_{gd} *5	$V_{GS} = 5V$	-	2.1	-			

•Body diode electrical characteristics (Source-Drain)($T_a = 25^{\circ}C$)

<It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Symbol		Values		Unit	
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Inverse diode continuous, forward current	ا _S *1	T _a = 25°C	-	-	1.25	A	
Forward voltage	V_{SD} *5	$V_{GS} = 0V, I_s = 6A$	-	-	1.2	V	

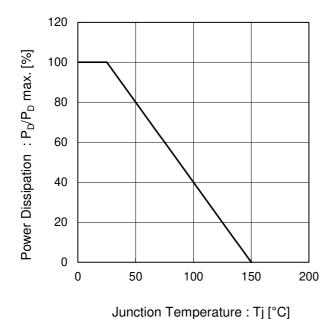
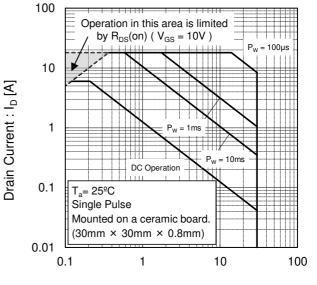
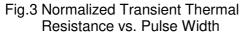


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]





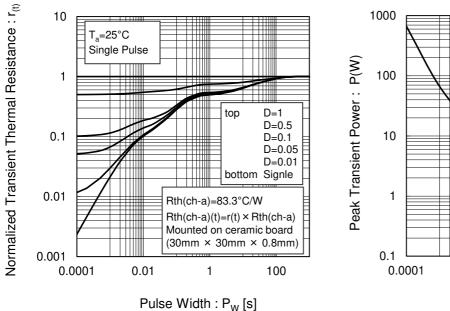
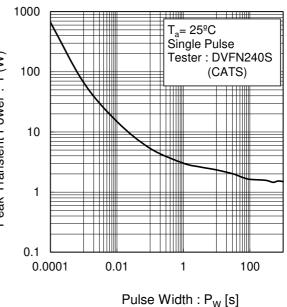


Fig.4 Single Pulse Maxmum Power dissipation



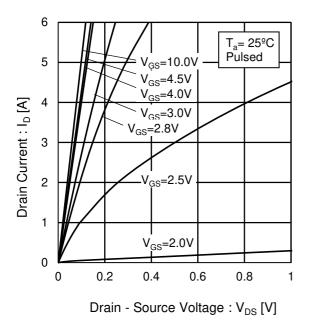
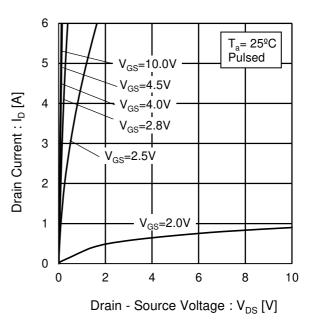


Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)



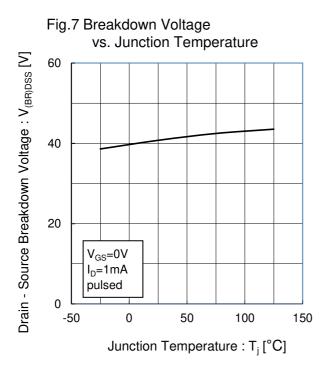


Fig.8 Typical Transfer Characteristics

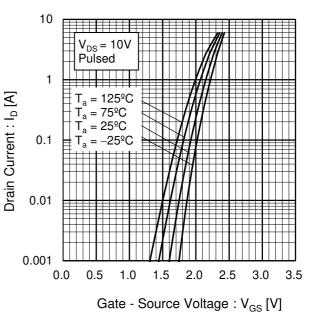
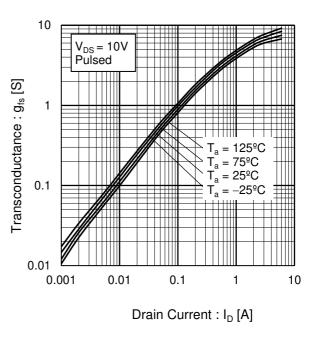
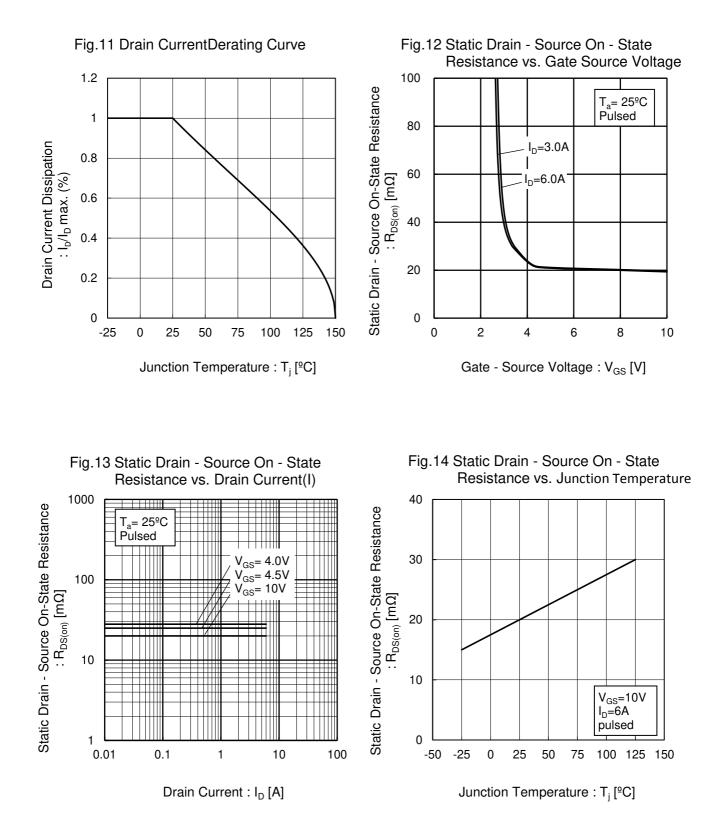


Fig.9 Gate Threshold Voltage vs. Junction Temperature 3 V_{DS}=10V I_D=1mA Gate Threshold Voltage : V_{GS(th)} [V] pulsed 2 1 0 -50 0 50 100 150 Junction Temperature : T_i [°C]

Fig.10 Transconductance vs. Drain Current





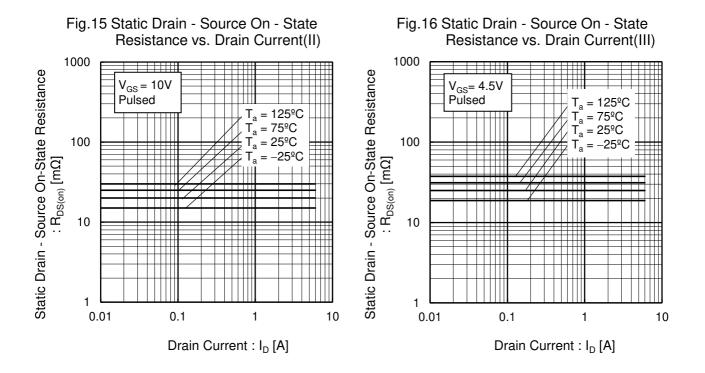
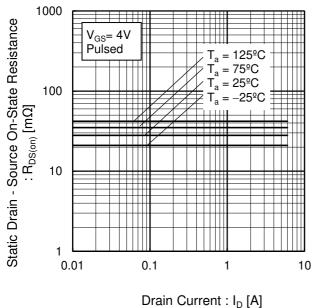
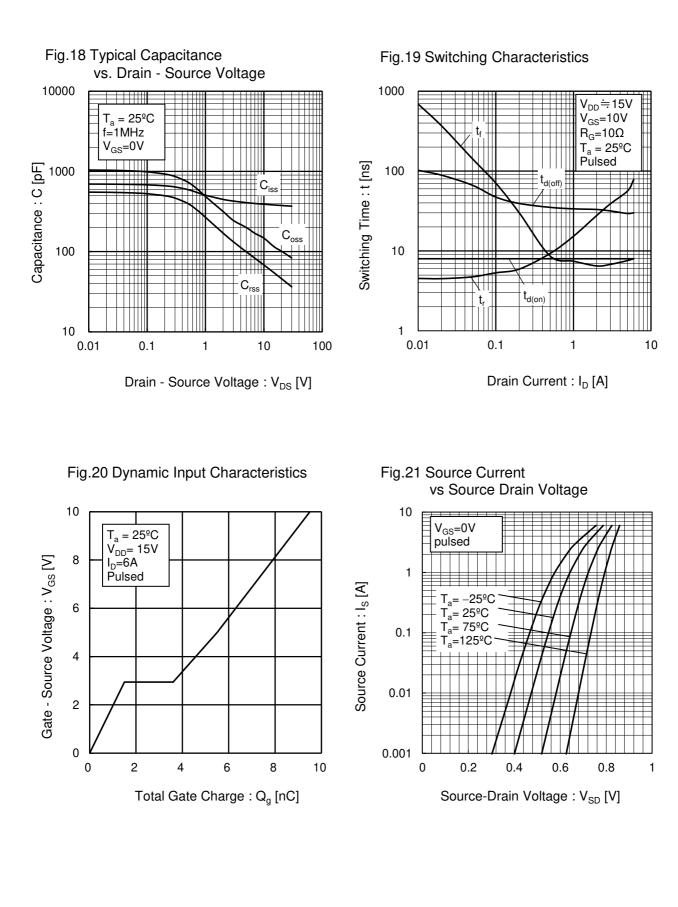
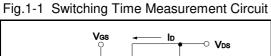


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)





Measurement circuits



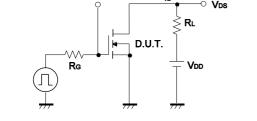


Fig.2-1 Gate Charge Measurement Circuit

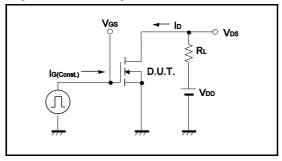
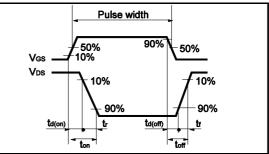
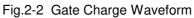
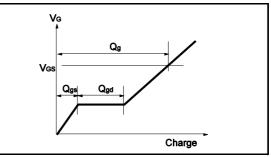


Fig.1-2 Switching Waveforms

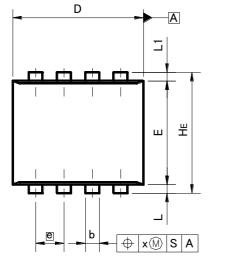


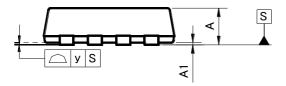


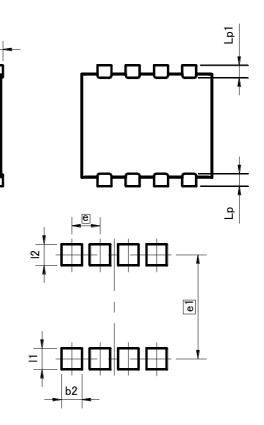


•Dimensions (Unit : mm)

TSMT8







Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.75	0.85	0.03	0.033
A1	0.00	0.05	0	0.002
b	0.27	0.37	0.011	0.015
с	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	2.30	2.50	0.091	0.098
е	0.0	65	0.0	03
HE	2.70	2.90	0.106	0.114
L	0.10	0.30	0.004	0.012
L1	0.10	0.30	0.004	0.012
Lp	0.19	0.39	0.007	0.015
Lp1	0.19	0.39	0.007	0.015
x	_	0.10	_	0.004
У	_	0.10	_	0.004

с

DIM	MILIM	ETERS	INC	HES
DIN	MIN	MAX MIN		MAX
e1	2.4	2.41		10
b3	-	0.47	-	0.019
1	-	0.49	-	0.019
12	-	0.49	-	0.019

Dimension in mm/inches

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