

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

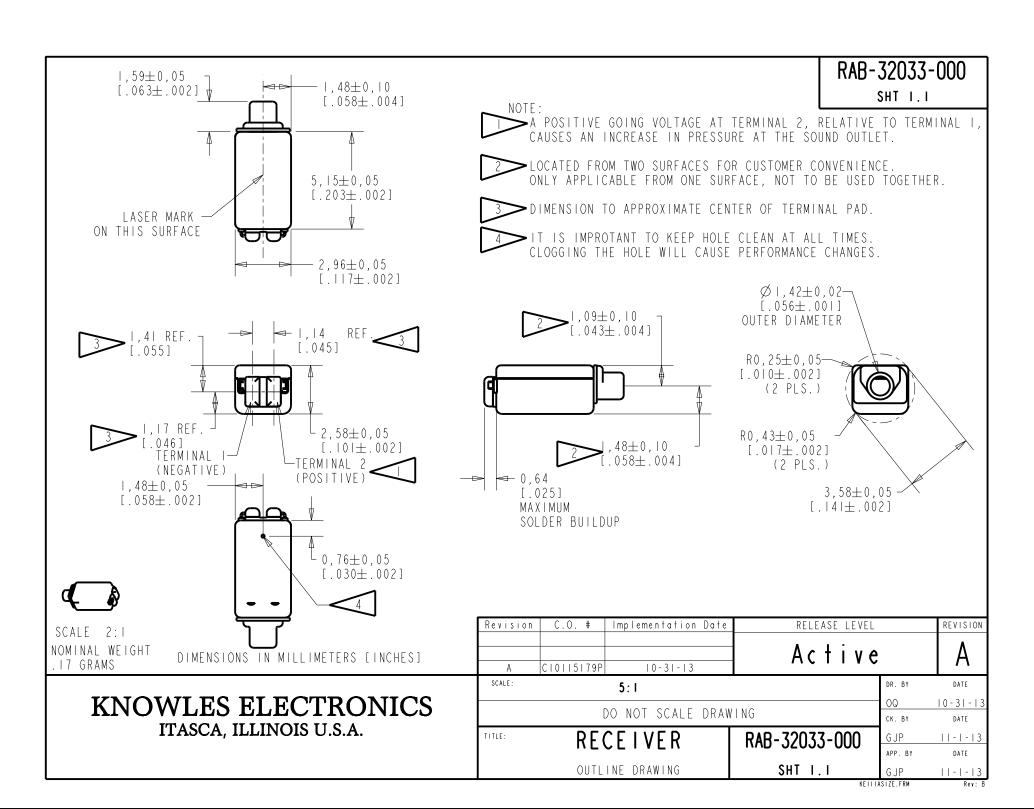
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

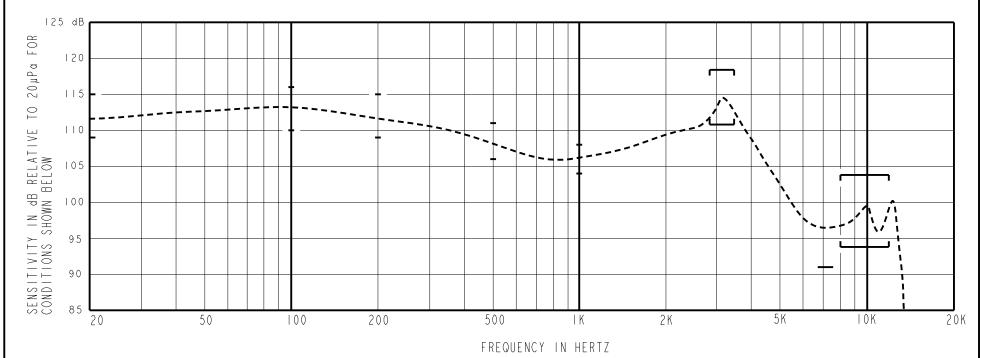








CONSTANT VOLTAGE DRIVE RESPONSE



ACOUSTICAL

SENSITIVITY DEVICE WILL PRODUCE THE SPL LISTED BELOW WITH THE TEST CONDITIONS DESCRIBED IN TABLE 3. NOMINAL SENSITIVITY AT I kHz IS dB RELATIVE TO 20μPα. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT I kHz.

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LIMIT TYPE	FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM		
REL	20	+3.0	+6.0	+9.0		
REL	100	+4.0	+7.0	+ 0 . 0		
REL	200	+3.0	+6.0	+9.0		
REL	500	0	+2.5	+5.0		
REF	1000	-2.0	106.0	+2.0		
PEAK	2850-3450	+5.0	+8.0	+ .0		
VALLEY	6700-7700	-15.0				
PEAK	8000-11800	-13.0	-8.0	- 3 . 0		

TABLE I.

TOTAL HARMONIC DISTORTION DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

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FREQUENCY (Hz)	DRIVE (Vrms)	LIMIT (%)			
50	0.115	6.0			
1/3 PEAK I (TYP. 1050)	0.115	7.0			
1/2 PEAK 1 (TYP. 1575)	0.115	5.0			
1/3 PEAK I (TYP. 1050)	0.324	10.0			
1/2 PEAK I (TYP. 1575)	0.324	10.0			

TABLE 2.

TEST CONDITIONS	5
NOMINAL SOURCE VOLTAGE	0.115 Vrms
SOURCE IMPEDANCE	< Ω
TUBING	TUBELESS, KNOWLES TOOL NUMBER T8688
COUPLER CAVITY	IEC 60318-4 (IEC 711)

TABLE 3.

ELECTRICAL

DC RESISTANCE	22.0 Ω ±10%		
IMPEDANCE @ 500 Hz	33.5 Ω ±15%		
IMPEDANCE @ kHz	50.0 Ω ±15%		
INDUCTANCE @ 500 Hz	7.2 mH TYPICAL		

TABLE 4.

ISOLATION: THE CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT

MECHANICAL

PORT LOCATION: 12C

SOLDER TYPE: ROHS COMPLIANT

SAC305

TEMPERATURE

OPERATING: SENSITIVITY WILL NOT VARY MORE THAN +1/-3 dB FROM 0°C TO 63°C

STORAGE: -40°C TO 63°C

Revision	C.O. #	Implementation Date	RELEASE LEVEL		REVISION
			Active		A
					Д
А	C10115179P	10-31-13			/ \
WHEN TEST I	LIMITS ARE US	SED TO ESTABLISH INCOMING	INSPECTION ACCEPTANCE/REJECTION	DR. BY	DATE

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION PR. BY CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION 10-31-13 CK. BY RECEIVER RAB-32033-000 GJP | | - | - | 3

SHT 2.1

PERFORMANCE SPECIFICATION

DATE

DATE

GJP