

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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RClamp2431T Ultra-Low Capacitance 1-Line ESD protection

PROTECTION PRODUCTS - RailClamp®

Description

RailClamp® TVS diodes are specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from overvoltage caused by ESD (electrostatic discharge), CDE (cable discharge events), and EFT (electrical fast transients).

The RClamp®2431T has a typical capacitance of only 0.35pF. This allows it to be used on Wi-Fi, RFID, and other circuits operating in excess of 3GHz without signal attenuation. It may be used to meet the ESD immunity requirements of IEC 61000-4-2.

The RClamp2431T is in a 2-pin SLP1006P2T package measuring $1.0 \times 0.6 \times 0.4$ mm. The leads are spaced at a pitch of 0.65mm and feature a lead-free finish. Each device will protect one high-speed line operating up to 24 volts. It gives the designer the flexibility to protect single lines in applications where arrays are not practical. The combination of small size, low capacitance, and high ESD surge capability makes it ideal for use in portable equipment such as cellular phones and netbooks.

Features

- Transient protection for data lines to
 - IEC 61000-4-2 (ESD) IEC 61000-4-4 (EFT)
- ◆ Ultra-small package (1.0 x 0.6 x 0.4mm)
- Protects one data line or one I/O pair
- ◆ Low capacitance: 0.35pF (Typical)
- Low clamping voltage
- ◆ Low operating voltage: 24V
- ◆ Solid-state silicon-avalanche technology

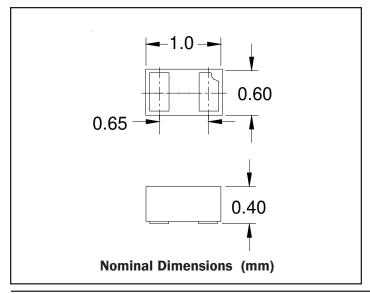
Mechanical Characteristics

- ◆ SLP1006P2T package
- Molding compound flammability rating: UL 94V-0
- Marking: Marking code
- Packaging : Tape and Reel
- Lead Finish: NiPdAu
- ◆ Pb-Free, Halogen Free, RoHS/WEEE Compliant

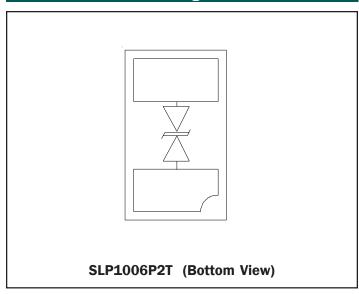
Applications

- Cellular Handsets & Accessories
- Wi-Fi Interfaces
- FeliCa / RFID
- Firewire
- ◆ FM Antenna
- Netbooks
- Serial ATA

Dimensions



Schematic & PIN Configuration





Absolute Maximum Rating

Rating	Symbol	Value	Units	
Peak Pulse Power (tp = 8/20µs)	P _{pk}	100	Watts	
Peak Pulse Current (tp = 8/20µs)	I _{PP}	2	А	
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	+/- 13 +/- 8	kV	
Operating Temperature	T _J	-40 to +85	°C	
Storage Temperature	T _{STG}	-55 to +150	°C	

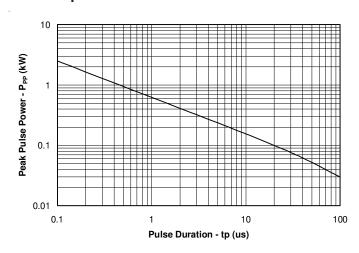
Electrical Characteristics (T=25°C)

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}				24	V
Reverse Breakdown Voltage	V_{BR}	I _t = 1mA	26.7	32	36	V
Reverse Leakage Current	I _R	V _{RWM} = 24V, T=25°C		0.005	0.1	μΑ
Clamping Voltage	V _c	I _{pp} = 1A, tp = 8/20μs			45	V
Clamping Voltage	V _c	$I_{pp} = 2A$, $tp = 8/20\mu s$			50	V
Junction Capacitance	C _j	V _R = OV, f = 1MHz		0.35	0.5	pF

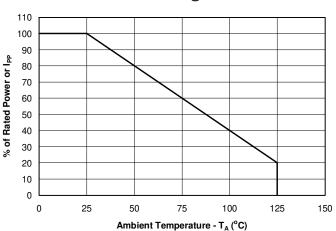


Typical Characteristics

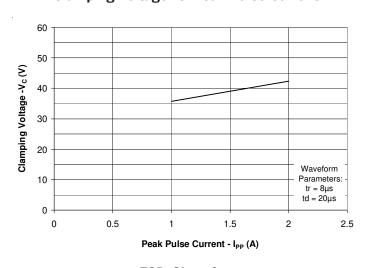
Non-Repetitive Peak Pulse Power vs. Pulse Time



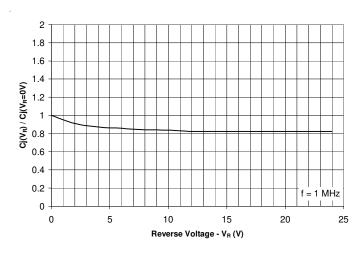
Power Derating Curve



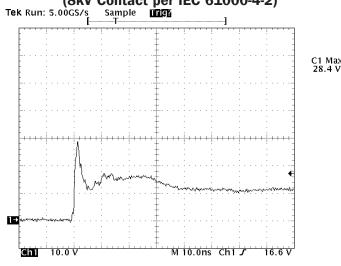
Clamping Voltage vs. Peak Pulse Current



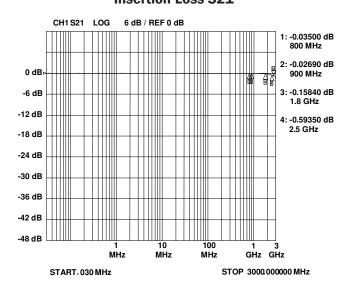
Normalized Capacitance vs. Reverse Voltage



ESD Clamping (8kV Contact per IEC 61000-4-2)



Insertion Loss S21



Note: Data is taken with a 10x attenuator



Applications Information

Device Connection Options

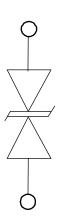
These low capacitance TVS diodes are designed to provide common mode protection for one high-speed line or differential protection for one line pair. The device is bidirectional and may be used on lines where the signal polarity is positive and negative.

Circuit Board Layout Recommendations for Suppression of ESD.

Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

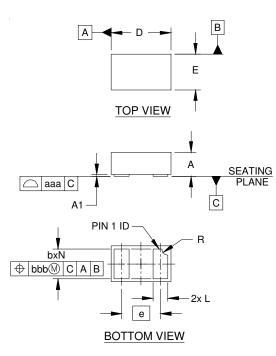
- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

Equivalent Circuit Diagram





Outline Drawing - SLP1006P2T

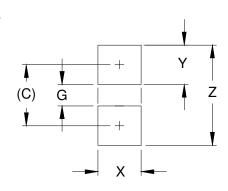


DIMENSIONS								
DIM	11	NCHE	S	MILLIMETERS				
DIM	MIN NOM MAX			MIN	NOM	MAX		
Α	.015	.016	.017	0.37	0.40	0.43		
A1	.000	.001	.002	0.00	0.03	0.05		
b	.018	.020	.022	0.45	0.50	0.55		
D	.035 .039		.043	0.90	1.00	1.10		
E	.020	.024	.028	0.50	0.60	0.70		
е	.0	26 BS	SC	0.65 BSC				
L	.008	.010	.012	0.20	0.25	0.30		
R	.002	.004	.006	0.05	0.10	0.15		
N	2			2				
aaa	.003			0.08				
bbb	.004				0.10			

NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

Land Pattern - SLP1006P2T



DIMENSIONS							
DIM	INCHES	MILLIMETERS					
С	(.033)	(0.85)					
G	.012	0.30					
Χ	.024	0.60					
Υ	.022	0.55					
Ζ	.055	1.40					

NOTES:

- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.



Marking Code



Ordering Information

Part Number	Working	Qty per	Reel	
	Voltage	Reel	Size	
RClamp2431T.TCT	24V	3,000	7 Inch	

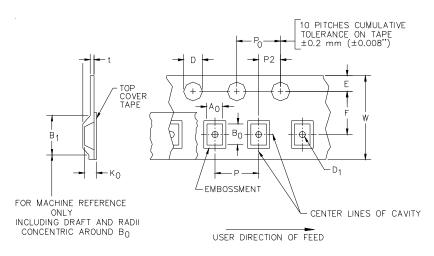
Note: Lead finish is lead-free NiPdAu.

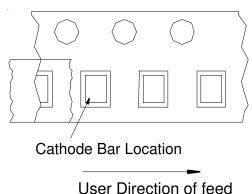
RailClamp and RClamp are marks of Semtech Corporation.

Notes:

- 1) Marking will also include line matrix date code
- 2) Device is electrically symmetrical

Tape and Reel Specification





Device Orientation in Tape

A0	во	КО		
0.69 +/-0.10 mm	1.19 +/-0.10 mm	0.66 +/-0.10 mm		

Tape Width	B, (Max)	D	D1	E	F	K (Max)	Р	PO	P2	T (Max)	W
8 mm	4.2 mm (.165)	1.5 + 0.1 mm - 0.0 mm (0.59 +.005 000)	0.8 mm ±0.25 (.031)	1.750±0.1 mm (.069±.004)	3.5±0.05 mm (.138±.002)	2.4 mm (.094)	4.0±0.10 mm (.157±.004)	4.0±0.1 mm (.157±.004)	2.0±0.05 mm (.079±.002)	0.4mm (.016)	8.0 mm + 0.3 mm - 0.1 mm (.312±.012)

Contact Information

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