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# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







### RE46C105

**Piezoelectric Horn Driver with Voltage Regulator and LED Driver** Product Specification

### **General Description**

The RE46C105 is a piezoelectric horn driver with a voltage regulator and an open drain NMOS driver suitable for use with a light emitting diode. It is intended for 9V battery applications which require a low voltage logic supply. The regulator can be operated at either 3.3V or 5V. The horn feedback control pin is designed for use with self-oscillating piezoelectric horn but can also be used in direct drive applications. A low battery detection circuit is also provided.

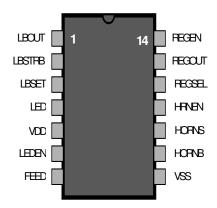
### **Applications**

Smoke detectors CO Detectors Personal Security Products Electronic Toys

### **Features**

- Low Quiescent Current
- Low Horn Driver Ron
- Voltage Regulation to 3.3V or 5V
- Low Battery Detection
- Available in DIP and SOIC packaging
- Available in Standard Packaging or RoHS Compliant Pb Free Packaging

### Pin Configuration



## Absolute maximum ratings

Supply Voltage V <sub>dd</sub>	5V to +14V
Input voltage Range V <sub>in</sub>	3V to V <sub>DD</sub> +.3V, except FEED
FEED Input Voltage Range Vinf	-10V to +22V
Input Current I <sub>in</sub>	10mA, except FEED
Operating Temperature	-40 to 85°C
Continuous Output Current (HornS, HornB)	30mA
Continuous Output Current (REGOUT)	55mA

Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and operation at these conditions for extended periods may affect device reliability.

This product utilizes CMOS technology with static protection; however proper ESD prevention procedures should be used when handling this product. Damage can occur when exposed to extremely high static electrical charges.



## Electrical Characteristics at $T_A$ = 25°C, $V_{DD}$ = 9V, $V_{SS}$ = 0V (unless otherwise noted).

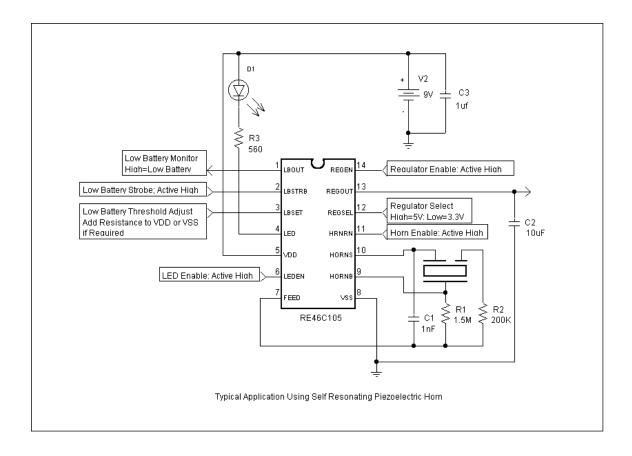
	Test		Limits			
Parameter	Pin	Test Conditions	Min	Тур	Max	Units
Supply Voltage	Vdd	Operating	6.0	9.0	13.8	V
Standby Supply	Vdd	Hrnen=Lbstrb=Leden=Vss			3.5	uA
Current		Regen=Vdd; No Loads				
Input Leakage	Hrnen,Leden, Lbstrb,Regen	Vin=Vdd or Vss	-100		100	nA
	FEED	Feed = +22V		20	50	uA
	FEED	Feed = -10V	-50	-15		uA
Input Voltage Low	Hrnen,Leden, Lbstrb,Regen				1.0	>
Input Voltage High	Hrnen,Leden Lbstrb,Regen		2.3			٧
Output Low Voltage	Horns or Hornb	lout=16mA Vdd=9V		0.3	0.5	V
		Vdd=7.2V			0.9	V
	LED	lout=10mA Vdd=7.2V		0.5	1.0	V
0	LBout	lout=100uA	0.5	0.3	0.5	V
Output High	Horns or Hornb	Iout=-16mA Vdd=9V	8.5	8.7		٧
Voltage		Vdd=7.2V	6.3	4 75		V V
	LBout	lout=-100uA Regsel=Vdd Regsel=Vss	4.5 2.8	4.75 3.0		V
Low Battery VoltageThreshold	Vdd	Lbstrb=Vdd, Vdd decreasing in voltage T <sub>A</sub> =-40 to 85°C See note #3	7.2	0.0	7.80	V
Low Battery Voltage Hysteresis	Lbstrb	Lbstrb=Vdd Vdd increasing in voltage		300		mV
Lbstrb to Lbout Active delay	Lbstrb, Lbout	Lbstrb=Vdd		500		uS
Regulator Voltage	Regout	Iout<50mA Regsel=Vdd Iout<50mA Regsel=Vss T <sub>A</sub> =-40 to 85°C See note #3	4.75 3.10	5.0 3.3	5.25 3.50	V V
Line Regulation	Regout	6V <vdd<12v No load</vdd<12v 		30		mV
Load Regulation	Regout	0mA <lout<50ma< td=""><td></td><td>100</td><td></td><td>mV</td></lout<50ma<>		100		mV
Brown-Out Threshold Voltage *See note #1	Vdd	Regsel=Vdd or Vss Falling edge of Vdd	4.5	5.0	5.5	V
Brown-Out Pull Down Current	Regout	Vdd=4.5V; Regout=2V	15	25		mA
Regout Overvoltage Clamp *See note #2	Regout	Regsel=Vdd; lout > 1mA Regsel=Vss; lout > 1mA	5.5 3.7	6.0 4.0	6.5 4.3	V V



### Notes:

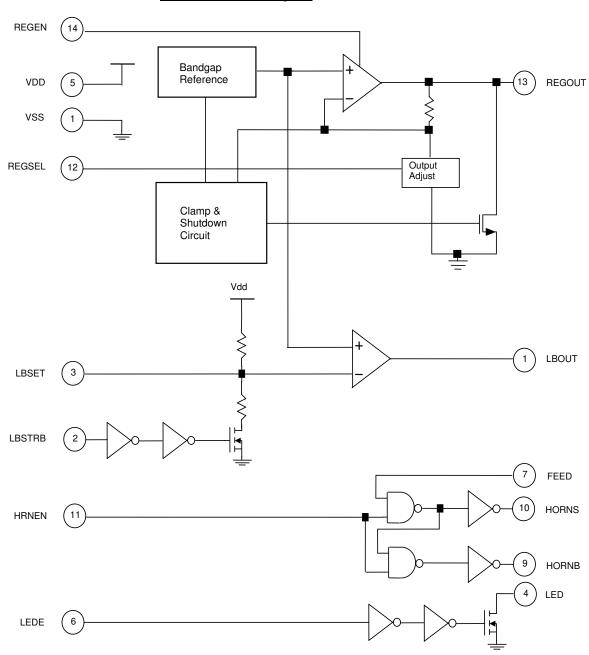
- 1/ The brown-out threshold voltage is the Vdd voltage at which the regulator will be disabled and Regout will be pulled to Vss.
- 2/ In normal operation, the regulator will provide high-side current of up to 20mA, but current sinking capability is typically under 1uA. The over-voltage clamp is intended to limit the voltage at REGOUT when it is pulled up by an external source.
- 3/ The limits shown are 100% tested at 25C only. Test limits are guard-banded based on temperature characterization to guarantee compliance at temperature extremes.

### **Typical Application**





### Functional Block Diagram



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