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RGS00TS65D

650V 50A Field Stop Trench IGBT

V _{CES}	650V
I _{C(100°C)}	50A
V _{CE(sat) (Typ.)}	1.65V
P_D	326W

Features

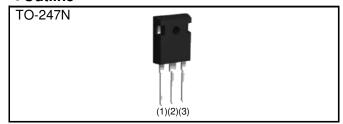
- 1) Low Collector Emitter Saturation Voltage
- 2) Short Circuit Withstand Time 8µs
- 3) Qualified to AEC-Q101
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating; RoHS Compliant

Applications

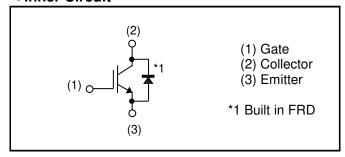
General Inverter

for Automotive and Industrial Use

Outline



●Inner Circuit



Packaging Specifications

	Packaging	Tube
	Reel Size (mm)	-
Tuno	Tape Width (mm)	-
Туре	Basic Ordering Unit (pcs)	450
	Packing Code	C11
	Marking	RGS00TS65D

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
Collector Current	$T_C = 25^{\circ}C$	I _C	88	А
Collector Current	T _C = 100°C	I _C	50	А
Pulsed Collector Current	I _{CP} *1	I _{CP} *1 150		
Diode Forward Current	$T_C = 25^{\circ}C$	I _F	56	Α
	T _C = 100°C	I _F	30	А
Diode Pulsed Forward Current	I _{FP} *1	150	А	
Power Dissipation	$T_C = 25^{\circ}C$	P _D	326	W
	T _C = 100°C	P _D	163	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{imax.}

●Thermal Resistance

Doromotor	Symbol	Values			Unit
Parameter		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.46	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	1.17	°C/W

ullet IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	UTIIL
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_C = 10 \mu A, V_{GE} = 0 V$	650	-	-	V
		$V_{CE} = 650V, V_{GE} = 0V$				
Collector Cut - off Current	I _{CES}	$T_j = 25$ °C $T_i = 175$ °C	-	-	10	μA
		T _j = 175°C	-	1	5	mA
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 2.5mA$	5.0	6.0	7.0	V
		$I_C = 50A, V_{GE} = 15V$				
Collector - Emitter Saturation Voltage	V _{CE(sat)}	T _j = 25°C	-	1.65	2.10	V
		T _j = 175°C	-	2.15	-	

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Lloit		
<u> </u>		Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	$V_{CE} = 30V$	-	1568	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	134	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	23	-	
Total Gate Charge	Q_g	V _{CE} = 300V	-	58	-	
Gate - Emitter Charge	Q_{ge}	I _C = 50A	-	15	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	24	-	
Turn - on Delay Time	$t_{d(on)}$	$I_C = 50A, V_{CC} = 400V$	-	36	-	
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	21	-	
Turn - off Delay Time	$t_{d(off)}$	T _j = 25°C	-	115	-	ns
Fall Time	t _f	Inductive Load	-	91	-	
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	1.46	-	m l
Turn - off Switching Loss	E _{off}	reverse recovery	-	1.29	-	mJ
Turn - on Delay Time	t _{d(on)}	$I_C = 50A, V_{CC} = 400V$	-	37	-	
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	33	-	
Turn - off Delay Time	$t_{d(off)}$	T _j = 175°C	-	145	-	ns
Fall Time	t _f	Inductive Load	-	147	-	
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	1.97	-	m l
Turn - off Switching Loss	E _{off}	reverse recovery	-	1.85	-	mJ
		$I_C = 150A, V_{CC} = 520V$				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650 V, V_{GE} = 15 V$	FULL SQUARE			_
		$R_G = 50\Omega, T_j = 175^{\circ}C$				
	1	$V_{CC} \le 360V$				
Short Circuit Withstand Time	t _{sc}	$V_{GE} = 15V, T_j = 25^{\circ}C$	8	-	1	μs
Short Circuit Withstand Time	t _{sc} *2	$V_{CC} \le 360V$	6	-	-	μs
	ι _{sc}	$V_{GE} = 15V, T_j = 150$ °C	0			

^{*2} Design assurance without measurement

•FRD Electrical Characteristics (at $T_j = 25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Offic
		I _F = 30A				
Diode Forward Voltage	V_{F}	$T_j = 25^{\circ}C$	-	1.45	1.90	V
		T _j = 175°C	-	1.55	-	
Diode Reverse Recovery Time	t _{rr}	$I_F = 30A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	103	-	ns
Diode Peak Reverse Recovery Current	l _{rr}		-	7.1	-	А
Diode Reverse Recovery Charge	Q_{rr}		-	0.4	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	15	-	μJ
Diode Reverse Recovery Time	t _{rr}	$I_F = 30A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 175^{\circ}C$	-	242	-	ns
Diode Peak Reverse Recovery Current	I _{rr}		-	9.8	-	Α
Diode Reverse Recovery Charge	Q_{rr}		-	1.3	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	113	-	μJ

Fig.1 Power Dissipation vs. Case Temperature

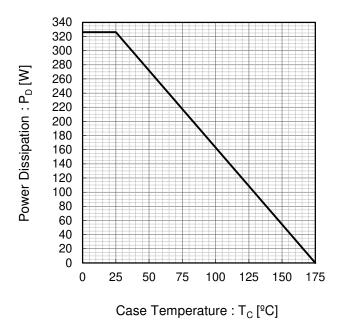


Fig.2 Collector Current vs. Case Temperature

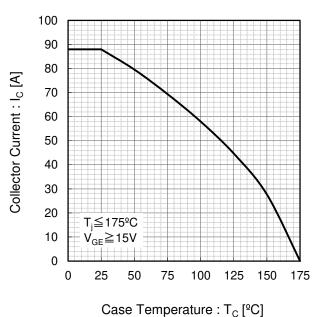


Fig.3 Forward Bias Safe Operating Area

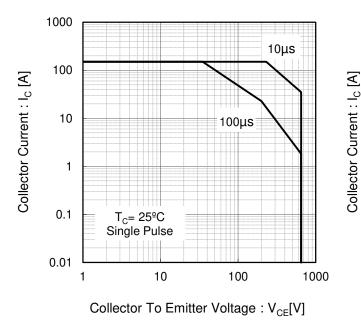
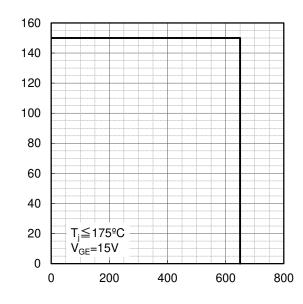


Fig.4 Reverse Bias Safe Operating Area



Collector To Emitter Voltage : V_{CE}[V]

Fig.5 Typical Output Characteristics

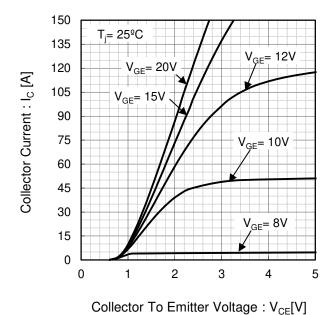
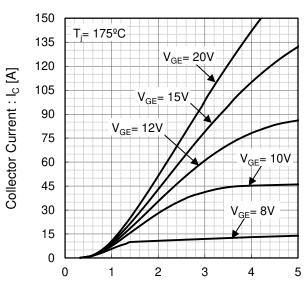


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage: V_{CE}[V]

Fig.7 Typical Transfer Characteristics

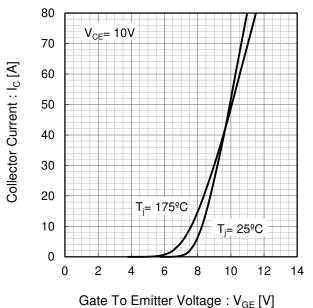


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

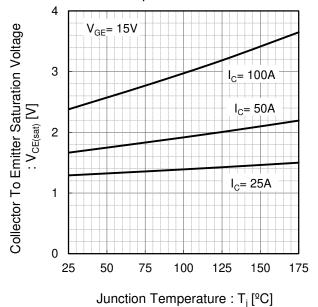
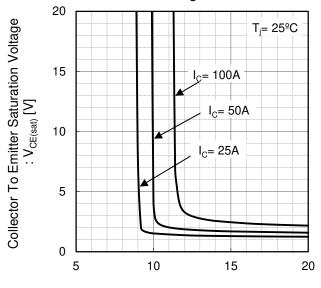
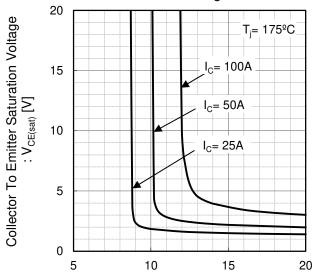


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



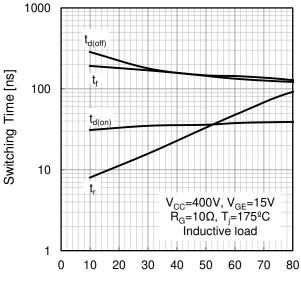
Gate To Emitter Voltage : V_{GE} [V]

Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



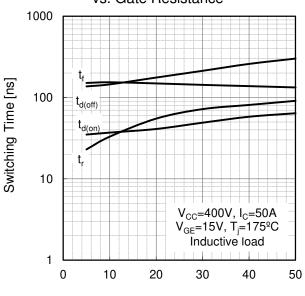
Gate To Emitter Voltage: V_{GE} [V]

Fig.11 Typical Switching Time vs. Collector Current



Collector Current : I_C [A]

Fig.12 Typical Switching Time vs. Gate Resistance



Gate Resistance : $R_G[\Omega]$

Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] Eoff 1 E_{on} 0.1 V_{CC} =400V, V_{GE} =15V R_{G} =10 Ω , T_{j} =175 $^{\circ}$ C Inductive load 0.01 50 0 20 30 60 70 80 10 40 Collector Current : I_C [A]

vs. Gate Resistance 10 E_{on} Switching Energy Losses [mJ] E_{off} 1 0.1 V_{CC} =400V, I_{C} =50A V_{GE} =15V, T_{j} =175 $^{\circ}$ C Inductive load 0.01 0 10 20 30 40 50 Gate Resistance : $R_G[\Omega]$

Fig.14 Typical Switching Energy Losses

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz $V_{GE}=0V$ T_i=25°C 0.01 0.1 1 10 100 Collector To Emitter Voltage: V_{CE}[V]

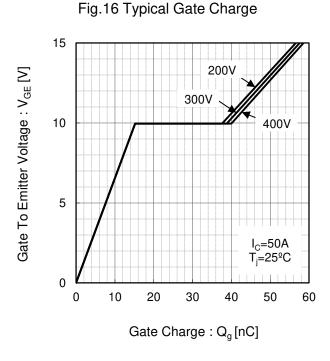


Fig.17 Typical Diode Forward Current vs. Forward Voltage 150 120 Forward Current : I_F [A] 90 60 30 T_i= 175ºC T_i= 25°C 0 0.5 1.5 2 2.5 3 3.5 Forward Voltage : V_F[V]

Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current 400 Reverse Recovery Time: t_{rr} [ns] 300 T_i= 175°C 200 T_i= 25ºC 100 V_{CC} =400V di_F/dt=200A/µs Inductive load 0 40 20 30 50 60 10 Forward Current : I_F [A]

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

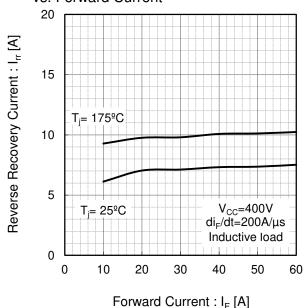


Fig.20 Typical Diode Reverse Recovery Energy Losses vs. Forward Current

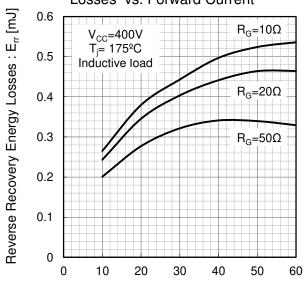
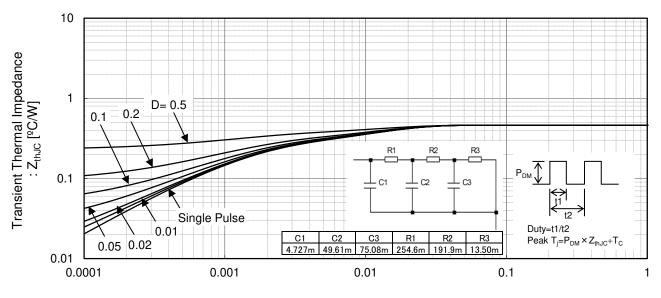
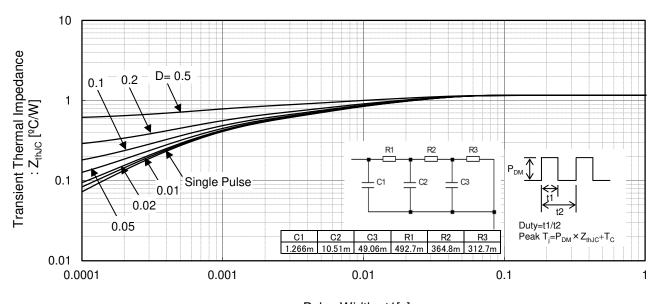


Fig.21 IGBT Transient Thermal Impedance



Pulse Width: t1[s]

Fig.22 Diode Transient Thermal Impedance



Pulse Width: t1[s]

●Inductive Load Switching Circuit and Waveform

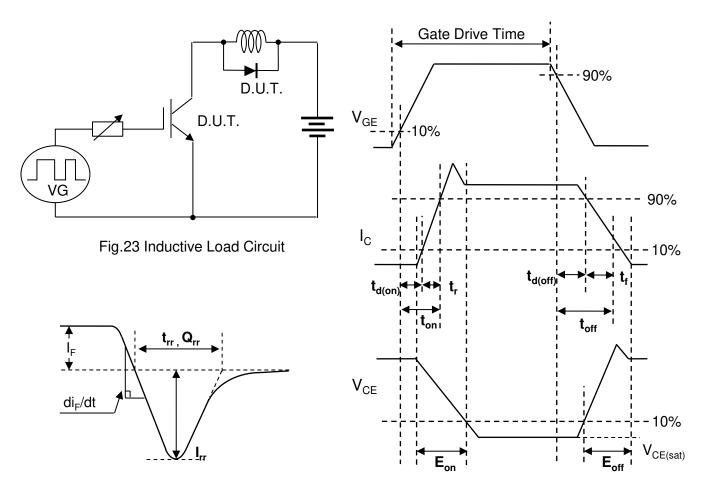


Fig.25 Diode Reverce Recovery Waveform

Fig.24 Inductive Load Waveform

Notes

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