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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









# RGTH40TS65D

### 650V 20A Field Stop Trench IGBT

$V_{CES}$	650V
I <sub>C(100°C)</sub>	20A
V <sub>CE(sat) (Typ.)</sub>	1.6V
$P_D$	144W

#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating; RoHS Compliant

#### Applications

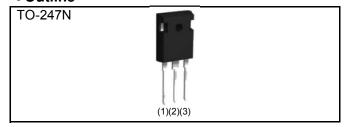
**PFC** 

**UPS** 

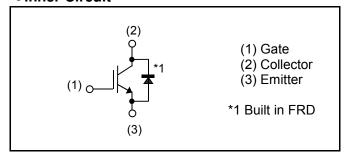
**Power Conditioner** 

ΙH

#### Outline



#### ●Inner Circuit



Packaging Specifications

	Packaging	Tube
	Reel Size (mm)	-
Typo	Tape Width (mm)	-
Туре	Basic Ordering Unit (pcs)	450
	Packing code	C11
	Marking	RGTH40TS65D

### ● Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CES</sub>	650	V
Gate - Emitter Voltage		$V_{GES}$	±30	V
Collector Current	T <sub>C</sub> = 25°C	I <sub>C</sub>	40	А
Collector Current	T <sub>C</sub> = 100°C	I <sub>C</sub>	20	А
Pulsed Collector Current		I <sub>CP</sub> *1	80	А
Diode Forward Current	T <sub>C</sub> = 25°C	I <sub>F</sub>	35	А
Diode Forward Current	T <sub>C</sub> = 100°C	I <sub>F</sub>	20	А
Diode Pulsed Forward Current		I <sub>FP</sub> *1	80	А
$T_C = 25^{\circ}C$		P <sub>D</sub>	144	W
Power Dissipation	T <sub>C</sub> = 100°C	P <sub>D</sub>	72	W
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C

<sup>\*1</sup> Pulse width limited by T<sub>jmax.</sub>

#### ●Thermal Resistance

Parameter	Symbol	Values			Unit
- Faranielei	Зупівої	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	ı	1.04	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	1	2.28	°C/W

## ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit	
r ai ai iletei	Syllibol	Conditions	Min.	Тур.	Max.	Offic	
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	-	-	V	
Collector Cut - off Current	I <sub>CES</sub>	V <sub>CE</sub> = 650V, V <sub>GE</sub> = 0V	1	-	10	μΑ	
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, V_{CE} = 0V$	1	-	±200	nA	
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 13.3 \text{mA}$	4.5	5.5	6.5	V	
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_C = 20A$ , $V_{GE} = 15V$ $T_j = 25$ °C $T_j = 175$ °C		1.6 2.1	2.1	٧	

## ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions -		Unit		
Parameter	Symbol		Min.	Тур.	Max.	Offic
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V	-	1060	-	
Output Capacitance	C <sub>oes</sub>	V <sub>GE</sub> = 0V	-	47	-	pF
Reverse Transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	18	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 300V	-	40	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 20A	-	9	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	15	-	
Turn - on Delay Time	t <sub>d(on)</sub>	I <sub>C</sub> = 20A, V <sub>CC</sub> = 400V	-	22	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_G = 10\Omega$	-	25	-	
Turn - off Delay Time	$t_{d(off)}$	T <sub>j</sub> = 25°C	-	73	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	48	-	
Turn - on Delay Time	t <sub>d(on)</sub>	I <sub>C</sub> = 20A, V <sub>CC</sub> = 400V	-	22	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_{G} = 10\Omega$	-	25	-	no
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 175°C	-	83	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	58	-	
		I <sub>C</sub> = 80A, V <sub>CC</sub> = 520V				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650V, V_{GE} = 15V$	FU	LL SQUA	RE	-
		$R_G = 60\Omega, T_j = 175^{\circ}C$				

## ●FRD Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Darameter	Cymbol	Conditions	Values			Unit
Parameter	Symbol		Min.	Тур.	Max.	Unit
Diode Forward Voltage	V <sub>F</sub>	$I_F = 20A$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	-	1.45 1.25	1.9 -	V
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A	-	58	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>	$V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	6.3	-	А
Diode Reverse Recovery Charge	$Q_{rr}$		-	0.20	-	μC
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 20A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 175^{\circ}C$	-	256	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	10.4	-	Α
Diode Reverse Recovery Charge	$Q_{rr}$		-	1.35	-	μC

Fig.1 Power Dissipation vs. Case Temperature

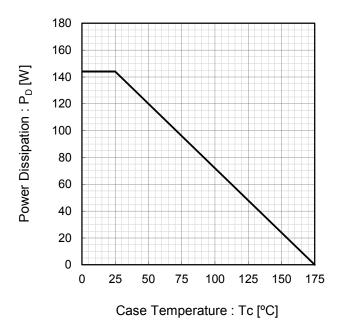


Fig.2 Collector Current vs. Case Temperature

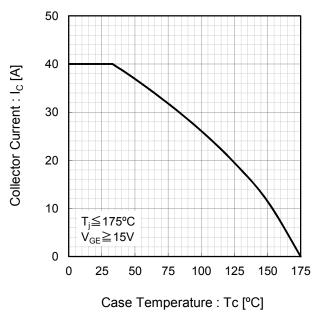
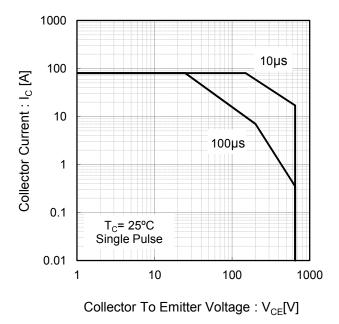


Fig.3 Forward Bias Safe Operating Area



Collector Current : I<sub>C</sub> [A]

120

Fig.4 Reverse Bias Safe Operating Area

100 80 60 40 20 T<sub>j</sub>≤175°C V<sub>GE</sub>=15V 0 200 400 600 800

Collector To Emitter Voltage :  $V_{CE}[V]$ 

Fig.5 Typical Output Characteristics

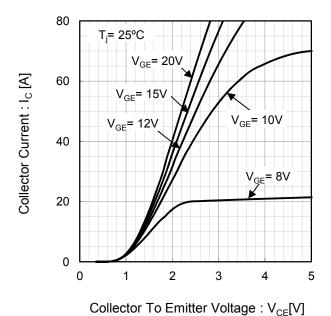
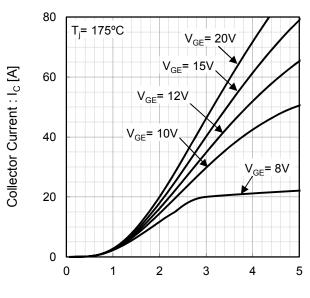


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage :  $V_{CE}[V]$ 

Fig.7 Typical Transfer Characteristics

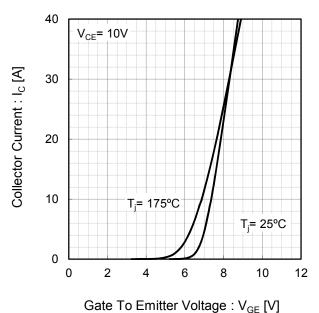


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

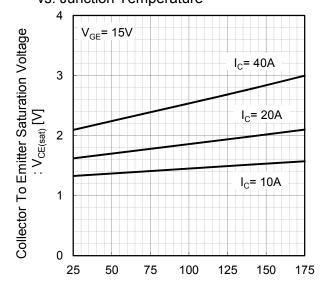


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

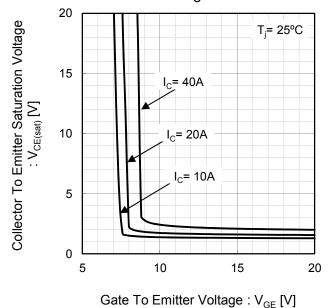
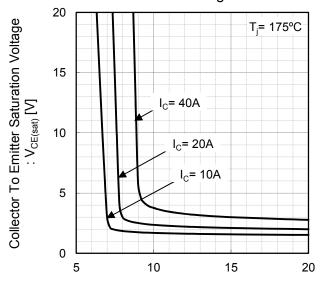


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate To Emitter Voltage : V<sub>GE</sub> [V]

Fig.11 Typical Switching Time vs. Collector Current

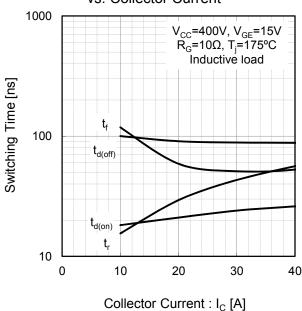
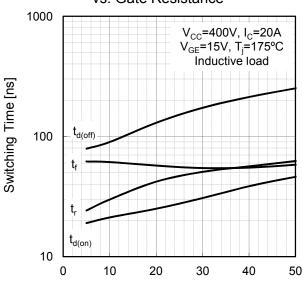


Fig.12 Typical Switching Time vs. Gate Resistance



Gate Resistance :  $R_{G}\left[\Omega\right]$ 

Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1  $\mathsf{E}_{\mathsf{off}}$ E<sub>on</sub> 0.1  $V_{CC}$ =400V,  $V_{GE}$ =15V  $R_{G}$ =10 $\Omega$ ,  $T_{j}$ =175°C Inductive load 0.01 0 10 20 30 40 Collector Current : I<sub>C</sub> [A]

vs. Gate Resistance

10

1  $E_{off}$ 0.1  $V_{cc}=400V, I_{c}=20A$   $V_{GE}=15V, T_{j}=175^{\circ}C$ Inductive load

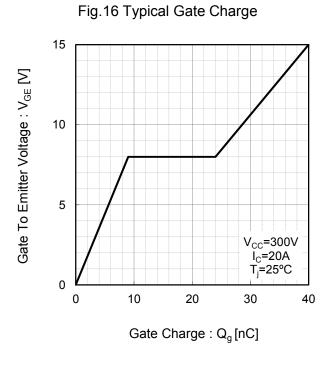
0 10 20 30 40 50

Gate Resistance :  $R_{G}[\Omega]$ 

Switching Energy Losses [mJ]

Fig.14 Typical Switching Energy Losses

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] 100 Coes Cres 10 f=1MHz V<sub>GE</sub>=0V =25°C 0.01 0.1 1 10 100 Collector To Emitter Voltage : V<sub>CE</sub>[V]



vs. Forward Voltage

80

Vs. Forward Voltage

80

T<sub>j</sub>= 175°C

T<sub>j</sub>= 25°C

1.5

Forward Voltage : V<sub>F</sub>[V]

2

2.5

3

Fig.17 Typical Diode Forward Current

Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current 400  $V_{CC}$ =400V di<sub>F</sub>/dt=200A/µs Reverse Recovery Time : t<sub>rr</sub> [ns] Inductive load 300 T<sub>i</sub>= 175°C 200 100 T<sub>i</sub>= 25°C 0 10 20 30 40 50

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

0.5

0

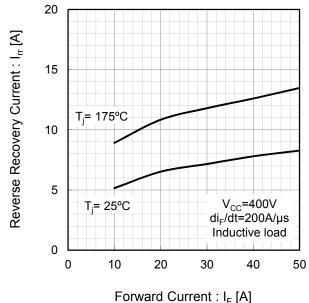


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current

Forward Current : I<sub>F</sub> [A]

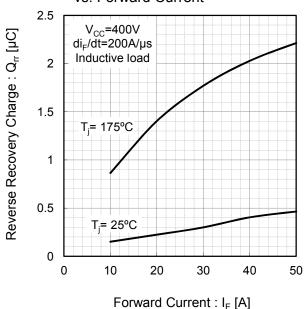


Fig.21 IGBT Transient Thermal Impedance

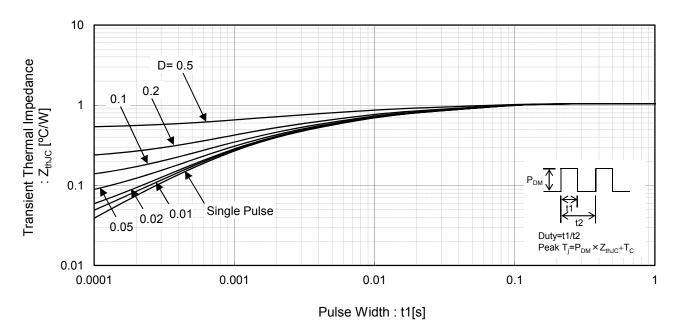
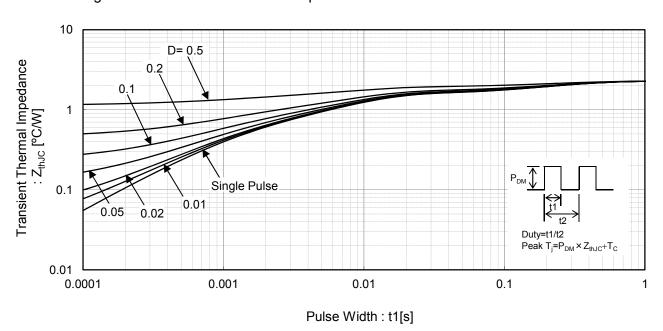


Fig.22 Diode Transient Thermal Impedance



### ●Inductive Load Switching Circuit and Waveform

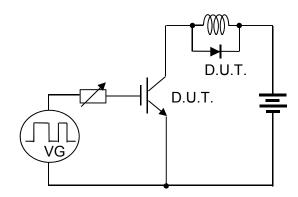


Fig.23 Inductive Load Circuit

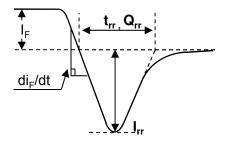


Fig.25 Diode Reverce Recovery Waveform

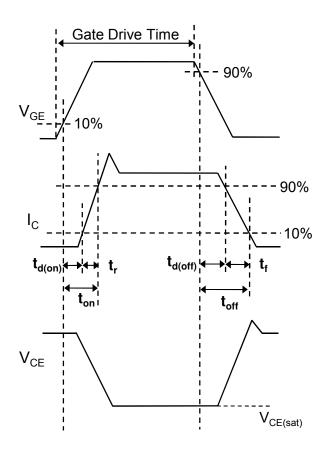


Fig.24 Inductive Load Waveform

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# RGTH40TS65D - Web Page

**Distribution Inventory** 

Part Number	RGTH40TS65D
Package	TO-247N
Unit Quantity	450
Minimum Package Quantity	450
Packing Type	Bulk
Constitution Materials List	inquiry
RoHS	Yes