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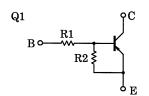
TOSHIBA Transistor Silicon PNP/NPN Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

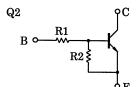
# **RN4901**

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Including two devices in US6 (ultra super mini type with 6 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

#### **Equivalent Circuit and Bias Resister Values**





R1: 4.7kΩ R2: 4.7kΩ (Q1, Q2 Common)

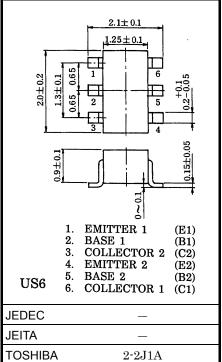
### Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-50	V
Collector-emitter voltage	V <sub>CEO</sub>	-50	V
Emitter-base voltage	V <sub>EBO</sub>	-10	V
Collector current	IC	-100	mA

# Q2 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	50	V
Collector-emitter voltage	V <sub>CEO</sub>	50	V
Emitter-base voltage	V <sub>EBO</sub>	10	V
Collector current	Ic	100	mA

# Unit: mm



Weight: 6.8mg (typ.)

### Q1, Q2 Common Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector power dissipation	P <sub>C</sub> *	200	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

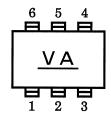
Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

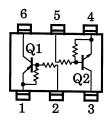
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

\* Total rating

#### Marking



### **Equivalent Circuit (Top View)**



## Q1 Electrical Characteristics (Ta = 25°C)

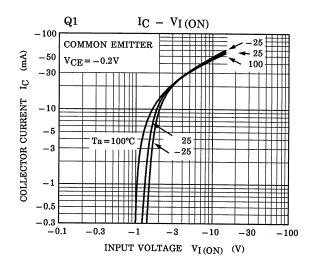
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	_	$V_{CB} = -50V$ , $I_E = 0$	_	_	-100	nA
	I <sub>CEO</sub>	_	$V_{CE} = -50V, I_B = 0$	_	_	-500	
Emitter cut-off current	I <sub>EBO</sub>	_	$V_{EB} = -10V, I_C = 0$	-0.82	_	-1.52	mA
DC current gain	h <sub>FE</sub>	_	$V_{CE} = -5V, I_{C} = -10mA$	30	_	_	_
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	_	$I_C = -5mA$ , $I_B = -0.25mA$	_	-0.1	-0.3	V
Input voltage (ON)	V <sub>I (ON)</sub>	_	$V_{CE} = -0.2V$ , $I_{C} = -5mA$	-1.1	_	-2.0	V
Input voltage (OFF)	V <sub>I (OFF)</sub>	_	$V_{CE} = -5V, I_{C} = -0.1mA$	-1.0	_	-1.5	V
Transition frequency	f <sub>T</sub>	_	V <sub>CE</sub> = -10V, I <sub>C</sub> = -5mA	_	200	_	MHz
Collector output capacitance	C <sub>ob</sub>	_	V <sub>CB</sub> = −10V, I <sub>E</sub> = 0, f = 1MHz	_	3	6	pF

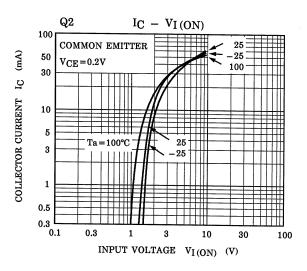
## Q2 Electrical Characteristics (Ta = 25°C)

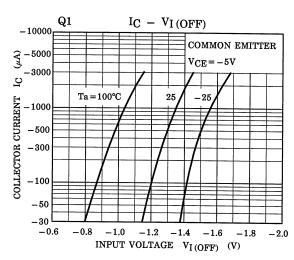
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	_	V <sub>CB</sub> = 50V, I <sub>E</sub> = 0	_	_	100	nA
	ICEO	_	$V_{CE} = 50V, I_B = 0$	_	_	500	
Emitter cut-off current	I <sub>EBO</sub>	_	V <sub>EB</sub> = 10V, I <sub>C</sub> = 0	0.82	_	1.52	mA
DC current gain	h <sub>FE</sub>	_	V <sub>CE</sub> = 5V, I <sub>C</sub> = 10mA	30	_	_	_
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	_	I <sub>C</sub> = 5mA, I <sub>B</sub> = 0.25mA	_	0.1	0.3	V
Input voltage (ON)	V <sub>I (ON)</sub>	_	V <sub>CE</sub> = 0.2V, I <sub>C</sub> = 5mA	1.1	_	2.0	V
Input voltage (OFF)	V <sub>I (OFF)</sub>	_	$V_{CE} = 5V, I_{C} = 0.1mA$	1.0	_	1.5	V
Transition frequency	f <sub>T</sub>	_	V <sub>CE</sub> = 10V, I <sub>C</sub> = 5mA	_	250	_	MHz
Collector output capacitance	C <sub>ob</sub>	_	$V_{CB} = 10V, I_E = 0, f = 1 MHz$	_	3	6	pF

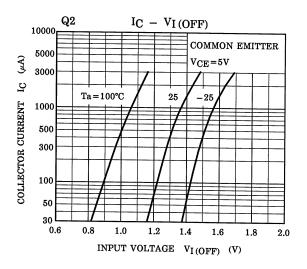
### Q1, Q2 Common Electrical Characteristics (Ta = 25°C)

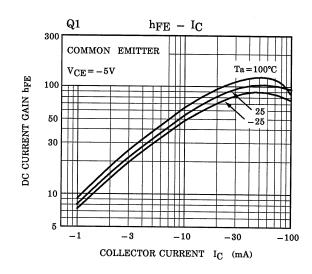
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1	_	_	3.29	4.7	6.11	kΩ
Resistor ratio	R1/R2	_	_	0.9	1.0	1.1	_

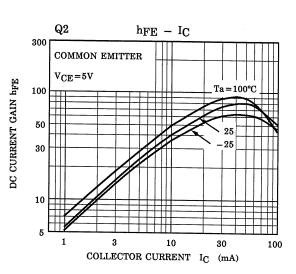












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