imall

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DC-DC Converter (-20V, -4.0A) RTQ040P02

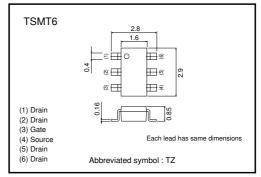
Features

- 1) Low on-resistance. (110m Ω at 2.5V)
- 2) High power package.
- 3) High speed switching.
- 4) Low voltage drive. (2.5V)

Applications

DC-DC converter

•External dimensions (Unit : mm)



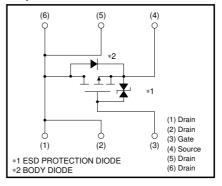
Structure

Silicon P-channel MOS FET

Packaging specifications

Туре	Package	Taping	
	Code	TR	
	Basic ordering unit (pieces)	3000	
RTQ040P02		0	

Equivalent circuit



Transistors

•Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit			
Drain-source voltage		VDSS	-20	V			
Gate-source voltage		Vgss	±12	V			
Drain current	Continuous	ID	±4.0	А			
	Pulsed	IDP	±16	A *1			
Source current (Body diode)	Continuous	ls	-1	A *1			
	Pulsed	Isp	-16	А			
Total power dissipation		PD	1.25	W *2			
Channel temperature		Tch	150	٥C			
Range of Storage temperature		Tstg	-55 to +150	°C			

*1 Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Gate-source leakage	Igss	-	-	±10	μA	$V_{GS}=\pm 12V, V_{DS}=0V$		
Drain-source breakdown voltage	V(BR) DSS	-20	-	_	V	$I_D = -1mA$, $V_{GS} = 0V$		
Zero gate voltage drain current	IDSS	-	-	-1	μA	VDS=-20V, VGS=0V		
Gate threshold voltage	VGS (th)	-0.7	-	-2.0	V	$V_{DS} = -10V$, $I_{D} = -1mA$		
Static drain-source on-state resistance	R _{DS} (on)	-	35	50	mΩ	$I_{D}=-4A, V_{GS}=-4.5V$ *		
		-	40	55	mΩ	$I_D = -4A$, $V_{GS} = -4V$ *		
		-	60	85	mΩ	$I_D = -2.0A, V_{GS} = -2.5V$ *		
Forward transfer admittance	Y _{fs}	3.5	-	-	S	V_{DS} = -10V, I _D = -2.0A *		
Input capacitance	Ciss	-	1350	_	pF	$V_{DS} = -10V$		
Output capacitance	Coss	-	210	_	pF	V _{GS} =0V		
Reverse transfer capacitance	Crss	-	150	-	pF	f=1MHz		
Turn-on delay time	td (on)	-	15	-	ns	ID= -2.0A *		
Rise time	tr	-	35	_	ns	VDD≒ –15V * VGs= –4.5V		
Turn-off delay time	td (off)	-	60	_	ns	$V_{GS} = -4.5 V$ *		
Fall time	tr	-	30	_	ns	Rgs=10 Ω *		
Total gate charge	Qg	-	12.2	_	nC	V _{DD} ≒−15V RL≒3.75Ω		
Gate-source charge	Qgs	-	2.6	_	nC	$V_{GS}=-4.5V$ R _{GS} =10 Ω		
Gate-drain charge	Q _{gd}	-	3.4	-	nC	I _D =-4.0A		
*Pulsed								
Body diode characteristics (source-drain characteristics)								
Forward voltage	VSD	_	-	-1.2	V	$I_{S}=-1A$, $V_{GS}=0V$		

RTQ040P02

Transistors

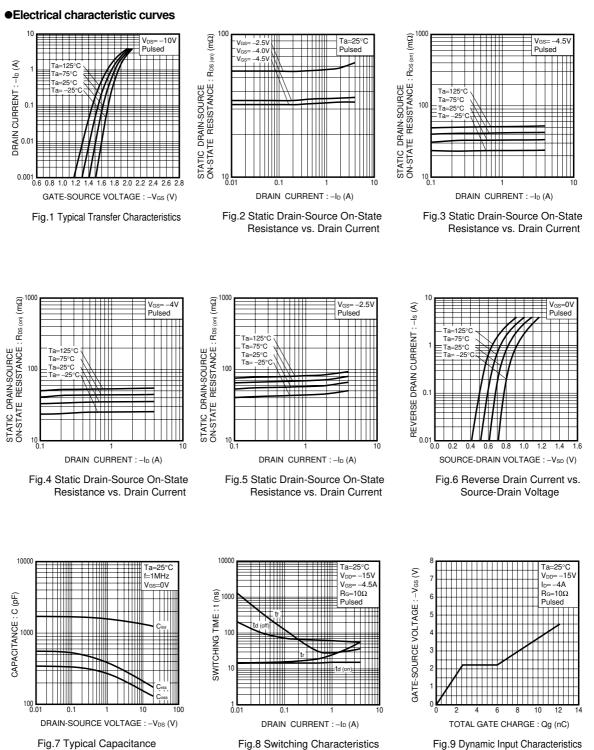


Fig.7 Typical Capacitance vs. Drain-Source Voltage

ROHM

Transistors

Measurement circuits

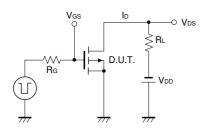


Fig.10 Switching Time Measurement Circuit

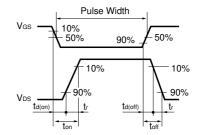


Fig.11 Switching Waveforms

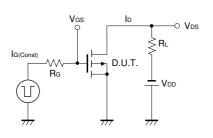


Fig.12 Gate Charge Measurement Circuit

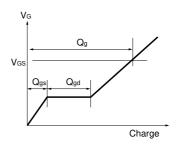


Fig.13 Gate Charge Waveforms

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