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## S-1135 Series

## HIGH RIPPLE-REJECTION LOW DROPOUT MIDDLE OUTPUT CURRENT CMOS VOLTAGE REGULATOR

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SII O

Rev.2.2\_01

The S-1135 Series, developed using the CMOS technology, is a positive voltage regulator IC which has the low dropout voltage, the high-accuracy output voltage and the low current consumption (300 mA output current).

A 1.0  $\mu$ F small ceramic capacitor can be used. It operates with low current consumption of 45  $\mu$ A typ.

The overcurrent protection circuit prevents the load current from exceeding the current capacity of the output transistor. The ON / OFF circuit ensures longer battery life.

Various capacitors, also small ceramic capacitors, can be used for this IC more than for the conventional regulator ICs which have CMOS technology.

Furthermore, a small SOT-89-5, SOT-23-5, and HSNT-6A packages realize high-density mounting.

### Features

Output voltage:	1.0 V to 3.5 V, selectable in 0.05 V step		
<ul> <li>Input voltage:</li> </ul>	1.5 V to 5.5 V		
<ul> <li>Output voltage accuracy:</li> </ul>	$\pm 1.0\%$ (1.0 V to 1.45 V output product : $\pm 15$ mV)		
<ul> <li>Dropout voltage:</li> </ul>	160 mV typ. (2.6 V output product, Iout = 300 mA)		
<ul> <li>Current consumption:</li> </ul>	During operation: 45 $\mu$ A typ., 65 $\mu$ A max.		
	During power-off: 0.1 μA typ., 1.0 μA max.		
Output current:	Possible to output 300 mA $(V_{IN} \ge V_{OUT(S)} + 1.0 \text{ V})^{*1}$		
<ul> <li>Input and output capacitors :</li> </ul>	A ceramic capacitor of 1.0 $\mu$ F or more can be used.		
Ripple rejection:	70 dB typ. (1.0 V output product, f = 1.0 kHz)		
<ul> <li>Built-in overcurrent protection circuit:</li> </ul>	Limits overcurrent of output transistor.		
<ul> <li>Built-in ON / OFF circuit:</li> </ul>	Ensures long battery life.		
<ul> <li>Discharge shunt function is selectable.</li> </ul>			

• Pull-up or pull-down resistor is selectable.

• Operation temperature range:

• Lead-free, Sn 100%, halogen-free\*2

Ta =  $-40^{\circ}$ C to  $+85^{\circ}$ C

\*1. Attention should be paid to the power dissipation of the package when the output current is large.

\*2. Refer to "■ Product Name Structure" for details.

## Applications

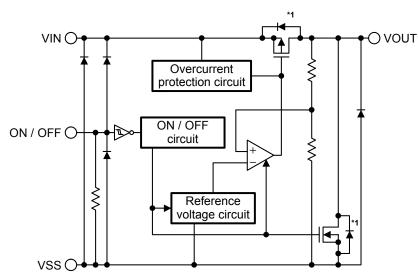
- Power supply for battery-powered device
- Power supply for cellular phone
- Power supply for portable equipment

## Packages

- SOT-89-5
- SOT-23-5
- HSNT-6A

## Block Diagrams

1. S-1135 Series A type

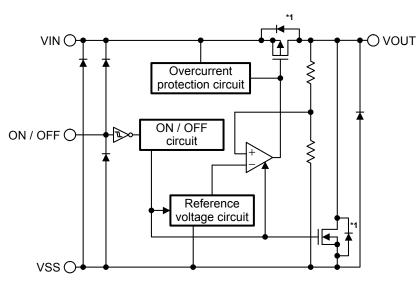


	Function	Status	
	ON / OFF logic	Active "H"	
Discharge shunt function		Available	
	Pull-up resistor	None	
	Pull-down resistor	Available	

\*1. Parasitic diode

Figure 1

#### 2. S-1135 Series B type

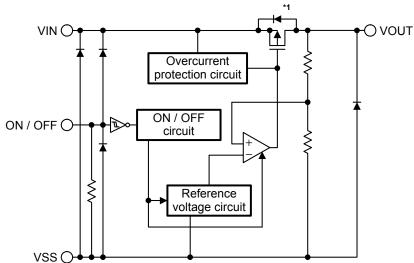


	Function	Status	
0	N / OFF logic	Active "H"	
	scharge shunt nction	Available	
Ρι	III-up resistor	None	
Ρι	Ill-down resistor	None	

\*1. Parasitic diode



#### 3. S-1135 Series C type

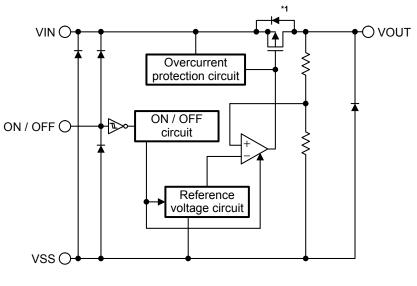


	Function	Status	
	ON / OFF logic	Active "H"	
-	Discharge shunt function	None	
	Pull-up resistor	None	
	Pull-down resistor	Available	

\*1. Parasitic diode

Figure 3

#### 4. S-1135 Series D type



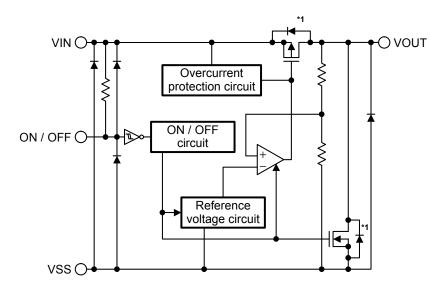
Function	Status	
ON / OFF logic	Active "H"	
Discharge shunt function	None	
Pull-up resistor	None	
Pull-down resistor	None	

**\*1.** Parasitic diode



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#### 5. S-1135 Series E type

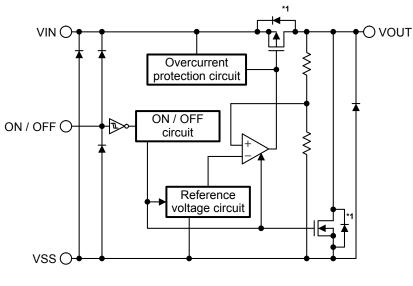


Function	Status	
ON / OFF logic	Active "L"	
Discharge shunt function	Available	
Pull-up resistor	Available	
Pull-down resistor	None	

\*1. Parasitic diode

Figure 5

#### 6. S-1135 Series F type

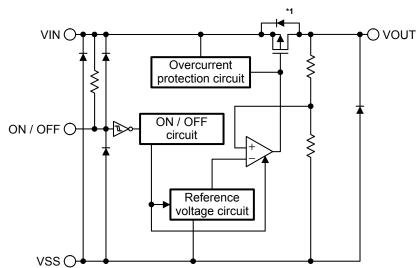


Function	Status
ON / OFF logic	Active "L"
Discharge shunt function	Available
Pull-up resistor	None
Pull-down resistor	None

\*1. Parasitic diode

Figure 6

#### 7. S-1135 Series G type

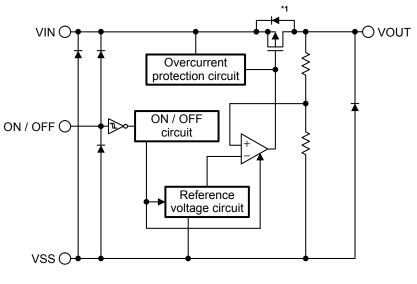


	Function	Status	
	ON / OFF logic	Active "L"	
•	Discharge shunt function	None	
	Pull-up resistor	Available	
	Pull-down resistor	None	

\*1. Parasitic diode

Figure 7

#### 8. S-1135 Series H type



Function	Status	
ON / OFF logic	Active "L"	
Discharge shunt function	None	
Pull-up resistor	None	
Pull-down resistor	None	

\*1. Parasitic diode

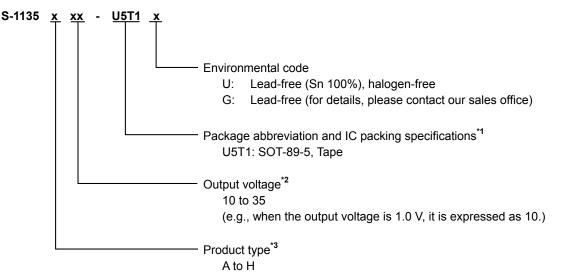
Figure 8

#### Product Name Structure

Users can select the product type, output voltage, and package type for the S-1135 Series. Refer to "1. Product name" regarding the contents of product name, "2. Function list of product type" regarding the product type, "3. Packages" regarding the package drawings.

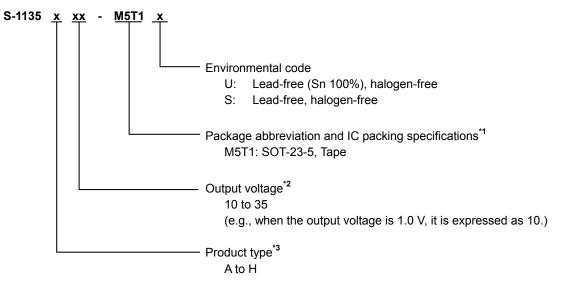
#### 1. Product name

#### 1.1 SOT-89-5



- \*1. Refer to the tape drawing.
- \*2. If you request the product which has 0.05 V step, contact our sales office.
- \*3. Refer to "2. Function list of product type".

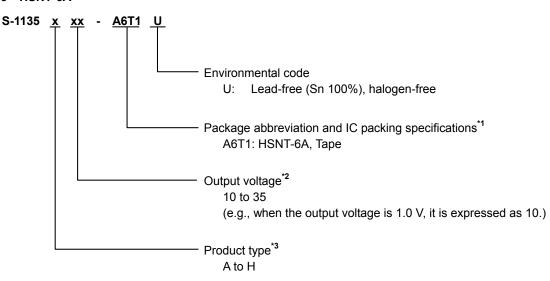




- \*1. Refer to the tape drawing.
- \*2. If you request the product which has 0.05 V step, contact our sales office.
- \*3. Refer to "2. Function list of product type".

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- **\*1.** Refer to the tape drawing.
- \*2. If you request the product which has 0.05 V step, contact our sales office.
- \*3. Refer to "2. Function list of product type".

#### 2. Function list of product type

Tab	ما	1
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Product Type	Product Type ON / OFF Logic Discharge Shunt Function		Pull-up Resistor	Pull-down Resistor	
А	Active "H"	Available	None	Available	
В	Active "H" Available		None None		
С	Active "H"	None	None	Available	
D	Active "H"	None	None None		
E	Active "L"	Available	Available	None	
F	Active "L"	Available	None	None	
G	Active "L"	None	Available	None	
Н	Active "L"	None	None	None	

#### 3. Packages

Dookogo Nomo	Drawing Code				
Package Name Package Tape			Reel	Land	Stencil Opening
SOT-89-5	UP005-A-P-SD	UP005-A-C-SD	UP005-A-R-SD	-	—
SOT-23-5	MP005-A-P-SD	MP005-A-C-SD	MP005-A-R-SD	_	—
HSNT-6A	PJ006-A-P-SD	PJ006-A-C-SD	PJ006-A-R-SD	PJ006-A-LM-SD	PJ006-A-LM-SD

#### 4. Product name list

#### 4.1 S-1135 Series A type

DN / OFF logic: Discharge shunt function:	Active "H" Available	Pull-up resistor: Pull-down resistor:	None Available
	т	able 2	
Output Voltage	SOT-89-5	SOT-23-5	HSNT-6A
1.0 V ±15 mV	S-1135A10-U5T1x	S-1135A10-M5T1y	S-1135A10-A6T1U
1.1 V ±15 mV	S-1135A11-U5T1x	S-1135A11-M5T1y	S-1135A11-A6T1U
1.2 V ±15 mV	S-1135A12-U5T1x	S-1135A12-M5T1y	S-1135A12-A6T1U
1.3 V ±15 mV	S-1135A13-U5T1x	S-1135A13-M5T1y	S-1135A13-A6T1U
1.4 V ±15 mV	S-1135A14-U5T1x	S-1135A14-M5T1y	S-1135A14-A6T1U
1.5 V ±1.0%	S-1135A15-U5T1x	S-1135A15-M5T1y	S-1135A15-A6T1U
1.6 V ±1.0%	S-1135A16-U5T1x	S-1135A16-M5T1y	S-1135A16-A6T1U

0 1100/(12 0011)	O TIOO/ (12 MOTTy	
S-1135A13-U5T1x	S-1135A13-M5T1y	S-1135A13-A6T1U
S-1135A14-U5T1x	S-1135A14-M5T1y	S-1135A14-A6T1U
S-1135A15-U5T1x	S-1135A15-M5T1y	S-1135A15-A6T1U
S-1135A16-U5T1x	S-1135A16-M5T1y	S-1135A16-A6T1U
S-1135A17-U5T1x	S-1135A17-M5T1y	S-1135A17-A6T1U
S-1135A18-U5T1x	S-1135A18-M5T1y	S-1135A18-A6T1U
S-1135A1J-U5T1x	S-1135A1J-M5T1y	S-1135A1J-A6T1U
S-1135A19-U5T1x	S-1135A19-M5T1y	S-1135A19-A6T1U
S-1135A20-U5T1x	S-1135A20-M5T1y	S-1135A20-A6T1U
S-1135A21-U5T1x	S-1135A21-M5T1y	S-1135A21-A6T1U
S-1135A22-U5T1x	S-1135A22-M5T1y	S-1135A22-A6T1U
S-1135A23-U5T1x	S-1135A23-M5T1y	S-1135A23-A6T1U
S-1135A24-U5T1x	S-1135A24-M5T1y	S-1135A24-A6T1U
S-1135A25-U5T1x	S-1135A25-M5T1y	S-1135A25-A6T1U
S-1135A26-U5T1x	S-1135A26-M5T1y	S-1135A26-A6T1U
S-1135A27-U5T1x	S-1135A27-M5T1y	S-1135A27-A6T1U
S-1135A28-U5T1x	S-1135A28-M5T1y	S-1135A28-A6T1U
S-1135A2J-U5T1x	S-1135A2J-M5T1y	S-1135A2J-A6T1U
S-1135A29-U5T1x	S-1135A29-M5T1y	S-1135A29-A6T1U
S-1135A30-U5T1x	S-1135A30-M5T1y	S-1135A30-A6T1U
S-1135A31-U5T1x	S-1135A31-M5T1y	S-1135A31-A6T1U
S-1135A32-U5T1x	S-1135A32-M5T1y	S-1135A32-A6T1U
S-1135A33-U5T1x	S-1135A33-M5T1y	S-1135A33-A6T1U
S-1135A34-U5T1x	S-1135A34-M5T1y	S-1135A34-A6T1U
S-1135A35-U5T1x	S-1135A35-M5T1y	S-1135A35-A6T1U
	S-1135A13-U5T1x         S-1135A13-U5T1x         S-1135A15-U5T1x         S-1135A16-U5T1x         S-1135A16-U5T1x         S-1135A17-U5T1x         S-1135A17-U5T1x         S-1135A17-U5T1x         S-1135A17-U5T1x         S-1135A19-U5T1x         S-1135A19-U5T1x         S-1135A20-U5T1x         S-1135A30-U5T1x         S-1135A31-U5T1x         S-1135A32-U5T1x         S-1135A32-U5T1x         S-1135A30-U5T1x         S-1135A30-U5T1x         S-1135A34-U5T1x	S-1135A13-U5T1x         S-1135A13-M5T1y           S-1135A14-U5T1x         S-1135A14-M5T1y           S-1135A15-U5T1x         S-1135A15-M5T1y           S-1135A16-U5T1x         S-1135A16-M5T1y           S-1135A16-U5T1x         S-1135A16-M5T1y           S-1135A17-U5T1x         S-1135A16-M5T1y           S-1135A17-U5T1x         S-1135A17-M5T1y           S-1135A18-U5T1x         S-1135A18-M5T1y           S-1135A19-U5T1x         S-1135A19-M5T1y           S-1135A19-U5T1x         S-1135A19-M5T1y           S-1135A20-U5T1x         S-1135A20-M5T1y           S-1135A20-U5T1x         S-1135A20-M5T1y           S-1135A20-U5T1x         S-1135A22-M5T1y           S-1135A23-U5T1x         S-1135A23-M5T1y           S-1135A25-U5T1x         S-1135A26-M5T1y           S-1135A25-U5T1x         S-1135A26-M5T1y           S-1135A26-U5T1x         S-1135A26-M5T1y           S-1135A27-U5T1x         S-1135A26-M5T1y           S-1135A27-U5T1x         S-1135A28-M5T1y           S-1135A29-U5T1x         S-1135A29-M5T1y           S-1135A30-U5T1x         S-1135A30-M5T1y           S-1135A31-U5T1x         S-1135A30-M5T1y           S-1135A31-U5T1x         S-1135A31-M5T1y           S-1135A33-U5T1x         S-1135A33-M5T1y

Remark 1. Please contact our sales office for products with specifications other than the above.

2. x: G or U

y: S or U

#### 4.

N / OFF logic:	Active "H"	Pull-up resistor:	None					
scharge shunt function	Available	Pull-down resistor:	None					
Table 3								
Output Voltage	SOT-89-5	SOT-23-5	HSNT-6A					
1.0 V ±15 mV	S-1135B10-U5T1x	S-1135B10-M5T1y	S-1135B10-A6T1U					
1.1 V ±15 mV	S-1135B11-U5T1x	S-1135B11-M5T1y	S-1135B11-A6T1U					
1.2 V ±15 mV	S-1135B12-U5T1x	S-1135B12-M5T1y	S-1135B12-A6T1U					
1.3 V ±15 mV	S-1135B13-U5T1x	S-1135B13-M5T1y	S-1135B13-A6T1U					
1.4 V ±15 mV	S-1135B14-U5T1x	S-1135B14-M5T1y	S-1135B14-A6T1U					
1.5 V ±1.0%	S-1135B15-U5T1x	S-1135B15-M5T1y	S-1135B15-A6T1U					
1.6 V ±1.0%	S-1135B16-U5T1x	S-1135B16-M5T1y	S-1135B16-A6T1U					
1.7 V ±1.0%	S-1135B17-U5T1x	S-1135B17-M5T1y	S-1135B17-A6T1U					
1.8 V ±1.0%	S-1135B18-U5T1x	S-1135B18-M5T1y	S-1135B18-A6T1U					
1.85 V ±1.0%	S-1135B1J-U5T1x	S-1135B1J-M5T1y	S-1135B1J-A6T1U					
1.9 V ±1.0%	S-1135B19-U5T1x	S-1135B19-M5T1y	S-1135B19-A6T1U					
2.0 V ±1.0%	S-1135B20-U5T1x	S-1135B20-M5T1y	S-1135B20-A6T1U					
2.1 V ±1.0%	S-1135B21-U5T1x	S-1135B21-M5T1y	S-1135B21-A6T1U					
2.2 V ±1.0%	S-1135B22-U5T1x	S-1135B22-M5T1y	S-1135B22-A6T1U					
2.3 V ±1.0%	S-1135B23-U5T1x	S-1135B23-M5T1y	S-1135B23-A6T1U					
2.4 V ±1.0%	S-1135B24-U5T1x	S-1135B24-M5T1y	S-1135B24-A6T1U					
2.5 V ±1.0%	S-1135B25-U5T1x	S-1135B25-M5T1y	S-1135B25-A6T1U					
2.6 V ±1.0%	S-1135B26-U5T1x	S-1135B26-M5T1y	S-1135B26-A6T1U					
2.7 V ±1.0%	S-1135B27-U5T1x	S-1135B27-M5T1y	S-1135B27-A6T1U					
2.8 V ±1.0%	S-1135B28-U5T1x	S-1135B28-M5T1y	S-1135B28-A6T1U					
2.85 V ±1.0%	S-1135B2J-U5T1x	S-1135B2J-M5T1y	S-1135B2J-A6T1U					
2.9 V ±1.0%	S-1135B29-U5T1x	S-1135B29-M5T1y	S-1135B29-A6T1U					
3.0 V ±1.0%	S-1135B30-U5T1x	S-1135B30-M5T1y	S-1135B30-A6T1U					
3.1 V ±1.0%	S-1135B31-U5T1x	S-1135B31-M5T1y	S-1135B31-A6T1U					
3.2 V ±1.0%	S-1135B32-U5T1x	S-1135B32-M5T1y	S-1135B32-A6T1U					
3.3 V ±1.0%	S-1135B33-U5T1x	S-1135B33-M5T1y	S-1135B33-A6T1U					
3.4 V ±1.0%	S-1135B34-U5T1x	S-1135B34-M5T1y	S-1135B34-A6T1U					
3.5 V ±1.0%	S-1135B35-U5T1x	S-1135B35-M5T1y	S-1135B35-A6T1U					

2. x: G or U

y: S or U

## HIGH RIPPLE-REJECTION LOW DROPOUT MIDDLE OUTPUT CURRENT CMOS VOLTAGE REGULATOR S-1135 Series Rev.2.2\_01

#### 4.3 S-1135 Series C type

ON / OFF logic: Discharge shunt function:	Active "H" None	Pull-up resistor: Pull-down resistor:	None Available					
Table 4								
Output Voltage	SOT-89-5	SOT-23-5	HSNT-6A					
1.0 V ±15 mV	S-1135C10-U5T1x	S-1135C10-M5T1y	S-1135C10-A6T1U					
1.1 V ±15 mV	S-1135C11-U5T1x	S-1135C11-M5T1y	S-1135C11-A6T1U					
1.2 V ±15 mV	S-1135C12-U5T1x	S-1135C12-M5T1y	S-1135C12-A6T1U					
1.3 V ±15 mV	S-1135C13-U5T1x	S-1135C13-M5T1y	S-1135C13-A6T1U					
1.4 V ±15 mV	S-1135C14-U5T1x	S-1135C14-M5T1y	S-1135C14-A6T1U					
1.5 V ±1.0%	S-1135C15-U5T1x	S-1135C15-M5T1y	S-1135C15-A6T1U					
1.6 V ±1.0%	S-1135C16-U5T1x	S-1135C16-M5T1y	S-1135C16-A6T1U					
1.7 V ±1.0%	S-1135C17-U5T1x	S-1135C17-M5T1y	S-1135C17-A6T1U					
1.8 V ±1.0%	S-1135C18-U5T1x	S-1135C18-M5T1y	S-1135C18-A6T1U					
1.85 V ±1.0%	S-1135C1J-U5T1x	S-1135C1J-M5T1y	S-1135C1J-A6T1U					
1.9 V ±1.0%	S-1135C19-U5T1x	S-1135C19-M5T1y	S-1135C19-A6T1U					
2.0 V ±1.0%	S-1135C20-U5T1x	S-1135C20-M5T1y	S-1135C20-A6T1U					
2.1 V ±1.0%	S-1135C21-U5T1x	S-1135C21-M5T1y	S-1135C21-A6T1U					
2.2 V ±1.0%	S-1135C22-U5T1x	S-1135C22-M5T1y	S-1135C22-A6T1U					
2.3 V ±1.0%	S-1135C23-U5T1x	S-1135C23-M5T1y	S-1135C23-A6T1U					
2.4 V ±1.0%	S-1135C24-U5T1x	S-1135C24-M5T1y	S-1135C24-A6T1U					
2.5 V ±1.0%	S-1135C25-U5T1x	S-1135C25-M5T1y	S-1135C25-A6T1U					
2.6 V ±1.0%	S-1135C26-U5T1x	S-1135C26-M5T1y	S-1135C26-A6T1U					
2.7 V ±1.0%	S-1135C27-U5T1x	S-1135C27-M5T1y	S-1135C27-A6T1U					
2.8 V ±1.0%	S-1135C28-U5T1x	S-1135C28-M5T1y	S-1135C28-A6T1U					
2.85 V ±1.0%	S-1135C2J-U5T1x	S-1135C2J-M5T1y	S-1135C2J-A6T1U					
2.9 V ±1.0%	S-1135C29-U5T1x	S-1135C29-M5T1y	S-1135C29-A6T1U					
3.0 V ±1.0%	S-1135C30-U5T1x	S-1135C30-M5T1y	S-1135C30-A6T1U					
3.1 V ±1.0%	S-1135C31-U5T1x	S-1135C31-M5T1y	S-1135C31-A6T1U					
3.2 V ±1.0%	S-1135C32-U5T1x	S-1135C32-M5T1y	S-1135C32-A6T1U					
3.3 V ±1.0%	S-1135C33-U5T1x	S-1135C33-M5T1y	S-1135C33-A6T1U					
3.4 V ±1.0%	S-1135C34-U5T1x	S-1135C34-M5T1y	S-1135C34-A6T1U					
3.5 V ±1.0%	S-1135C35-U5T1x	S-1135C35-M5T1y	S-1135C35-A6T1U					

**Remark 1.** Please contact our sales office for products with specifications other than the above.

2. x: G or U

y: S or U

#### 4.

V / OFF logic:	Active "H"	Pull-up resistor:	None						
scharge shunt function	n: None	Pull-down resistor:	None						
Table 5									
Output Voltage	SOT-89-5	SOT-23-5	HSNT-6A						
1.0 V ±15 mV	S-1135D10-U5T1x	S-1135D10-M5T1y	S-1135D10-A6T1U						
1.1 V ±15 mV	S-1135D11-U5T1x	S-1135D11-M5T1y	S-1135D11-A6T1U						
1.2 V ±15 mV	S-1135D12-U5T1x	S-1135D12-M5T1y	S-1135D12-A6T1U						
1.3 V ±15 mV	S-1135D13-U5T1x	S-1135D13-M5T1y	S-1135D13-A6T1U						
1.4 V ±15 mV	S-1135D14-U5T1x	S-1135D14-M5T1y	S-1135D14-A6T1U						
1.5 V ±1.0%	S-1135D15-U5T1x	S-1135D15-M5T1y	S-1135D15-A6T1U						
1.6 V ±1.0%	S-1135D16-U5T1x	S-1135D16-M5T1y	S-1135D16-A6T1U						
1.7 V ±1.0%	S-1135D17-U5T1x	S-1135D17-M5T1y	S-1135D17-A6T1U						
1.8 V ±1.0%	S-1135D18-U5T1x	S-1135D18-M5T1y	S-1135D18-A6T1U						
1.85 V ±1.0%	S-1135D1J-U5T1x	S-1135D1J-M5T1y	S-1135D1J-A6T1U						
1.9 V ±1.0%	S-1135D19-U5T1x	S-1135D19-M5T1y	S-1135D19-A6T1U						
2.0 V ±1.0%	S-1135D20-U5T1x	S-1135D20-M5T1y	S-1135D20-A6T1U						
2.1 V ±1.0%	S-1135D21-U5T1x	S-1135D21-M5T1y	S-1135D21-A6T1U						
2.2 V ±1.0%	S-1135D22-U5T1x	S-1135D22-M5T1y	S-1135D22-A6T1U						
2.3 V ±1.0%	S-1135D23-U5T1x	S-1135D23-M5T1y	S-1135D23-A6T1U						
2.4 V ±1.0%	S-1135D24-U5T1x	S-1135D24-M5T1y	S-1135D24-A6T1U						
2.5 V ±1.0%	S-1135D25-U5T1x	S-1135D25-M5T1y	S-1135D25-A6T1U						
2.6 V ±1.0%	S-1135D26-U5T1x	S-1135D26-M5T1y	S-1135D26-A6T1U						
2.7 V ±1.0%	S-1135D27-U5T1x	S-1135D27-M5T1y	S-1135D27-A6T1U						
2.8 V ±1.0%	S-1135D28-U5T1x	S-1135D28-M5T1y	S-1135D28-A6T1U						
2.85 V ±1.0%	S-1135D2J-U5T1x	S-1135D2J-M5T1y	S-1135D2J-A6T1U						
2.9 V ±1.0%	S-1135D29-U5T1x	S-1135D29-M5T1y	S-1135D29-A6T1U						
3.0 V ±1.0%	S-1135D30-U5T1x	S-1135D30-M5T1y	S-1135D30-A6T1U						
3.1 V ±1.0%	S-1135D31-U5T1x	S-1135D31-M5T1y	S-1135D31-A6T1U						
3.2 V ±1.0%	S-1135D32-U5T1x	S-1135D32-M5T1y	S-1135D32-A6T1U						
3.3 V ±1.0%	S-1135D33-U5T1x	S-1135D33-M5T1y	S-1135D33-A6T1U						
3.4 V ±1.0%	S-1135D34-U5T1x	S-1135D34-M5T1y	S-1135D34-A6T1U						
3.5 V ±1.0%	S-1135D35-U5T1x	S-1135D35-M5T1y	S-1135D35-A6T1U						

2. x: G or U

y: S or U

## Pin Configurations

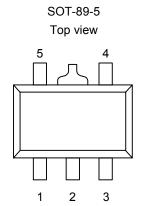


Figure 9



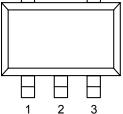
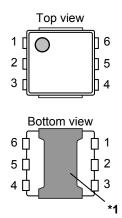


Figure 10

HSNT-6A



Pin No. Symbol Description VOUT Output voltage pin 1 2 VSS GND pin 3 NC<sup>\*1</sup> No connection ON / OFF ON / OFF pin 4 5 VIN Input voltage pin \*1.

Table 6

The NC pin is electrically open.

The NC pin can be connected to VIN or VSS.

Table 7					
Pin No. Symbol Description					
1	VIN	Input voltage pin			
2	VSS	GND pin			
3	ON / OFF	ON / OFF pin			
4	NC <sup>*1</sup>	No connection			
5 VOUT		Output voltage pin			

The NC pin is electrically open. \*1.

The NC pin can be connected to VIN or VSS.

Table 8					
Pin No. Symbol Description					
1	VOUT	Output voltage pin			
2	VSS	GND pin			
3	NC <sup>*1</sup>	No connection			
4	ON / OFF	ON / OFF pin			
5	NC <sup>*1</sup>	No connection			
6	VIN	Input voltage pin			

The NC pin is electrically open. \*1.

The NC pin can be connected to VIN or VSS.

\*1. Connect the heat sink of backside at shadowed area to the board, and set electric potential open or GND.

> However, do not use it as the function of electrode.

> > Figure 11

## Absolute Maximum Ratings

Table 9

			(Ta = 25°C unless othe	erwise specified)
Item		Symbol	Absolute Maximum Rating	Unit
Input voltage			$V_{\rm SS}-0.3$ to $V_{\rm SS}+6.0$	V
Input voltage		Von / OFF	$V_{SS} - 0.3$ to $V_{IN} + 0.3$	V
Output voltage	utput voltage		$V_{SS} - 0.3$ to $V_{IN} + 0.3$	V
	SOT-89-5		1000 <sup>*1</sup>	mW
Power dissipation	SOT-23-5	PD	600 <sup>*1</sup>	mW
	HSNT-6A		1000 <sup>*2</sup>	mW
Operation ambient temperature		T <sub>opr</sub>	-40 to +85	°C
Storage temperature		T <sub>stg</sub>	-40 to +125	°C

\*1. When mounted on board

[Mounted board]

(1) Board size : 114.3 mm  $\times$  76.2 mm  $\times$  t1.6 mm

(2) Name : JEDEC STANDARD51-7

\*2. When mounted on board

[Mounted board]

(1) Board size :  $50 \text{ mm} \times 50 \text{ mm} \times t1.6 \text{ mm}$ 

(2) Wiring ratio : 50%

Caution The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

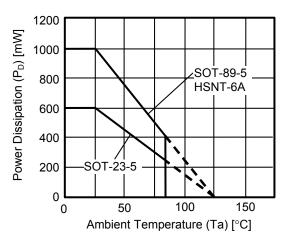


Figure 12 Power Dissipation of Package

## Electrical Characteristics

				0 (1 / 2)	(Ta = 2	5°C unles	ss otherw	vise spe	cified)
Item	Symbol	Conditions		Min.	Тур.	Max.	Unit	Test Circuit	
Output valtage*1	N	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V,		$1.0~V \leq V_{OUT(S)} < 1.5~V$	V <sub>OUT(S)</sub> - 0.015	V <sub>OUT(S)</sub>	V <sub>OUT(S)</sub> + 0.015	V	1
Output voltage*1	V <sub>OUT(E)</sub>	I <sub>OUT</sub> = 100 mA		$1.5~V \leq V_{OUT(S)} \leq 3.5~V$	$\begin{array}{c} V_{OUT(S)} \\ \times  0.99 \end{array}$	V <sub>OUT(S)</sub>	$V_{OUT(S)} \times 1.01$	V	1
Output current*2	Іоит	$V_{\text{IN}} \geq V_{\text{OUT}(s)} + 1.0 \text{ V}$			300* <sup>5</sup>		_	mA	3
				$1.0~V \leq V_{OUT(S)} < 1.1~V$	0.50	0.61	0.72	V	1
				$1.1~V \leq V_{OUT(S)} < 1.2~V$	_	0.49	0.61	V	1
				$1.2~V \leq V_{OUT(S)} < 1.3~V$	_	0.38	0.53	V	1
Dronout voltage*3	N.	I <sub>OUT</sub> = 300 mA		$1.3~V \leq V_{OUT(S)} < 1.4~V$	_	0.31	0.47	V	1
Dropout voltage*3	V <sub>drop</sub>	$I_{OUT} = 300 \text{ mA}$		$1.4 \text{ V} \le V_{OUT(S)} < 1.5 \text{ V}$	_	0.27	0.41	V	1
				$1.5 \text{ V} \le \text{V}_{\text{OUT}(S)} < 2.0 \text{ V}$	_	0.25	0.38	V	1
	$2.0 \text{ V} \le V_{\text{OUT(S)}} < 2.6 \text{ V}$ —	_	0.21	0.32	V	1			
				$2.6 \text{ V} \leq \text{V}_{\text{OUT}(S)} \leq 3.5 \text{ V}$	_	0.16	0.26	V	1
	ΔVουτ1	$1.6 \text{ V} \le \text{V}_{\text{IN}} \le 5.5 \text{ V},$ Iout = 100 mA		$1.0 \text{ V} \le V_{OUT(S)} < 1.1 \text{ V}$	_	0.05	0.2	%/V	1
Line regulation	$\Delta VIN \bullet VOUT$	$V_{OUT(S)} + 0.5 V \le V_{IN} \le 5.5$ $I_{OUT} = 100 \text{ mA}$	$N \le 5.5 \text{ V},$ 1.1 V $\le$ Vout(s) $\le 3.5 \text{ V}$	_	0.05	0.2	%/V	1	
		$V_{IN} = V_{OUT(S)} + 1.0 V,$		$1.0 \text{ V} \le V_{OUT(S)} \le 2.0 \text{ V}$	_	15	20	0 mV	
		$1.0 \text{ mA} \le I_{OUT} \le 150 \text{ mA}$		$2.0 \text{ V} < \text{V}_{\text{OUT(S)}} \le 3.5 \text{ V}$	_	15	25	mV	1
Load regulation	$\Delta V_{OUT2}$	$V_{IN} = V_{OUT(S)} + 1.0 V,$		$1.0 \text{ V} \le V_{\text{OUT}(S)} \le 3.0 \text{ V}$	_	30	40	mV	1
		1.0 mA $\leq$ I <sub>OUT</sub> $\leq$ 300 mA 3.0 V $<$ V <sub>OUT(S)</sub> $\leq$ 3.5 V			_	30	45	mV	1
Output voltage temperature coefficient*4	ΔVουτ Δ <b>Τα</b> •Vουτ	$V_{IN} = V_{OUT(S)} + 1.0 V, I_{OUT}$ -40°C ≤ Ta ≤ +85°C	= 100			±130	_	ppm/°C	1
Current consumption during operation	I <sub>SS1</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, ON	/ OFF	pin = ON, no load	_	45	65	μΑ	2
Current consumption during power-off	Iss2	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, ON	/ OFF	pin = OFF, no load	_	0.1	1.0	μΑ	2
Input voltage	VIN		_		1.5	_	5.5	V	_
ON / OFF pin input voltage "H"	V <sub>SH</sub>	$V_{IN} = V_{OUT(S)} + 1.0 V, R_L =$ determined by $V_{OUT}$ outp			1.0	_	_	V	4
ON / OFF pin input voltage "L"	Vsl	$V_{\text{IN}} = V_{\text{OUT}(\text{S})} + 1.0 \text{ V}, \text{ R}_{\text{L}} = 1.0 \text{ k}\Omega$ determined by V_{\text{OUT}} output level		_	_	0.3	V	4	
ON / OFF pin				/ E / F / G / H type	-0.1	_	0.1	μA	4
input current "H"	Ish				1.0	2.5	5.0	μA	4
ON / OFF pin				/ C / D / F / H type	-0.1	_	0.1	μA	4
input current "L"	I <sub>SL</sub>		E/G		1.0	2.5	5.0	μA	4
	RR	$V_{IN} = V_{OUT(S)} + 1.0 V,$ f = 1.0 kHz,		$1.0 \text{ V} \leq V_{\text{OUT(S)}} \leq 1.2 \text{ V}$		70	_	dB	5
Ripple rejection		ΔV <sub>rip</sub> = 0.5 Vrms, Ι <sub>ΟUT</sub> = 50 mA	1.2 V < V <sub>OUT(S)</sub> ≤ 3			65	_	dB	5
Short-circuit current	Ishort	$V_{IN} = V_{OUT(S)} + 1.0 V, ON$	/ OFF	pin = ON, V <sub>OUT</sub> = 0 V	_	100	_	mA	3

#### Table 10 (1 / 2)

## HIGH RIPPLE-REJECTION LOW DROPOUT MIDDLE OUTPUT CURRENT CMOS VOLTAGE REGULATOR Rev.2.2\_01 S-1135 Series

#### Table 10 (2 / 2)

#### S-1135 Series A / B / E / F type (With discharge shunt function)

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	Test Circuit
"L" output Nch ON resistance	RLOW	V <sub>OUT</sub> = 0.1 V, V <sub>IN</sub> = 5.5 V	—	35	_	Ω	3
S-1135 Series A / C	/ E / G type (W	ith pull-up / pull-down resistor)					
ltem	Symbol	Conditions	Min.	Тур.	Max.	Unit	Test Circuit
Power-off pull-up / pull-down resistor	R <sub>PD</sub>	_	1.0	2.2	5.0	MΩ	4

\*1. VOUT(S): Set output voltage

V<sub>OUT(E)</sub>: Actual output voltage

Output voltage when fixing  $I_{OUT}$  (= 100 mA) and inputting  $V_{OUT(S)}$  +1.0 V

\*2. The output current at which the output voltage becomes 95% of  $V_{OUT(E)}$  after gradually increasing the output current.

\*3.  $V_{drop} = V_{IN1} - (V_{OUT3} \times 0.98)$ 

 $V_{\text{OUT3}}$  is the output voltage when  $V_{\text{IN}}$  =  $V_{\text{OUT}(S)}$  + 1.0 V and  $I_{\text{OUT}}$  = 300 mA.

 $V_{\text{IN1}}$  is the input voltage at which the output voltage becomes 98% of  $V_{\text{OUT3}}$  after gradually decreasing the input voltage.

\*4. A change in the temperature of the output voltage [mV/°C] is calculated using the following equation.

$$\frac{\Delta V_{\text{OUT}}}{\Delta Ta} [\text{mV/}^{\circ}\text{C}]^{*1} = V_{\text{OUT}(S)} [V]^{*2} \times \frac{\Delta V_{\text{OUT}}}{\Delta Ta \bullet V_{\text{OUT}}} [\text{ppm/}^{\circ}\text{C}]^{*3} \div 1000$$

\*1. Change in temperature of output voltage

\*2. Set output voltage

\*3. Output voltage temperature coefficient

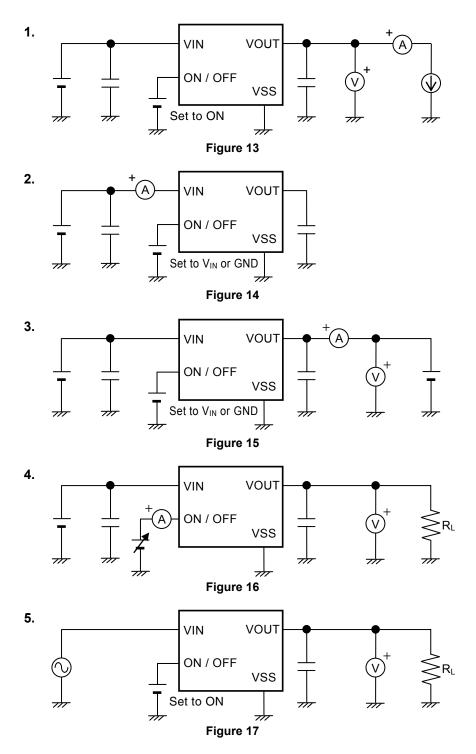
**\*5.** The output current can be at least this value.

Due to restrictions on the package power dissipation, this value may not be satisfied. Attention should be paid to the power dissipation of the package when the output current is large.

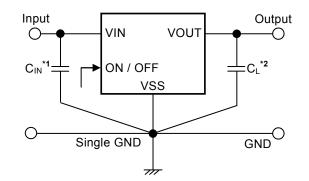
This specification is guaranteed by design.

## HIGH RIPPLE-REJECTION LOW DROPOUT MIDDLE OUTPUT CURRENT CMOS VOLTAGE REGULATOR S-1135 Series Rev.2.2\_01

## Test Circuits



## Standard Circuit



- **\*1.**  $C_{IN}$  is a capacitor for stabilizing the input.
- \*2. A ceramic capacitor of 1.0  $\mu$ F or more can be used as C<sub>L</sub>.

#### Figure 18

Caution The above connection diagram and constant will not guarantee successful operation. Perform thorough evaluation using the actual application to set the constant.

## Condition of Application

Input capacitor ( $C_{IN}$ ) : 1.0  $\mu$ F or more Output capacitor ( $C_L$ ) : 1.0  $\mu$ F or more

Caution Generally a series regulator may cause oscillation, depending on the selection of external parts. Confirm that no oscillation occurs in the application for which the above capacitors are used.

## ■ Selection of Input and Output Capacitors (C<sub>IN</sub>, C<sub>L</sub>)

The S-1135 Series requires an output capacitor between the VOUT and VSS pin for phase compensation. Operation is stabilized by a ceramic capacitor with an output capacitance of 1.0  $\mu$ F or more over the entire temperature range. When using an OS capacitor, a tantalum capacitor, or an aluminum electrolytic capacitor, the capacitance must be 1.0  $\mu$ F or more.

The value of the output overshoot or undershoot transient response varies depending on the value of the output capacitor. The required capacitance of the input capacitor differs depending on the application.

The recommended capacitance for an application is  $C_{IN} \ge 1.0 \ \mu\text{F}$ ,  $C_L \ge 1.0 \ \mu\text{F}$ ; however, when selecting the output capacitor, perform sufficient evaluation, including evaluation of temperature characteristics, on the actual device.

### Explanation of Terms

#### 1. Low dropout voltage regulator

This voltage regulator has the low dropout voltage due to its built-in low on-resistance transistor.

#### 2. Output voltage (VOUT)

The accuracy of the output voltage is ensured at  $\pm 1.0\%$  or  $\pm 15 \text{ mV}^{*1}$  under the specified conditions of fixed input voltage<sup>\*2</sup>, fixed output current, and fixed temperature.

- \*1. When  $V_{OUT} < 1.5 \text{ V}$  : ±15 mV, When 1.5 V ≤  $V_{OUT}$  : ±1.0%
- \*2. Differs depending on the product.

Caution If the above conditions change, the output voltage value may vary and exceed the accuracy range of the output voltage. Refer to "■ Electrical Characteristics" and "■ Characteristics (Typical Data)" for details.

3. Line regulation 
$$\left(\frac{\Delta V_{OUT1}}{\Delta V_{IN} \bullet V_{OUT}}\right)$$

Indicates the dependency of the output voltage on the input voltage. That is, the values show how much the output voltage changes due to a change in the input voltage with the output current remaining unchanged.

#### 4. Load regulation (ΔV<sub>OUT2</sub>)

Indicates the dependency of the output voltage on the output current. That is, the values show how much the output voltage changes due to a change in the output current with the input voltage remaining unchanged.

#### 5. Dropout voltage (Vdrop)

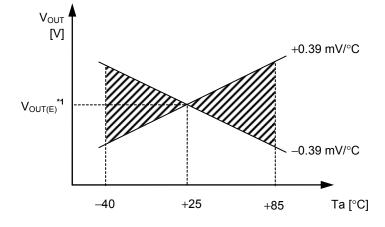
Indicates the difference between input voltage (V<sub>IN1</sub>) and the output voltage when; decreasing input voltage (V<sub>IN</sub>) gradually until the output voltage has dropped out to the value of 98% of output voltage (V<sub>OUT3</sub>), which is at V<sub>IN</sub> =  $V_{OUT(S)} + 1.0 \text{ V}$ .

 $V_{drop} = V_{IN1} - (V_{OUT3} \times 0.98)$ 

## 6. Output voltage temperature coefficient $\left(\frac{\Delta V_{OUT}}{\Delta Ta \bullet V_{OUT}}\right)$

The shaded area in **Figure 19** is the range where  $V_{OUT}$  varies in the operation temperature range when the output voltage temperature coefficient is  $\pm 130$  ppm/°C.

#### Example of S-1135B30 typ. product



\*1.  $V_{OUT(E)}$  is the value of the output voltage measured at Ta = +25°C.

#### Figure 19

A change in the temperature of the output voltage  $[mV/^{\circ}C]$  is calculated using the following equation.

 $\frac{\Delta V_{OUT}}{\Delta Ta} [mV/^{\circ}C]^{*1} = V_{OUT(S)} [V]^{*2} \times \frac{\Delta V_{OUT}}{\Delta Ta \bullet V_{OUT}} [ppm/^{\circ}C]^{*3} \div 1000$ 

- \*1. Change in temperature of output voltage
- \*2. Set output voltage
- \*3. Output voltage temperature coefficient

## Operation

#### 1. Basic operation

Figure 20 shows the block diagram of the S-1135 Series.

The error amplifier compares the reference voltage ( $V_{ref}$ ) with feedback voltage ( $V_{fb}$ ), which is the output voltage resistance-divided by feedback resistors ( $R_s$  and  $R_f$ ). It supplies the gate voltage necessary to maintain the constant output voltage which is not influenced by the input voltage and temperature change, to the output transistor.

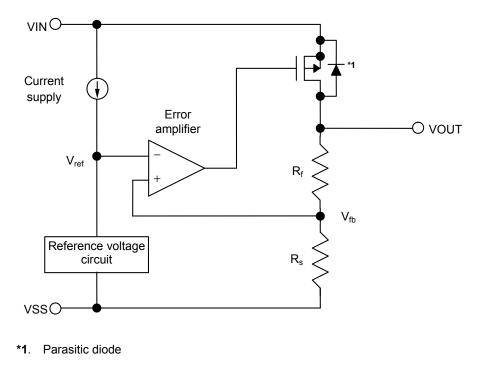


Figure 20

#### 2. Output transistor

In the S-1135 Series, a low on-resistance P-channel MOS FET is used as the output transistor. Be sure that  $V_{OUT}$  does not exceed  $V_{IN} + 0.3$  V to prevent the voltage regulator from being damaged due to reverse current flowing from the VOUT pin through a parasitic diode to the VIN pin, when the potential of  $V_{OUT}$  became higher than  $V_{IN}$ .

#### 3. ON / OFF pin

This pin starts and stops the regulator.

When the ON / OFF pin is set to OFF level, the entire internal circuit stops operating, and the built-in P-channel MOS FET output transistor between the VIN and VOUT pins is turned off, reducing current consumption significantly. Since the S-1135 Series A / B / E / F type has a built-in discharge shunt circuit to discharge the output capacitance, the VOUT pin is forcibly set to V<sub>SS</sub> level. In the S-1135 Series C / D / G / H type, the VOUT pin is set to V<sub>SS</sub> level through several hundred k $\Omega$  internal divided resistors between the VOUT and VSS pins. Note that the current consumption increases when a voltage of 0.3 V to 1.0 V (Ta = 25°C) is applied to the ON / OFF pin.

The ON / OFF pin is configured as shown in **Figures 21** and **22**. In the S-1135 Series A / C / E / G type, the ON / OFF pin is internally pulled up to the VIN pin or pulled down to the VSS pin in the floating status, so the VOUT pin is set to the V<sub>SS</sub> level. In the S-1135 Series B / D / F / H type, the ON / OFF pin is not internally pulled up or pulled down, so do not use the ON / OFF pin in the floating status. When not using the ON / OFF pin, connect it to the VIN pin in the product B / D type, and connect it to the VSS pin in the F / H type.

#### Table 11

Product Type	ON / OFF Pin	Internal Circuit	VOUT Pin Voltage	Current Consumption
A/B/C/D	"H": ON	Operate	Set value	I <sub>SS1</sub> *1
A/B/C/D	"L": OFF	Stop	V <sub>SS</sub> level	I <sub>SS2</sub>
E/F/G/H	"H": OFF	Stop	Vss level	ISS2
E/F/G/H	"L": ON	Operate	Set value	I <sub>SS1</sub> *1

\*1. Note that the IC's current consumption increases as much as current flows into the pull-up / pull-down resistor when; the ON / OFF pin is connected to the VIN pin in the A / C type, the ON / OFF pin is connected to the VSS pin in the E / G type (Refer to Figure 21).

(1) S-1135 Series A / C / E / G type

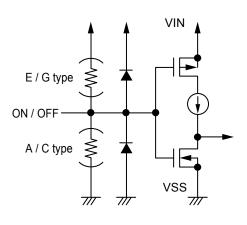


Figure 21

#### (2) S-1135 Series B / D / F / H type

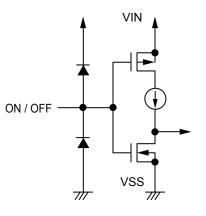
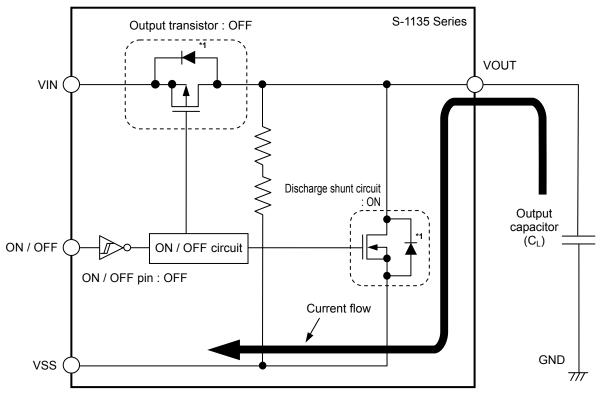


Figure 22

#### 4. Discharge shunt function (S-1135 Series A / B / E / F type)

The S-1135 Series A / B / E / F type has a built-in discharge shunt circuit to discharge the output capacitance. When the ON / OFF pin is set to OFF level, turns the output transistor off, and turns the discharge shunt circuit on so that the output capacitor discharges. These types allow the VOUT pin to reach the V<sub>SS</sub> level faster than the S-1135 Series C / D / G / H type that does not have a discharge shunt circuit.



\*1. Parasitic diode

Figure 23

#### 5. Overcurrent protection circuit

The S-1135 Series has an overcurrent protection circuit having the characteristics shown in "1. Output Voltage vs. Output Current (When Load Current Increases) (Ta =  $25^{\circ}$ C)" in "**■** Characteristics (Typical Data)", in order to protect the output transistor against an excessive output current and short circuiting between the VOUT and VSS pins. The current when the output pin is short-circuited (I<sub>short</sub>) is internally set at approx. 100 mA (typ.), and the normal value is restored for the output voltage, if releasing a short circuit once.

Caution This overcurrent protection circuit does not work as for thermal protection. If this IC long keeps short circuiting inside, pay attention to the conditions of input voltage and load current so that, under the usage conditions including short circuit, the loss of the IC will not exceed power dissipation of the package.

#### 6. Pull-down / pull-up resistor (S-1135 Series A / C / E / G type)

In the S-1135 Series A / C / E / G type, the ON / OFF pin is internally pulled up to the VIN pin or pulled down to the VSS pin in the floating status, so the VOUT pin is set to the  $V_{SS}$  level.

Note that the IC's current consumption increases as much as current flows into the pull-up / pull-down resistor of 2.2 M $\Omega$  (typ.) when; the ON / OFF pin is connected to the VIN pin in the A / C type, the ON / OFF pin is connected to the VSS pin in the E / G type.

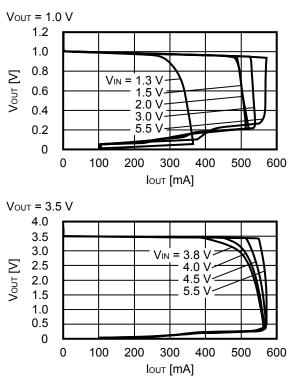
### Precautions

- Wiring patterns for the VIN pin, the VOUT pin and GND should be designed so that the impedance is low. When
  mounting an output capacitor between the VOUT and VSS pins (C<sub>L</sub>) and a capacitor for stabilizing the input between
  the VIN and VSS pins (C<sub>IN</sub>), the distance from the capacitors to these pins should be as short as possible.
- Note that generally the output voltage may increase when a series regulator is used at low load current (1.0 mA or less).
- Note that generally the output voltage may increase due to the leakage current from an output driver when a series regulator is used at high temperature.
- Generally a series regulator may cause oscillation, depending on the selection of external parts. The following conditions are recommended for the S-1135 Series. However, be sure to perform sufficient evaluation under the actual usage conditions for selection, including evaluation of temperature characteristics. Refer to "5. Example of Equivalent Series Resistance vs. Output Current Characteristics (Ta = 25°C)" in "■ Reference Data" for the equivalent series resistance (R<sub>ESR</sub>) of the output capacitor.

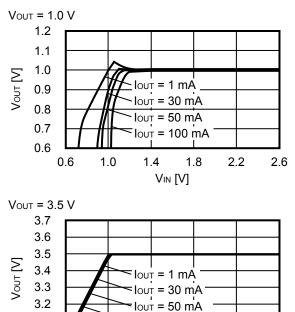
- The voltage regulator may oscillate when the impedance of the power supply is high and the input capacitance is small or an input capacitor is not connected.
- If the output capacitance is small, power supply's fluctuation and the characteristics of load fluctuation become worse. Sufficiently evaluate the output voltage's fluctuation with the actual device.
- Overshoot may occur in the output voltage momentarily if the voltage is rapidly raised at power-on or when the power supply fluctuates. Sufficiently evaluate the output voltage at power-on with the actual device.
- The application conditions for the input voltage, the output voltage, and the load current should not exceed the package power dissipation.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- In determining the output current, attention should be paid to the output current value specified in **Table 10** in "■ **Electrical Characteristics**" and footnote **\*5** of the table.
- SII Semiconductor Corporation claims no responsibility for any disputes arising out of or in connection with any infringement by products including this IC of patents owned by a third party.

## Characteristics (Typical Data)

#### 1. Output Voltage vs. Output Current (When Load Current Increases) (Ta = 25°C)



#### 2. Output Voltage vs. Input Voltage (Ta = 25°C)



Ιουτ = 100 mA

VIN [V]

4.0

4.5

5.0

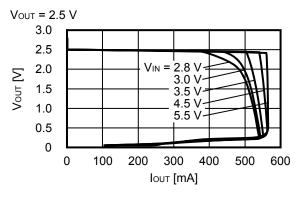
5.5

3.1

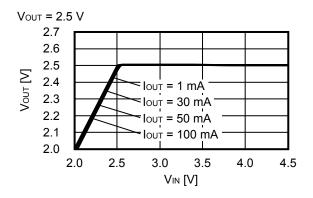
3.0

3.0

3.5



- **Remark** In determining the output current, attention should be paid to the following.
  - The minimum output current value and footnote \*5 in Table 10 in the "■ Electrical Characteristics"
  - 2. The package power dissipation



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