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Taiwan Semiconductor

1.5A, 200V - 1000V Surface Mount Rectifiers

FEATURES

- Ideal for automated placement
- Compact package size
- High surge current capability
- Low power loss, high efficiency
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

APPLICATIONS

- High frequency rectification
- Freewheeling application
- Switching mode converters and inverters in computer, automotive and telecommunication.

MECHANICAL DATA

- Case: SOD-123W
- Molding compound meets UL 94V-0 flammability rating
- Moisture sensitivity level: level 1, per J-STD-020
- Part no. with suffix "H" means AEC-Q101 qualified
- Packing code with suffix "G" means green compound (halogen-free)
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 19mg (approximately)

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C ur	less otherwise	noted)					
PARAMETER	SYMDOL	S15	S15	S15	S15	S15	UNIT
PARAMETER	SYMBOL		GLW	JLW	KLW	MLW	UNIT
Mading and an the device		15	15	15	15	15	
Marking code on the device		DLW	GLW	JLW	KLW	MLW	
Repetitive peak reverse voltage	V _{RRM}	200	400	600	800	1000	V
Reverse voltage, total rms value	V _{R(RMS)}	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	200	400	600	800	1000	V
Forward current	I _{F(AV)}			1.5			А
Surge peak forward current, 8.3 ms single half sine-wave superimposed on rated load per diode	I _{FSM}			50			А
Junction temperature	TJ		-5	55 to +1	75		°C
Storage temperature T _{STG} -55 to +175			°C				

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I _{F(AV)}	1.5	А		
V _{RRM}	200 - 1000	V		
I _{FSM}	50	А		
T _{J MAX}	175	°C		
Package	SOD-123W			
Configuration	Single die			





SOD-123W





THERMAL PERFORMANCE			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-lead thermal resistance	R _{eJL}	29	°C/W
Junction-to-ambient thermal resistance	R _{eja}	85	°C/W
Junction-to-case thermal resistance	R _{eJC}	31	°C/W

Thermal Performance Note: Units mounted on recommended PCB (5mm*5mm Cu pad test board)

PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
	$I_{\rm F}$ = 1A, $T_{\rm J}$ = 25°C		0.94	1.05	V
Forward voltage per diode ⁽¹⁾	I _F = 1.5A, T _J = 25°C	V _F	0.98	1.1	V
	I _F = 1A, T _J = 125°C		0.81	1	V
	I _F = 1.5A, T _J = 125°C		0.87	1.05	V
$\mathbf{D}_{\mathbf{r}}$	T _J = 25°C	1	0.06	1	μA
Reverse current @ rated V_R per diode ⁽²⁾	T _J = 125°C	I _R	6.06	100	μA
Junction Capacitance	1 MHz, V _R =4.0V	CJ	10	-	ρF

Notes:

- 1. Pulse test with PW=0.3 ms
- 2. Pulse test with PW=30 ms

ORDERING INFORMATION					
PART NO.	PART NO. SUFFIX	PACKING Code	PACKING CODE SUFFIX	PACKAGE	PACKING
S15xLW		RV	G	SOD-123W	3,000 / 7" Reel
(Note 1,2)	Н	RQ		SOD-123W	10,000 / 13" Reel

Notes:

- 1. "x" defines voltage from 200V (S15DLW) to 1000V (S15MLW)
- 2. Whole series with green compound (halogen-free)

EXAMPLE P/N					
EXAMPLE P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
S15MLWHRVG	S15MLW	Н	RV	G	AEC-Q101 qualified Green compound



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

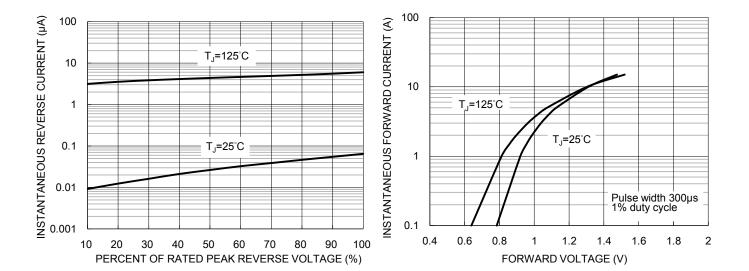
2 100 AVERAGE FORWARD CURRENT (A) 1.5 CAPACITANCE (pF) 1 0.5 Resistive or inductive load with heat sink f=1.0MHz Vsig=50mVp-p 0 0.1 30 60 90 120 150 100 1 10 CASE TEMPERATURE (°C) **REVERSE VOLTAGE (V)**

Fig.1 Forward Current Derating Curve

Fig.2 Typical Junction Capacitance

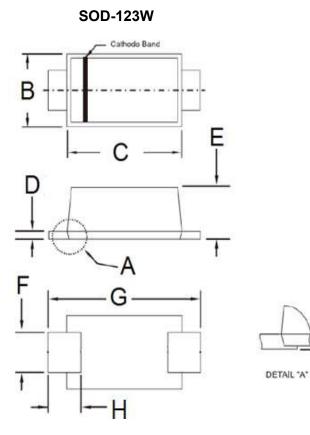
Fig.3 Typical Reverse Characteristics

Fig.4 Typical Forward Characteristics



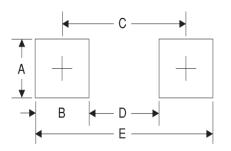
S15DLW - S15MLW Taiwan Semiconductor

PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)		
DIN.	Min	Max	Min	Max	
В	1.70	1.90	0.067	0.075	
С	2.60	2.90	0.102	0.114	
D	0.10	0.22	0.004	0.009	
E	0.90	1.02	0.035	0.040	
F	0.90	1.05	0.035	0.041	
G	3.60	3.80	0.142	0.150	
Н	0.50	0.85	0.020	0.033	
I	0.00	0.10	0.000	0.004	

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
А	1.4	0.055
В	1.2	0.047
С	3.1	0.122
D	1.9	0.075
E	4.3	0.169

MARKING DIAGRAM



P/N	= Marking Code
G	= Green Compound
YW	= Date Code
F	= Factory Code



Taiwan Semiconductor

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