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NPN Silicon General Purpose Amplifier Transistor

This NPN transistor is designed for general purpose amplifier applications. This device is housed in the SC-75/SOT-416 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Reduces Board Space
- High h_{FF}, 210–460 (typical)
- Low $V_{CE(sat)}$, < 0.5 V
- Available in 8 mm, 7 inch/3000 Unit Tape and Reel
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS $(T_J = 25^{\circ}C)$

Rating	Symbol	Value	Unit
Collector-Base Voltage	V _{(BR)CBO}	50	Vdc
Collector-Emitter Voltage	V _{(BR)CEO}	50	Vdc
Emitter-Base Voltage	V _{(BR)EBO}	5.0	Vdc
Collector Current – Continuous	I _C	100	mAdc

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 1)	P _D	125	mW
Junction Temperature	TJ	150	°C
Storage Temperature Range	T _{stg}	−55 ~ +150	°C

 Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.



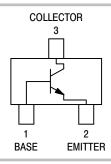
ON Semiconductor®

http://onsemi.com

NPN GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT



SC-75 CASE 463-01 STYLE 1



MARKING DIAGRAM



B9 = Device Code
M = Date Code*

- Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
2SC4617G	SC-75 (Pb-Free)	3,000/Tape & Reel
S2SC4617G	SC-75 (Pb-Free)	3,000/Tape & Reel
2SC4617T1G	SC-75 (Pb-Free)	3,000/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

^{*}Date Code orientation may vary depending upon manufacturing location.

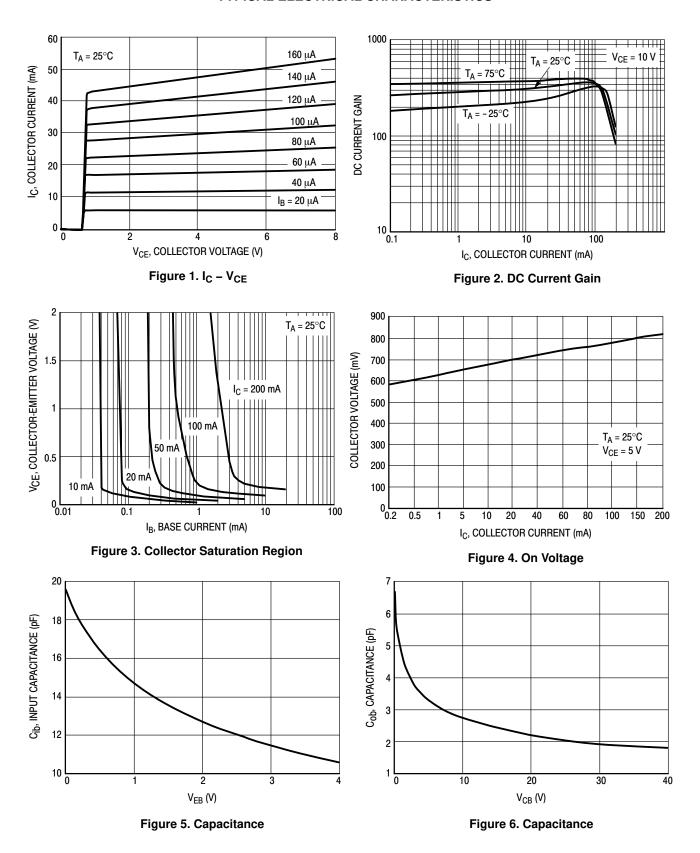
ELECTRICAL CHARACTERISTICS (T_A = 25°C)

Characteristic	Symbol	Min	Тур	Max	Unit
Collector-Base Breakdown Voltage (I _C = 50 μAdc, I _E = 0)	V _{(BR)CBO}	50	-	-	Vdc
Collector-Emitter Breakdown Voltage (I _C = 1.0 mAdc, I _B = 0)	V _{(BR)CEO}	50	_	-	Vdc
Emitter-Base Breakdown Voltage (I _E = 50 μAdc, I _E = 0)	V _{(BR)EBO}	5.0	_	-	Vdc
Collector-Base Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	I _{CBO}	-	_	0.5	μΑ
Emitter-Base Cutoff Current (V _{EB} = 4.0 Vdc, I _B = 0)	I _{EBO}	-	-	0.5	μΑ
Collector-Emitter Saturation Voltage (Note 2) $(I_C = 60 \text{ mAdc}, I_B = 5.0 \text{ mAdc})$	V _{CE(sat)}	-	_	0.4	Vdc
DC Current Gain (Note 2) (V _{CE} = 6.0 Vdc, I _C = 1.0 mAdc)	h _{FE}	120	_	560	-
Transition Frequency (V _{CE} = 12 Vdc, I _C = 2.0 mAdc, f = 30 MHz)	f _T	-	180	-	MHz
Output Capacitance (V _{CB} = 12 Vdc, I _C = 0 Adc, f = 1 MHz)	C _{OB}	-	2.0	-	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

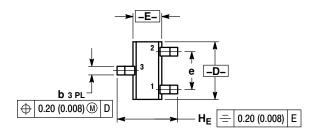
^{2.} Pulse Test: Pulse Width $\leq 300~\mu s,~D.C. \leq 2\%.$

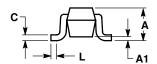
TYPICAL ELECTRICAL CHARACTERISTICS



PACKAGE DIMENSIONS

SC-75/SOT-416 CASE 463 ISSUE F





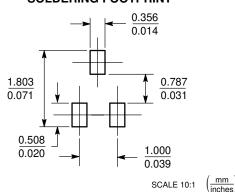
NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS INCHES			;		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
С	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.059	0.063	0.067
E	0.70	0.80	0.90	0.027	0.031	0.035
е	1.00 BSC		0	0.04 BSC)	
L	0.10	0.15	0.20	0.004	0.006	0.008
He	1.50	1 60	1 70	0.061	0.063	0.065

STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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