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S6SBP401AJ0SA1001, S6SBP401AM2SA100

Automotive Evaluation Board Operation Guide

Doc. No. 002-08764 Rev. *A

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Preface



This manual explains how to use the evaluation board. Be sure to read this manual before using the product. For this product, please consult with sales representatives or support representatives.

Handling and Use

Handling and use of this product and notes regarding its safe use are described in the manuals.

Follow the instructions in the manuals to use this product.

Keep this manual at hand so that you can refer to it anytime during use of this product.

Notice on this Document

All information included in this document is current as of the date it is issued. Such information is subject to change without any prior notice.

Please confirm the latest relevant information with the sales representatives

Cautions



Caution of the Products Described in this Document

The following precautions apply to the product described in this manual.

|--|

Electric Shock,	Before performing any operation described in this manual, turn off all the power supplies to the system.
Damage	Performing such an operation with the power on may cause an electric shock or device fault.
Electric Shock,	Once the product has been turned on, do not touch any metal part of it.

Cuts, Damage	Before moving the product, be sure to turn off all the power supplies and unplug the cables. Watch your step when carrying the product. Do not use the product in an unstable location such as a place exposed to strong vibration or a sloping surface. Doing so may cause the product to fall, resulting in an injury or fault.
Cuto	The product contains sharp edges that are left unavoidably exposed, such as jumper plugs.
Cuts	Handle the product with due care not to get injured with such pointed parts.
Damage	Do not place anything on the product or expose the product to physical shocks. Do not carry the product after the power has been turned on.
3	Doing so may cause a malfunction due to overloading or shock.
Damage	Since the product contains many electronic components, keep it away from direct sunlight, high temperature, and high humidity to prevent condensation. Do not use or store the product where it is exposed to much dust or a strong magnetic or electric field for an extended period of time. Inappropriate operating or storage environments may cause a fault.
	Use the product within the ranges given in the specifications.
Damage	Operation over the specified ranges may cause a fault.
Damage	To prevent electrostatic breakdown, do not let your finger or other object come into contact with the metal parts of any of the connectors. Before handling the product, touch a metal object (such as a door knob) to discharge any static electricity from your body.



Damage	When turning the power on or off, follow the relevant procedure as described in this document. Before turning the power on, in particular, be sure to finish making all the required connections. Furthermore, be sure to configure and use the product by following the instructions given in this document. Using the product incorrectly or inappropriately may cause a fault.
Damage	Always turn the power off before connecting or disconnecting any cables from the product. When unplugging a cable, unplug the cable by holding the connector part without pulling on the cable itself. Pulling the cable itself or bending it may expose or disconnect the cable core, resulting in a fault.
Damage	Because the product has no casing, it is recommended that it be stored in the original packaging. Transporting the product may cause a damage or fault. Therefore, keep the packaging materials and use them when re-shipping the product.

Contents



1.	Description7				
2.	Evaluation Board Specification	8			
3.	PIN Descriptions	9			
	3.1 Input/OutputDescriptions3.2 JumperDescriptions	9 11			
4.	Setup	12			
	4.1 Connecting4.2 Verification	12 13			
5.	Layout	14			
	5.1 Component Layout5.2 Wiring Layout	14 14			
6.	Circuit Schematic	16			
7.	Component List	17			
8.	Ordering Information	19			
Rev	ision History	20			
	Document Revision History				

1. Description



S6SBP401AJ0SA1001 and S6SBP401AM2SA1001 are the evaluation kit for power block of automotive ADAS platform.

These boards implement S6BP401A of power management IC.

It is necessary to prepare DC power supply.

Figure 1-1. Board Outline



2. Evaluation Board Specification



	Symbol	Min.	Тур.	Max.	Unit	
Input Voltage	VIN	4.5	5.0	5.5	V	
		VOUT1	-	1.250	-	V
		VOUT2	-	1.250	-	V
	SCSRD401A (0SA1001	VOUT3	-	1.850	-	V
	S056F40TAJ05AT00T	VOUT4	-	3.375	-	V
		VOUTL1	-	3.300	-	V
Output Maltage		VOUTL2	-	2.800	-	V
Output Voltage	S6SBP401AM2SA1001	VOUT1	-	1.250	-	V
		VOUT2	-	1.125	-	V
		VOUT3	-	2.550	-	V
		VOUT4	-	3.375	-	V
		VOUTL1	-	3.325	-	V
		VOUTL2	-	2.800	-	V
	S6SBP401AJ0SA1001 S6SBP401AM2SA1001	IOUT1	-	-	2.0	А
Output Current		IOUT2	-	-	3.0	А
		IOUT3	-	-	2.0	А
		IOUT4	-	-	1.0	А
		IOUTL1	-	-	0.2	А
		IOUTL2	-	-	0.5	А

Table 2-1. Evaluation Board Specification

3. PIN Descriptions



3.1 Input/OutputDescriptions

Table 3-1. Input/OutputDescriptions

ConnecterSymbol		I/O	Function Description
CNL VIN		I	+5Vdc power supply terminal
	GND	-	Ground terminal
	VOUT1	0	CH1 DC/DC convertor output terminal
	GND	-	Ground terminal
	VOUT2	0	CH2DC/DC convertor output terminal
CN_VOU12	GND	-	Ground terminal
	VOUT3	0	CH3DC/DC convertor output terminal
CN_VOU13	GND	-	Ground terminal
	VOUT4	0	CH4DC/DC convertor output terminal
CN_VOU14	GND	-	Ground terminal
	VOUTL1	0	CH1 LDO output terminal
CN_VOUIL1	GND	-	Ground terminal
	VOUTL2	0	CH2 LDO output terminal
CN_VOUTL2	GND	-	Ground terminal
	SYNC	I	External clock input terminal. Please use by referring to the data sheet of S6BP401A.
CN1	WDI	I	Trigger input terminal of watchdog timer. Please use by referring to the data sheet of S6BP401A.
	RST	0	Reset output terminal of watchdog timer. Please use by referring to the data sheet of S6BP401A.
	NC	-	-
010	EN1, EN2, EN3, EN4	I	CH1 -CH4 DC/DC convertor enable terminal
CN2	ENL1, ENL2	I	CH1,CH2 LDO enable terminal
	PG1, PG2, PG3, PG4	0	CH1 -CH4 DC/DC convertor power good terminal
CN3	PGL2	0	CH1,CH2 LDO power good terminal
	VCCPullUP	I	Voltage supply terminal for the pull up resistors of the power good



Figure 3-1. Pin Layout





3.2 JumperDescriptions

Table 3-2. Jumper Descriptions

Jumper	Description	Initial Setting
JP1	Shorted between VIN and EN1 JP1Short: EN1 pin is set to the H level. JP1Open: EN1 pin isset to the L level. Input external signal to EN1 pin. Don'tinputexternal signal to EN1 pin with shorted JP1.	Open
JP2	Shorted between VIN and EN2 JP2 Short: EN2 pin is set to the H level. JP2 Open: EN2 pin isset to the L level. Input external signal toEN2pin. Don't inputexternal signal to EN2pin with shorted JP2.	Open
JP3	Shorted between VIN and EN3 JP3 Short: EN3 pin is set to the H level. JP3 Open: EN3 pin isset to the L level. Input external signal toEN3pin. Don't inputexternal signal to EN3pin with shorted JP3.	Open
JP4	Shorted between VIN and EN4 JP4 Short: EN4 pin is set to the H level. JP4 Open: EN4 pin isset to the L level. Input external signal toEN4pin. Don't inputexternal signal toEN4pin with shorted JP4.	Open
JP5	Shorted between VIN and ENL1 JP5 Short: ENL1 pin is set to the H level. JP5 Open: ENL1 pin isset to the L level. Input external signal toENL1 pin. Don't inputexternal signal to ENL1 pin with shorted JP5.	Open
JP6	Shorted between VIN and ENL2 JP6 Short: ENL2 pin is set to the H level. JP6 Open: ENL2 pin isset to the L level. Input external signal toENL2pin. Don't inputexternal signal toENL2pin with shorted JP6.	Open
JP7	Shorted VIN or VOUTL1 to power good pin through resistor. JP7Short: Shorted between VOUTL1andpower good pin through resistor.	Open
JP8	JP8Short: Shorted between VINandpower good pin through resistor. JP7 and JP8 must not be shorted simultaneously.	Short
JP9	Shorted VIN or VOUT4 to PVCCL2 of IC 37 pin. JP9 Short: Shorted between VINandPVCCL2.	Short
JP14	JP14 Short: Shorted between VOUT4andPVCCL2. JP9 and JP14 must not be shorted simultaneously.	Open

4. Setup



4.1 Connecting

Connect the board to the power supply, load equipment and voltage meter as below.
Figure 4-1. Connectingthe Board and Some Equipment





4.2 Verification

- 1. Turn on +5V power supply.
- 2. Connect some enable pin to VIN, then, each DC/DC (or LDO) channel will start output.
- 3. Check that each output voltage is the same as the 'Output Voltage' of Figure 2-1 Evaluation Board Specification.

5. Layout



5.1 Component Layout



Figure 5-1. Evaluation Board Component Layout



Layer4Top View

Board size: 45.72mm ×30.48mm

5.2 Wiring Layout



Figure 5-2. Evaluation Board Wiring Layout





layer 2 Top View layer 4 Top View \bigcirc

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6. Circuit Schematic





Figure 6-1. Evaluation Board Circuit Schematic

7. Component List



No.	Component	Parts Number	Vendor	Value	Rated Voltage(V)	Rated Current(A)	Remarks
1	C1	CGA5L1X7R1C106K160AC	TDK	10µF	16	-	-
2	C2	CGA6P1X7R1C226M250AC	TDK	22µF	16	-	-
3	C3	CGA6P1X7R1C226M250AC	TDK	22µF	16	-	-
4	C6	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
5	C7	CGA5L1X7R1C106K160AC	TDK	10µF	16	-	-
6	C8	CGA6P1X7R1C226M250AC	TDK	22µF	16	-	-
7	C9	CGA6P1X7R1C226M250AC	TDK	22µF	16	-	-
8	C10	CGA6P1X7R1C226M250AC	TDK	22µF	16	-	-
9	C14	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
10	C15	CGA5L1X7R1C106K160AC	TDK	10µF	16	-	-
11	C16	CGA6P1X7R1C226M250AC	TDK	22µF	16	-	-
12	C17	CGA6P1X7R1C226M250AC	TDK	22µF	16	-	-
13	C20	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
14	C21	CGA5L1X7R1C106K160AC	TDK	10µF	16	-	-
15	C22	CGA6P1X7R1C226M250AC	TDK	22µF	16	-	-
16	C23	CGA6P1X7R1C226M250AC	TDK	22µF	16	-	-
17	C26	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
18	C27	CGA3E1X7R1C105M080AC	TDK	1μF	16	-	-
19	C28	CGA3E1X7R1C105M080AC	TDK	1µF	16	-	-
20	C30	CGA3E1X7R1C105M080AC	TDK	1µF	16	-	-
21	C31	CGA5L1X7R1C106K160AC	TDK	10µF	16	-	-
22	C33	CGA3E1X7R1C105M080AC	TDK	1µF	16	-	-
23	C34	CGA3E1X7R1C105M080AC	TDK	1μF	16	-	-
24	C35	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
25	C36	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
26	C37	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
27	C38	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
28	C40	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-

Table 7-1. Evaluation Board Component List



No.	Component	Parts Number	Vendor	Value	Rated Voltage(V)	Rated Current(A)	Remarks
29	C41	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
30	C42	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
31	C43	CGA2B3X7R1H104K050BB	TDK	0.1µF	50	-	-
32	L1	CLF6045T-1R5N-D	TDK	1.5µH	-	4.2	-
33	L2	CLF6045T-1R5N-D	TDK	1.5µH	-	4.2	-
34	L3	CLF6045T-1R5N-D	TDK	1.5µH	-	4.2	-
35	L4	CLF6045T-1R5N-D	TDK	1.5µH	-	4.2	-
		S6BP401AJ0SN1B000	Cypress	-	-	-	S6SBP40 1AJ0SA10 01
30		S6BP401AM2SN1B000					S6SBP40 1AM2SA1 001
37	R5	RG1608P-473-B	SSM	47kΩ	-	-	-
38	R6	RG1608P-473-B	SSM	47 kΩ	-	-	-
39	R7	RG1608P-473-B	SSM	47 kΩ	-	-	-
40	R8	RG1608P-473-B	SSM	47 kΩ	-	-	-
41	R9	RG1608P-473-B	SSM	47 kΩ	-	-	-
42	R10	RG1608P-473-B	SSM	47 kΩ	-	-	-

TDK : TDK Corporation

Cypress : Cypress Semiconductor Corp

SSM : SUSUMU CO., LTD.

NMT : No mount.

These components are compliant with RoHS, and please ask each vendor for details if necessary.

8. Ordering Information



Table 8-1. Ordering Information

Part Number	EVB Revision	Note
S6SBP401AJ0SA1001	S6SBP401A-SA1	Mounted IC: S6BP401AJ0SN1B000
S6SBP401AM2SA1001	Rev 2.0	Mounted IC: S6BP401AM2SN1B000

Revision History



Document Revision History

Document Title: S6SBP401AJ0SA1001, S6SBP401AM2SA100 Automotive Evaluation Board Operation Guide			
Document Number: 002-08764			
Revision	Issue Date	Origin of Change	Description of Change
**	09/11/2015	ΤΑΟΑ	Initial release
*A	02/05/2016	TAOA	Migrated Spansion Guide from S6SBP401AJ0SA1001_SS901-00045-1v0-E to Cypress format