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SC1541 1.0A Low Dropout Voltage Regulator with Enable

POWER MANAGEMENT

Description

The SC1541 is a high performance low dropout positive voltage regulator designed for use in applications where power management is critical, such as battery powered systems.

Additionally, the SC1541 provides excellent regulation over variations in line, load and temperature. Outstanding features include low dropout performance at rated current, fast transient response, internal current limiting and thermal shutdown protection of the output device. A very low quiescent current of $10\mu A$ in shutdown mode reduces power dissipation.

The SC1541 is available with three different voltage options as standard and two package options, SO-8 and 5-lead TO-263.

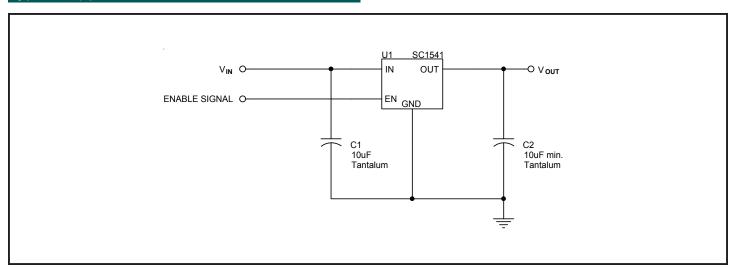
Features

- ◆ Low dropout performance, 1.3V max
- Full current rating over line and temperature
- ◆ Fast transient response
- ±2% total output regulation over line, load and temperature
- ◆ 10µA max. quiescent current in shutdown
- Three fixed output voltages
- Line regulation 0.2% max.
- ◆ Load regulation 0.4% max.
- ◆ S0-8 and T0-263 packages

Applications

- Low voltage microcontrollers
- Switching power supply post-regulation
- Instantly available circuits
- ◆ Motherboards
- Battery operated equipment

Typical Application Circuit



Notes:

- (1) Input and output capacitors should be located close to the device.
- (2) Connect all pins to PCB (SO-8 package) for optimal thermal characteristics.
- (3) Increasing the output capacitor value will improve the overall transient response.
- (4) The device is enabled when $V_{EN} = V_{IN}$, and shut down when EN is pulled to ground. EN should not be taken higher than V_{IN} . A CMOS signal referenced to V_{IN} would be ideal for switching the SC1541 on and off.



Absolute Maximum Ratings

Exceeding the specifications below may result in permanent damage to the device, or device malfunction. Operation outside of the parameters specified in the Electrical Characteristics section is not implied.

Parameter	Symbol	Maximum	Units
Maximum Input Pin Voltage	V _{IN} , V _{EN}	7	V
Power Dissipation	P_{D}	Internally Limited	W
Thermal Resistance Junction to Case SO-8 TO-263	$\theta_{ extsf{JC}}$	39 3	°C/W
Thermal Resistance Junction to Ambient SO-8 ⁽¹⁾ TO-263	θ_{JA}	65 60	°C/W
Operating Junction Temperature Range	T _J	0 to 125	°C
Storage Temperature Range	T _{STG}	-65 to 150	°C
Lead Temperature (Soldering) 10 Sec	T _{LEAD}	300	°C

Note:

(1) 1 inch square of 1/16" FR-4, double sided, 1 oz. minimum copper weight.

Electrical Characteristics

Unless otherwise specified: $I_0 = 0$ mA to 1A, $V_{EN} = V_{IN}$, 1.8V Option: $V_{IN} = 3.2$ V to 7.0V, 2.5 Option: $V_{IN} = 3.9$ V to 7.0V, 3.3V Option: $V_{IN} = 4.7$ V to 7.0V. Values in **bold** apply over full operating temperature range.

Parameter	Symbol		Min	Тур	Max	Units
Output Voltage (1)	V _{out}	V _{IN} = 5V, I _{OUT} = 0mA	-1	V _{OUT}	+1	%
			-2		+2	
Line Regulation ⁽¹⁾	REG _(LINE)	I _{OUT} = 10mA		0.035	0.2	%
Load Regulation ⁽¹⁾	REG _(LOAD)	V _{IN} = 5V		0.2	0.4	%
Dropout Voltage(1)(2)	V _D	I _{OUT} = 1A		1.10	1.30	V
Current Limit ⁽¹⁾	I _{CL}		1.1			Α
Quiescent Current	I _Q	V _{IN} = V _{EN} = 5V		5	7	mA
					10	
		V _{IN} = 5V, V _{EN} < 0.25V		6	8	μΑ
					10	
Enable Pin Control Voltage	V _{EN}	Device OFF	0.25	0.45		V
		Device ON		(V _{OUT} + 0.5)	(V _{OUT} + 0.8)	
Enable Pin Current	I _{EN}	V _{EN} = 0V, V _{IN} = 5V (OFF)		0.1	1.0	μΑ
		$V_{EN} = V_{IN} = 5V (ON)$		15	25	



Electrical Characteristics (Cont.)

Unless otherwise specified: I_0 = 0mA to 1A, V_{EN} = V_{IN} , 1.8V Option: V_{IN} = 3.2V to 7.0V, 2.5 Option: V_{IN} = 3.9V to 7.0V, 3.3V Option: V_{IN} = 4.7V to 7.0V. Values in **bold** apply over full operating temperature range.

Parameter	Symbol		Min	Тур	Max	Units
Temperature Coefficient	T _c			0.005		%/C
Temperature Stability	T _s			0.5		%
RMS Output Noise(3)	V _N			0.003		%V _{OUT}
Ripple Rejection Ratio ⁽⁴⁾	R _A	V _{IN} = 5V	60	72		dB

Notes:

- (1) Low duty cycle pulse testing with Kelvin connections required.
- (2) ΔV_{OUT} , $\Delta V_{REF} = 1\%$. (3) Bandwidth of 10 Hz to 10kHz.
- (4) 120Hz input ripple.
- (5) This device is ESD sensitive. Use of standard ESD handling precautions is required.

IN

OUT

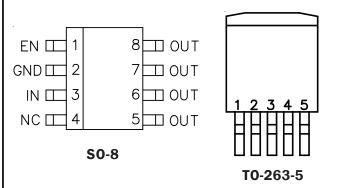
 v_{REF}

GND



POWER MANAGEMENT

Pin Configurations



PIN	FUNCTION
1	EN
2	GND
3/TAB	OUT
4	IN
5	NC

Ordering Information

Device ⁽¹⁾⁽²⁾	Package
SC1541CM-X.X.TR	TO 202 F
SC1541CM-X.X.TRT ⁽³⁾	TO-263-5
SC1541CS-X.X.TR	00.0
SC1541CS-X.X.TRT ⁽³⁾	SO-8

Notes:

Block Diagram

- (1) Where X.X denotes voltage options. Available voltages are: 1.8V, 2.5V and 3.3V. Contact factory for additional voltage options.
- (2) Only available in tape and reel packaging. A reel contains 800 (T0-263-5) or 2500 (S0-8) devices.
- (3) Halogen Free, and RoHS/WEEE compliant.

CURRENT LIMIT S.O.A. LIMITING AMPLIFIER

THERMAL OVERLOAD

VOLTAGE REGULATION AMPLIFIER SENSE

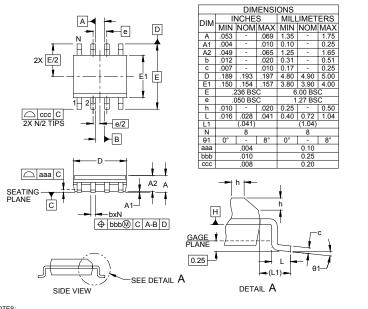
Pin Descriptions

Pin Name	SO-8 Pin #	TO-263-5 Pin #	Pin Function
EN	1	1	Active high enable for the regulator output. Connect to $V_{\mathbb{N}}$ if not being used.
GND	2	2	Ground pin. Bottom end of the internal feedback resistor chain.
NC	4	5	No connection.
IN	3	4	This is the input supply pin for the device.
OUT	5, 6, 7, 8	3/ТАВ	This is the power output of the device. This pin is electrically connected to the tab of the TO-263-5 package.

ΕN



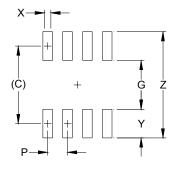
Outline Drawing - SO-8



NOTES:

- CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. DATUMS -A- AND -B- TO BE DETERMINED AT DATUM PLANE -H-
- 3. DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- 4. REFERENCE JEDEC STD MS-012, VARIATION AA

Land Pattern - SO-8(1)



DIMENSIONS				
DIM	INCHES	MILLIMETERS		
С	(.205)	(5.20)		
G	.118	3.00		
Р	.050	1.27		
X	.024	0.60		
Υ	.087	2.20		
Z	.291	7.40		

NOTES:

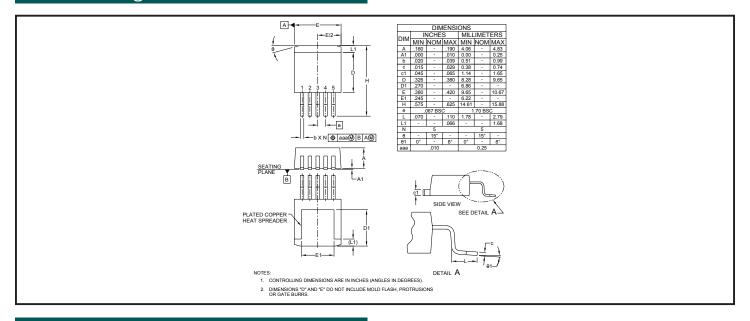
- THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.
- 2. REFERENCE IPC-SM-782A, RLP NO. 300A.

Note:

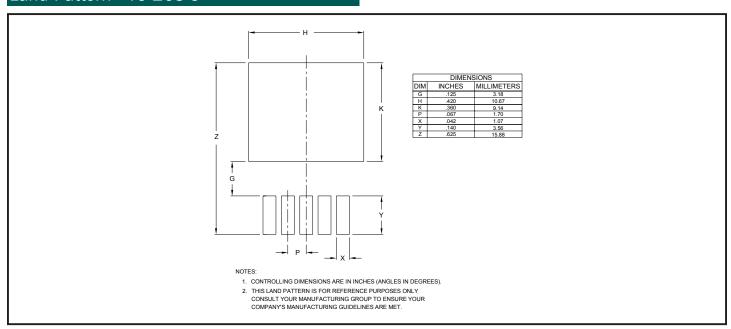
(1) More copper area should be used to lower $\boldsymbol{R}_{\text{TH}(J-\boldsymbol{A})}$ as needed.



Outline Drawing - TO-263-5



Land Pattern - TO-263-5(1)



Note:

(1) More copper area should be used to lower $R_{\mbox{\tiny TH(J-A)}}$ as needed.

Contact Information

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