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SD1274 RF POWER BIPOLAR TRANSISTORS VHF MOBILE APPLICATIONS

FEATURES SUMMARY

- 160 MHz
- 13.6 VOLTS
- COMMON EMITTER
- P_{OUT} = 30 W MIN. WITH 10 dB GAIN

obsolete Productls

DESCRIPTION

The SD1274 is a 13.6 V Class C epitaxial silicon NPN planar transistor designed primarily for VHF communications. The SD1274 utilizes an emitter ballasted die geometry to withstand severe load mismatch conditions.

Figure 1. Package

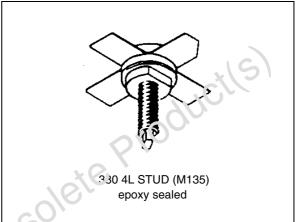


Figure 2. Pin Connection

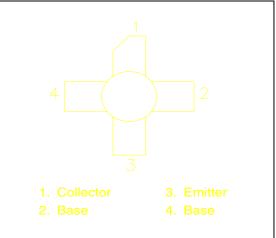


Table 1. Order Codes

Order Codes	Marking	Package	Packaging
SD1274	SD1274	M135	BLACK CARDBOARDS

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	36	V
V _{CEO}	Collector-Emitter Voltage	16	V
V _{CES}	Collector-Emitter Voltage	36	V
V _{EBO}	Emitter-Base Voltage	4.0	V
IC	Device Current	8.0	A
P _{DISS}	Power Dissipation	70	W
TJ	Junction Temperature	+200	°C
T _{STG}	Storage Temperature	- 65 to +150	S°¢

Table 2. Absolute Maximum Ratings (Tcase = 25°C)

Table 3. Thermal Data

Symbol	Parameter	Ve lue	Unit
R _{TH(j-c)}	Junction-Case Thermal Resistance	1.2	°C/W
		40	
		161	
	. SPECIFICATIONS ($T_{CASE} = 25^{\circ}C$)		

ELECTRICAL SPECIFICATIONS (T_{CASE} = 25°C)

Table 4. Static

Symbol	Test Conditions	Value			Unit
Symbol		Min.	Тур.	Max.	Unit
BV _{CES}	$I_{C} = 15 \text{ mA}; V_{BE} = 0 \text{ mA}$	36	_	_	V
BV _{CEO}	$I_{C} = 50 \text{ mA}; I_{B} = 0 \text{ mA}$	16	_		V
BV _{EBO}	$I_{E} = 5 \text{ m/}; I_{C} = 0 \text{ mA}$	4.0	_	_	V
I _{CBO}	V _{CB} = 15 V; I _E = 0 mA	—	_	5	mA
h _{FE}	$\int \Psi_{\rm CE} = 5 \text{ V}; I_{\rm C} = 250 \text{ mA}$	20		_	-

Table 5. Dynamic

Symbol	Test Conditions		Unit		
Symbol		Min.	Тур.	Max.	Onit
Pout	f = 160 MHz; P _{IN} = 3.0 W; V _{CE} = 13.6 V	30	_	_	W
GP	f = 160 MHz; P _{IN} = 3.0 W; V _{CE} = 13.6 V	10	_	_	dB
C _{OB}	f = 1 MHz; V _{CB} = 15 V	—	95	—	pF

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TYPICAL PERFORMANCE

Figure 3. Power Output vs Supply Voltage (136 MHz)

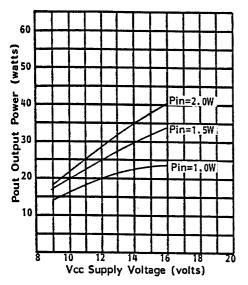


Figure 5. Power Output vs Supply Voltage (175 MHz)

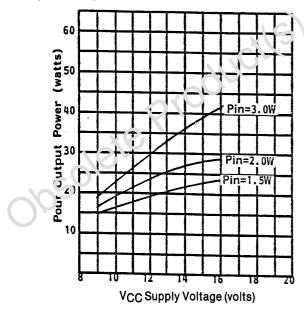
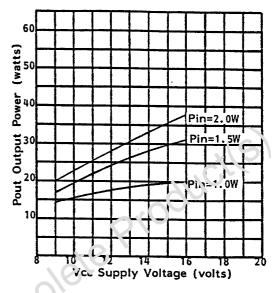


Figure 4. Power Output vs Supply Voltage (150 MHz)



Ficure 6. Power Gain vs Frequency

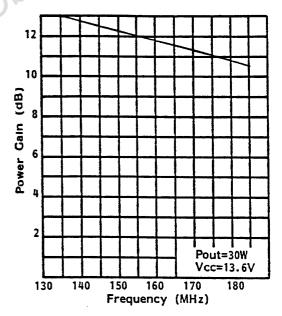


Figure 7. Power Output vs Power Input

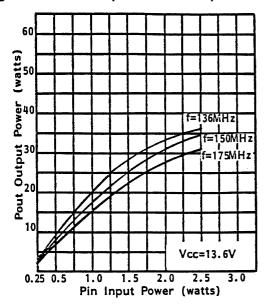


Table 6. Impedance Data (1)

Pin Input Power ([.] Fable 6. Impedance Data ⁽¹⁾	watts)	Productle
FREQ.	$\frac{\mathbf{Z}_{IN}(\Omega)}{1.0 + j 0.4\epsilon}$	Ζ_{CL} (Ω)
osolete Produ	CILL	

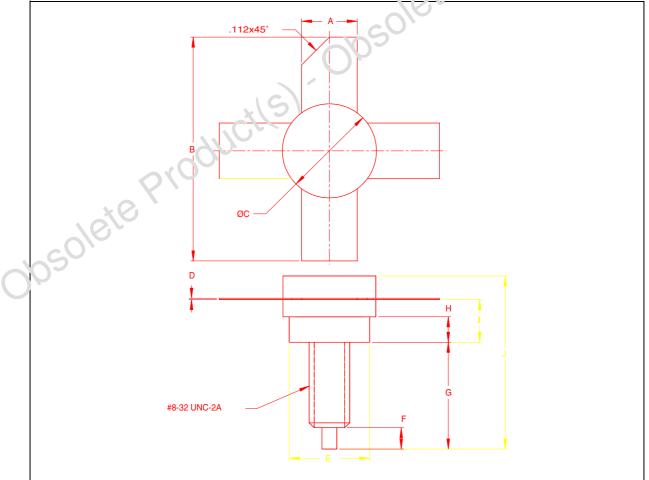


PACKAGE MECHANICAL

Symbol	millimeters			inches			
Symbol	Min	Тур	Max	Min	Тур	Мах	
A	5.59		5.84	0.220		0.230	
В	24.89			0.980			
С	9.40		9.78	0.370		0.385	
D	0.10		0.18	0.004		0.007	
E	8.13		8.38	0.320		0.330	
F	2.54		3.30	0.100		0.136	
G	11.43		12.45	0.450		0.490	
Н	2.29		2.54	0.090	21)	0.100	
I	3.94		4.45	0.155		0.175	
J			19.05			0.750	

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Note: Drawing is not to scale.

REVISION HISTORY

Table 8. Revision History

Date	Revision	Description of Changes
June-1993	1	First Issue
7-May-2004	2	Stylesheet update. No content change.

obsolete Product(s). Obsolete Product(s)

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