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SD1275

RF POWER BIPOLAR TRANSISTORS VHF MOBILE APPLICATIONS

FEATURES SUMMARY

- 160 MHz
- 13.6 VOLTS
- COMMON EMITTER
- P_{OUT} = 40 W MIN. WITH 9 dB GAIN

DESCRIPTION

The SD1275 is a 13.6 V Class C epitaxial silicon NPN planar transistor designed primarily for VHF communications. The SD1275 utilizes an emitter ballasted die geometry to withstand severe load mismatch conditions.

Figure 1. Package

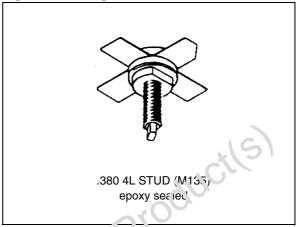


Figure 2. Pir Cornection

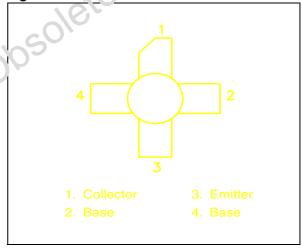


Table 1. Order Codes

Order Codes	Marking	Package	Packaging
SD1275	SD1275	M135	BLACK CARDBOARDS

REV. 2 May 2004

Table 2. Absolute Maximum Ratings (T_{case} = 25°C)

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	36	V
V _{CEO}	Collector-Emitter Voltage	16	V
V _{CES}	Collector-Emitter Voltage	36	V
V _{EBO}	Emitter-Base Voltage	4.0	V
Ic	Device Current	8.0	А
P _{DISS}	Power Dissipation	70	W
TJ	Junction Temperature	+200	°C
T _{STG}	Storage Temperature	- 65 to +150	°C

Table 3. Thermal Data

Symbol	Parameter	Value	Unit
R _{TH(j-c)}	Junction-Case Thermal Resistance	1.2	°C/W
ELECTRICAL Table 4. Static	SPECIFICATIONS (T _{CASE} = 25°C)	e Produc.	
Symbol	Test Conditions	Value	Unit

ELECTRICAL SPECIFICATIONS (TCASE = 25°C)

Table 4. Static

Symbol	Test Conditions	Value			Unit
Symbol	rest Conditions		Тур.	Max.	Oilit
BV _{CES}	I _C = 15 mA; V _{BE} = 0 mA	36		_	V
BV _{CEO}	$I_C = 50 \text{ mA}; I_B = 0 \text{ mA}$	16		_	V
BV _{EBO}	$I_E = 5 \text{ mA}; I_C = 0 \text{ mA}$	4.0	_	_	V
I _{CBO}	V _{CB} = 15 V; I _E = 0 mA	_	_	5	mA
h _{FE}	V _{CE} = 5 V; I _C = 250 mA	20	_	_	_

Table 5. Dynamic

Symbol	Test Conditions	Value			Unit	
Symbol	rest conditions	Min.	Тур.	Max.	O.III	
Роит	f = 160 MHz; P _{IN} = 5.0 W; V _{CE} = 13.6 V	40	_	_	W	
Gp	f = 160 MHz; P _{IN} = 5.0 W; V _{CE} = 13.6 V	9	_	_	dB	
СОВ	f = 1 MHz; V _{CB} = 15 V	_	95	_	pF	

TYPICAL PERFORMANCE

Figure 3. Power Gain vs Frequency

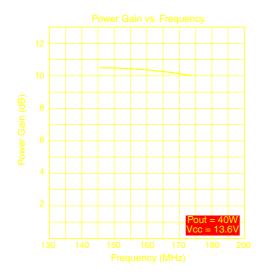


Figure 4. Power Output vs Power Input

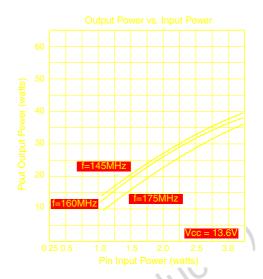


Figure 5. Power Output vs Supply Voltage (175 MHz)

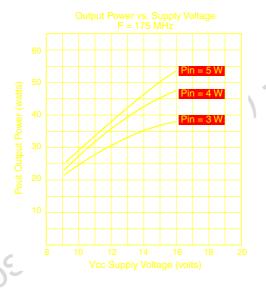


Figure 6. Power Output vs Supply Voltage (145 MHz)

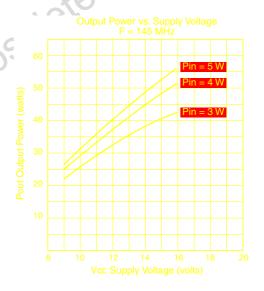


Figure 7. Power Output vs Supply Voltage (160 MHz)

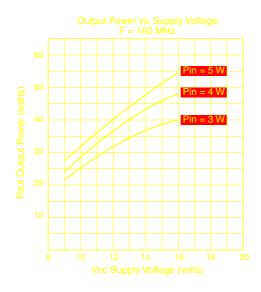


Table 6. Impedance Data (1)

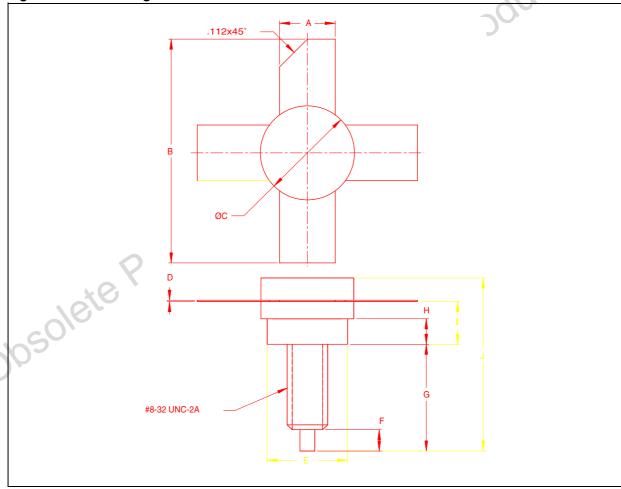
Vcc Supply Voltage (v Table 6. Impedance Data (1)		oroducile
FREQ.	Z _{IN} (Ω)	Z _{CL} (Ω)
160 MHz	1.0 + j 0.4	2.3 + j 0.1
obsoleite Produ	ci(s)	

PACKAGE MECHANICAL

Table 7. M135 Mechanical Data

Symbol		millimeters		inches		
Symbol	Min	Тур	Max	Min	Тур	Max
Α	5.59		5.84	0.220		0.230
В	24.89			0.980		
С	9.40		9.78	0.370		0.385
D	0.10		0.18	0.004		0.007
E	8.13		8.38	0.320		0.330
F	2.54		3.30	0.100		0.130
G	11.43		12.45	0.450		0.490
Н	2.29		2.54	0.090		0.100
ı	3.94		4.45	0.155		0.175
J			19.05			0.750

Figure 8. M135 Package Dimensions



Note: Drawing is not to scale.

REVISION HISTORY

Table 8. Revision History

Date	Revision	Description of Changes
June-1993	1	First Issue
25-May-2004	2	Stylesheet update. No content change.

Obsolete Product(s). Obsolete Product(s)

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