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SD5620/5630 Optoschmitt Detector

FEATURES

- TO-46 metal can package
- 6° (nominal) acceptance angle
- TTL/LSTTL/CMOS compatible
- High noise immunity output
- Buffer (SD5620) or inverting (SD5630) logic available
- Two sensitivity ranges
- Mechanically and spectrally matched to SE3450/5450, SE3455/5455 and SE3470/5470 infrared emitting diodes

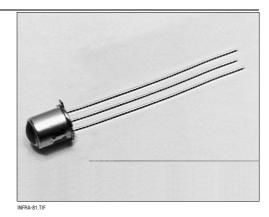
DESCRIPTION

The SD5620/5630 series is family of single chip Optoschmitt IC detectors mounted in a TO-46 metal can package. The photodetector consists of a photodiode, amplifier, voltage regulator, Schmitt trigger and an NPN output transistor with a 10 k Ω (nominal) pull-up resistor. Output rise and fall times are independent of rate of change of incident light. Detector sensitivity has been internally temperature compensated. The TO-46 package is ideally suited for operation in hostile environments.

Device Polarity:

Buffer - Output is HI when incident light intensity is above the turn- on threshold level.

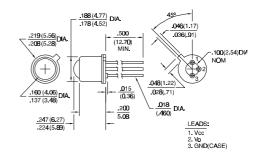
Inverter - Output is LO when incident light intensity is above the turn- on threshold level.



OUTLINE DIMENSIONS in inches (mm)

Tolerance

3 plc decimals ±0.005(0.12) 2 plc decimals ±0.020(0.51)



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SD5620/5630

Optoschmitt Detector

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Operating Supply Voltage	Vcc	4.5		16.0	V	T₄=25°C
Turn-on Threshold Irradiance (2)	Eet(+)				mW/cm ²	Vcc=5 V
SD5620-001, SD5630-001				0.25		T _A =25°C
SD5620-002, SD5630-002				0.13		
Hysteresis (3)	HYST	5		30	%	
Supply Current	lcc				mA	Ee=0 Or 3.0 mW/cm ²
				12.0		Vcc=5 V
				15.0		Vcc=16 V
High Level Output Voltage	Vон				V	Vcc=5 V, Іон=0
SD5620		2.4				Ee=3.0 mW/cm ²
SD5630		2.4				Ee=0
Low Level Output Voltage	Vol				V	Vcc=5 V, IoL=12.8 mA
SD5620				0.4		Ee=0
SD5630				0.4		Ee=3.0 mW/cm ²
Internal Pull-Up Resistor	RINT	5.0	10.0	20.0	kΩ	
Operate Point Temperature Coefficient	Ортс		-0.76		%/°C	Emitter @ Constant
						Temperature
Output Rise Time	tr		60		ns	RL=390 Ω, CL=50 pF
Output Fall Time	tf		15		ns	RL=390 Ω, CL=50 pF
Propagation Delay, Low-High, High-Low	t _{PLH} , t _{PHL}		5.0		μs	RL=390 Ω, CL=50 pF
Clock Frequency				100	kHz	RL=390 Ω, CL=50 pF

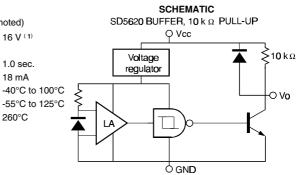
Notes
It is recommended that a bypass capacitor, 0.1 µF typical, be added between V_{CC} and GND near the device in order to stabilize power supply line.
The radiation source is an IRED with a peak wavelength of 935 nm.
Hysteresis is defined as the difference between the operating and release threshold intensities, expressed as a percentage of the

operate threshold intensity.

ABSOLUTE MAXIMUM RATINGS

(25°C Free-Air Temperature unless otherwise noted)

Supply Voltage Duration of Output Short to Vcc or Ground Output Current Operating Temperature Range Storage Temperature Range Soldering Temperature (10 sec) Notes 1. Derate linearly from 25°C to 7 V at 100°C.



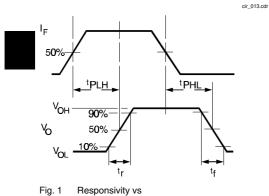
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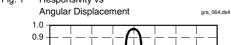
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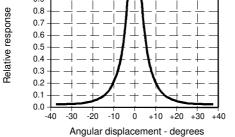
SD5620/5630 Optoschmitt Detector

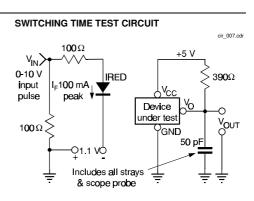
SCHEMATIC SD5630 INVERTER, 10 k Ω PULL-UP Votage regulator Votage regulator Votage regulator Votage

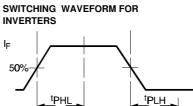
SWITCHING WAVEFORM FOR BUFFERS



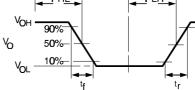


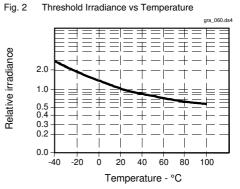






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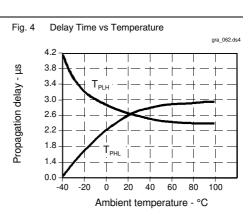
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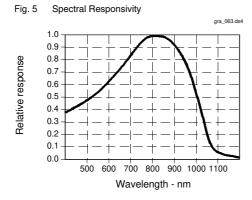
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Fig. 3 Output Rise Time (tr) and Output Fall Time (t_f) vs Temperature gra_061.ds4 240 220 Rise time ($T_{\rm R}$) & fall time ($T_{\rm F}$) - ns Symbol definition & test 200 circuit per figures 180 160 140 120 100 Rise time T 80 60 40 Fall time T 20 4 ò 20 40 60 -40 -20 80 100 Temperature - °C





All Performance Curves Show Typical Values

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