imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





Glass Passivated Super Fast Rectifiers

FEATURES

- Glass passivated chip junction
- High current capability, Low VF
- High reliability
- High surge current capability
- Low power loss
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

MECHANICAL DATA

Case: DO-201AD

Molding compound, UL flammability classification rating 94V-0 Base P/N with suffix "G" on packing code - green compound (halogen-free) Base P/N with prefix "H" on packing code - AEC-Q101 qualified Terminal: Matte tin plated leads, solderable per JESD22-B102 Meet JESD 201 class 1A whisker test with prefix "H" on packing code meet JESD 201 class 2 whisker test Weight: 1.1 g (approximately)



DO-201AD



	SYMBOL	SF SF		SF	SF	SF	SF	SF	SF	
PARAMETER		31G	32G	33G	34G	35G	36G	37G	38G	UNIT
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	150	200	300	400	500	600	V
Maximum RMS voltage	V _{RMS}	35	70	105	140	210	280	350	420	V
Maximum DC blocking voltage	V _{DC}	50	100	150	200	300	400	500	600	V
Maximum average forward rectified current	I _{F(AV)}				:	3				А
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	125					A			
Maximum instantaneous forward voltage (Note 1) @ 3 A	V _F	0.95 1.3 1.7		.7	V					
Maximum reverse current @ rated VR $T_J=25 \degree C$ $T_J=125 \degree C$	I _R	5 100					μA			
Maximum reverse recovery time (Note 2)	Trr				3				ns	
Typical junction capacitance (Note 3)	Cj		8	0		35 60			pF	
Typical thermal resistance	R _{θjC} R _{θjL} R _{θjA}	9 10 35				^o C/W				
Operating junction temperature range	TJ	- 55 to +150				°C				
Storage temperature range	T _{STG}					°C				

Note 1: Pulse Test with PW=300µs, 1% Duty Cycle

Note 2: Reverse Recovery Test Conditions: I_F =0.5A, I_R =1.0A, I_{RR} =0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.



SF31G thru SF38G

Taiwan Semiconductor

ORDERING INFORMATION

PART NO.	AEC-Q101	PACKING	GREEN COMPOUND	PACKAGE	PACKING	
	QUALIFIED	CODE	CODE			
SF3xG (Note 1)	Prefix "H"	A0	Suffix "G"	DO-201AD	500 / Ammo box	
		R0		DO-201AD	1,250 / 13" Paper reel	
		B0		DO-201AD	500 / Bulk packing	
		X0		DO-201AD	Forming	

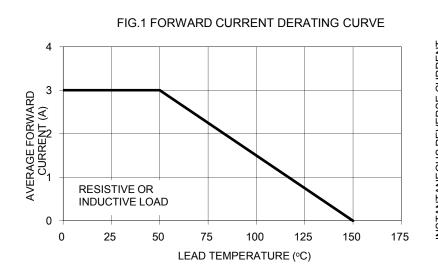
Note 1: "x" defines voltage from 50V (SF31G) to 600V (SF38G)

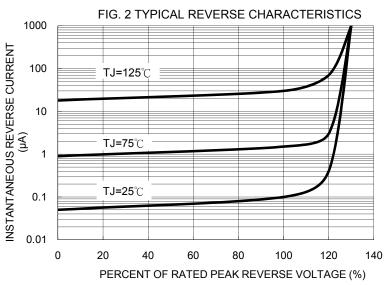
EXAMPLE

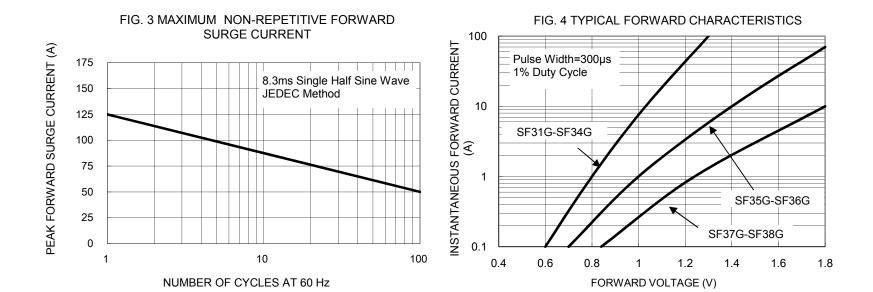
PREFERRED P/N	PART NO.	AEC-Q101 QUALIFIED	PACKING CODE	GREEN COMPOUND CODE	DESCRIPTION		
SF38G A0	SF38G		A0				
SF38G A0G	SF38G		A0	G	Green compound		
SF38GHA0	SF38G	Н	A0		AEC-Q101 qualified		

RATINGS AND CHARACTERISTICS CURVES

(TA=25 $^{\circ}$ C unless otherwise noted)





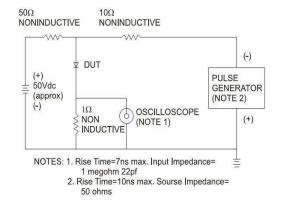


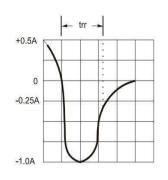


140 120 (1) 100 (1) 100 (2) 10

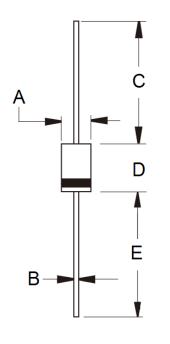
FIG. 5 TYPICAL JUNCTION CAPACITANCE

FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)			
	Min	Max	Min	Max		
А	5.00	5.60	0.197	0.220		
В	1.20	1.30	0.048	0.052		
С	25.40	-	1.000	-		
D	8.50	9.50	0.335	0.375		
E	25.40	-	1.000	-		

MARKING DIAGRAM



- P/N = Specific Device Code
- G = Green Compound
- YWW = Date Code
- F = Factory Code



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