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8A, 50V - 600V Surface Mount Super Fast Rectifiers

FEATURES

- Glass passivated junction chip
- Ideal for automated placement
- High efficiency, low VF
- High surge current capability
- Low power loss
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21



TO-263AB (D²PAK)

- 55 to +150

- 55 to +150





MECHANICAL DATA

Case: TO-263AB (D²PAK)

Molding compound, UL flammability classification rating 94V-0

Moisture sensitivity level: level 1, per J-STD-020 Part no. with suffix "H" means AEC-Q101 qualified

Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 2 whisker test

Polarity: As marked

Weight: 1.33 g (approximately)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°C unless otherwise noted)										
PARAMETER	SYMBOL	SFAS	SFAS	SFAS	SFAS	SFAS	SFAS	SFAS	SFAS	UNIT
PANAMEIEN		801G	802G	803G	804G	805G	806G	807G	808G	
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	150	200	300	400	500	600	V
Maximum RMS voltage	V_{RMS}	35	70	105	140	210	280	350	420	V
Maximum DC blocking voltage	V_{DC}	50	100	150	200	300	400	500	600	V
Maximum average forward rectified current	I _{F(AV)}	8					Α			
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	125				Α				
Maximum instantaneous forward voltage I _F = 8 A	V _F		0.	95		1	.3	1	.7	V
Maximum reverse current @ rated V_R $T_J=25^{\circ}C$ $T_J=100^{\circ}C$	I _R	10 400			μΑ					
Maximum reverse recovery time (Note 1)	mum reverse recovery time (Note 1) t _{rr}		35						ns	
Typical junction capacitance (Note 2)	CJ	80 60					pF			
pical thermal resistance R _{θJC}		2.2						°C/W		

 T_J

Note 1: Reverse Recovery Test Conditions: I_F =0.5A, I_R =1.0A, I_{RR} =0.25A

Note 2: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.

Operating junction temperature range

Storage temperature range

°C





ORDERING INFORMATION					
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX ^(*)	PACKAGE	PACKING
SFAS80xG	Н	RN	G	D ² PAK	800 / 13" Paper reel
(Note 1)	11	MN	G	DPAK	800 / 13" Plastic reel

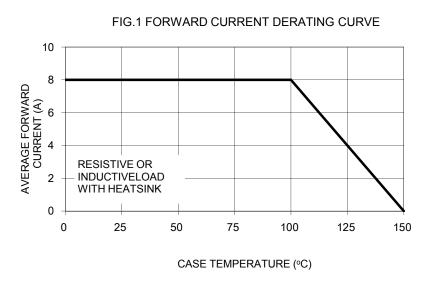
Note 1: "x" defines voltage from 50V (SFAS801G) to 600V (SFAS808G)

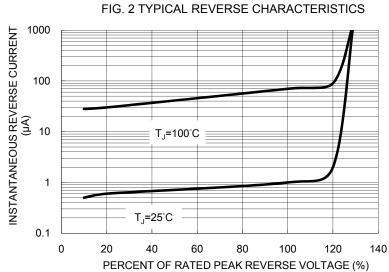
^{*:} Optional available

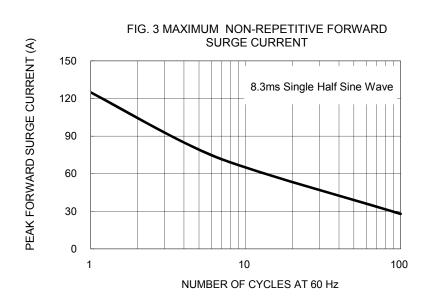
EXAMPLE						
PREFERRED P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION	
SFAS801GHRNG	SFAS801G	Н	RN	G	AEC-Q101 qualified Green compound	

RATINGS AND CHARACTERISTICS CURVES

(TA=25°C unless otherwise noted)







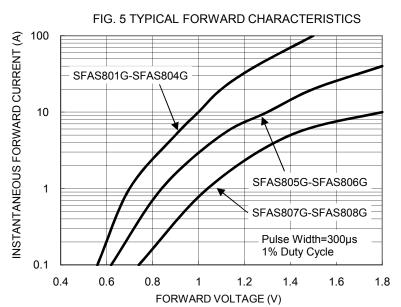
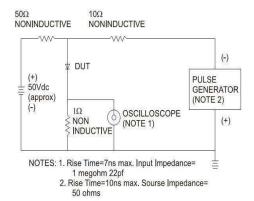
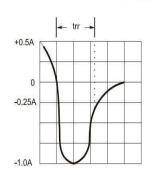




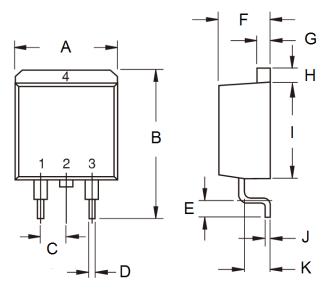
FIG. 5 TYPICAL JUNCTION CAPACITANCE 100 90 (a) F=1.0MHz Vsig=50mVp-p Vsig=50mVp-p 50 SFAS801G-SFAS804G 1 10 100 1000 REVERSE VOLTAGE (V)

FIG.6 REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



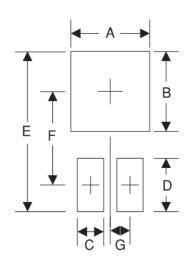


PACKAGE OUTLINE DIMENSIONS TO-263AB (D²PAK)



DIM.	Unit	(mm)	Unit (inch)		
DIIVI.	Min Max		Min	Max	
Α	-	10.5	-	0.413	
В	14.60	15.88	0.575	0.625	
С	2.41	2.67	0.095	0.105	
D	0.68	0.94	0.027	0.037	
Е	2.29	2.79	0.090	0.110	
F	4.44	4.70	0.175	0.185	
G	1.14	1.40	0.045	0.055	
Н	1.14	1.40	0.045	0.055	
I	8.25	9.25	0.325	0.364	
J	0.36	0.53	0.014	0.021	
K	2.03	2.79	0.080	0.110	

SUGGESTED PAD LAYOUT

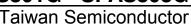


Symbol	Unit (mm)	Unit (inch)
Α	10.8	0.425
В	8.3	0.327
С	1.1	0.043
D	3.5	0.138
E	16.9	0.665
F	9.5	0.374
G	2.5	0.098

MARKING DIAGRAM



P/N = Specific Device Code
G = Green Compound
YWW = Date Code
F = Factory Code





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