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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







CRYSTAL OSCILLATOR (Programmable)

OUTPUT: CMOS

SG-8018 series

• Frequency range: 0.67 MHz ~ 170 MHz (1 ppm Step)

• Supply voltage : 1.62 V ~ 3.63 V

Function : Output enable (OE) or Standby (ST)
 Frequency tolerance : ±50 ppm (-40 °C ~ +105 °C)
 Include frequency aging(+25 °C, 10 years)

• Package : 2.5 x 2.0, 3.2 x 2.5, 5.0 x 3.2, 7.0 x 5.0 (mm)

• PLL technology to enable short lead time

• Available field oscillator programmer "SG-Writer II"





Product Number (please contact us) SG-8018CA: X1G005571xxxx00 SG-8018CB: X1G005581xxxx00 SG-8018CE: X1G005591xxxx00 SG-8018CG: X1G005601xxxx00









CG CE CB CA 2.5 x 2.0mm 3.2 x 2.5mm 5.0 x 3.2mm 7.0 x 5.0mm

Specifications (characteristics)

VoH 90 % V _{CC} Min.	Specifications (characteristics)										
Supply Voltage	Item Symbol		Specifications				Conditions/Remarks				
Output frequency range 6			Voc						_		
Storage temperature			VCC	1.62 V ~ 1.98 V	1.98 V ~ 2.20 V	2.20 V ~ 2.80 V	2.70 V ~ 3.63 V				
Comparing temperature T_use -40 °C ~ +105 °C T_use = -40 °C ~ +105 °C Frequency tolerance T_tole 3.2 mA Max. 3.3 mA Max. 3.4 mA Max. 3.5 mA Max. T_use = +105 °C Courrent consumption Ico 2.7 mA Typ. 2.9 mA Typ. 3.0 mA Typ. T_use = +25 °C Courrent consumption Ico 2.7 mA Typ. 5.7 mA Typ. 6.8 mA Max. T_use = +105 °C Courrent consumption Ico 3.2 mA Max. 3.2 mA Max. 3.3 mA Max. 3.5 mA Max. T_use = +105 °C Courrent consumption Ico 3.2 mA Max. 3.2 mA Max. 3.3 mA Max. 3.5 mA Max. 3.5 mA Max. 0.6 = GND, to = 170 MHz Courrent I_dis 3.2 mA Max. 3.2 mA Max. 3.3 mA Max. 3.5 mA Max. 0.6 = GND, to = 170 MHz Standby current I_stal 0.9 μA Max. 1.0 μA Max. 1.5 μA Max. 2.5 μA Max. 1.2 μes = +105 °C Symmetry SYM 45 % ~ 55 % 50 % V _{Co} Level VoH 90 % V _{Co} Min. 90 % V _{Co} Max. 90 % V	1 , 0			0.67 MHz ~ 170 MHz							
Frequency tolerance T							Storage as single product.				
Current consumption			T_use					•			
Current consumption Current consumption Current consumption Current consumption Current consumption Current Cur	Frequency toler	rance ^{*1}	f_tol	J: ±50 × 10 ⁻⁶				$T_use = -40 {}^{\circ}C \sim +105 {}^{\circ}C$			
Current consumption Icc 2.7 mA Ivp. 2.9 mA Ivp. 3.0 mA Ivp. 1_use = ±25 °C No load, f _o = 170 MHz								_	No load fo = 20 MHz		
S. S mA Max. S. S mA Max. S. S mA Max. S. Ma Max. S. Max. S. T mA Max. S. T mA Max. S. T mA Max. S. T mA Max. T_use = +10 ° °C	Current consum	ntion	laa	1 7.2	mA Typ.	2.9 mA Typ.	3.0 mA Typ.	T_use = +25 °C	140 1044, 10 = 20 1411 12		
A-7m yp. 5.7m yp. 6.8 m yp. 1_use = +25 °C	Current consum	iption	100	5.5 mA Max.	5.8 mA Max.	6.7 mA Max.	8.1 mA Max.	_	No load fo = 170 MHz		
Standby current L_std 0.9 μA Max. 1.0 μA Max. 1.5 μA Max. 2.5 μA Max. T_use = +105 °C ST = GND				1.71	mA Typ.		6.8 mA Typ.	T_use = +25 °C	170 10112		
Standby current	Output disable	current	l_dis	3.2 mA Max.	3.2 mA Max.	3.3 mA Max.	3.5 mA Max.	OE = GND, f _O = 170 MHz			
Symmetry SYM 45 % ~ 55 % 50 % V _{CC} Level	Standby curren	+	Letd	0.9 μA Max.	1.0 µA Max.	1.5 µA Max.	2.5 µA Max.	_	ST - GND		
VoH 90 % Vcc Min.	Starioby current		1_510	0.3 μA Typ.	0.4 μA Typ.	0.5 μA Typ.	1.1 μA Typ.	T_use = +25 °C	ST = GIVD		
Output voltage (DC characteristics) Vol. 10 % V _{CC} Max. Max. Personance of the processes of the proceses of the processes of the processes of the processes of the proc	Symmetry		SYM		45 %	~ 55 %					
Output voltage (DC characteristics) Vol. 90 % V _{CC} Min. Default (fo > 40 MHz), feat fo 4.2.5 3.5 4.0 5.1 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 3.5 4.0 5.1 5.2.0 2.5 5.3 5.2.0 2.5									[mA]		
Output voltage (DC characteristics) Vol. 10 % V _{CC} Max. Vol. 10 % V _{CC} Min. Vol. 10 % V _{CC} Min. Vol. 70 % V _{CC} Min. OE or ST Rise and Fall time Test 30 % V _{CC} Max. 0E or ST 0E or ST 20 % - 80 % V _{CC} . L_CMOS = 15 pF Rise and Fall time Test 3.0 ns Max. f ₀ > 40 MHz 20 % - 80 % V _{CC} . L_CMOS = 15 pF Disable Time t_stp 1 μs Max. Measured from the time OE or ST pin crosses 30 % V _{CC} . L_CMOS = 15 pF Enable Time t_sta 1 μs Max. Measured from the time OE pin crosses 70 % V _{CC} . Starture time t_str 3 ms Max. Measured from the time ST pin crosses 70 % V _{CC} .			.,	90 % V _{CC} Min.							
Output voltage (DC characteristics) VoL 10 % V _{CC} Max. Default (fo ≤ 40 MHz) IoH (-1.5) -2.0 -2.5 -3.1 (loc 1.5) 2.0 -2.5 3.4 (loc 1.0) 1.5 -2.0 2.5 3.4 (loc			VOH								
Default (To ≤ 40 MHz)	Output voltage								I15 -20 -25 -30		
VoL 10 % V _{CC} Max. Slow Iol 1.0 1.5 2.0 2.1 *A: 1.62 V ~ 1.98 V, *B: 1.98 V ~ 2.20 V ° C: 2.20 V ~ 2.80 V, *D: 2.70 V ~ 3.63 V ° C: 2.20 V ~ 2.80 V, *D: 2.70 V ~ 2.80 V ~ 2.80 V ~ 2.80 V ~ 2		tics)					Default (f _O ≤ 40 MHz)	***			
VoL 10 % Voc Max.			V _{OL}					Slow			
Output load condition L_CMOS Input voltage V_{IH} V_{IL}				10 % V _{CC} Max.			I _{OL} 1.0 1.5 2.0 2.5				
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$											
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Output load cor	ndition	L CMOS	15 pF Max.			0. 2.20	-			
Input voltage $ V_{IL} $	'		V _{IH}								
Rise and Fall time Default Fast Slow Default Default Fast Slow Default Default Fast Slow Default Default Default Fast Slow Default	Input voltage						OE or ST				
Rise and Fall time Default			- 12					f ₀ > 40 MHz			
time Fast 3.0 ns Max. $f_0 = 0.67 \text{ MHz} \sim 170 \text{ MHz}$ $L_CMOS = 15 \text{ pF}$ Slow 10.0 ns Max. $f_0 = 0.67 \text{ MHz} \sim 20 \text{ MHz}$ $CMOS = 15 \text{ pF}$ Disable Time $f_0 = 0.67 \text{ MHz} \sim 20 \text{ MHz}$ L_CMOS = 15 pF $f_0 = 0.67 \text{ MHz} \sim 20 \text{ MHz}$ Measured from the time OE or ST pin crosses 30 V_{CC} Enable Time $f_0 = 0.67 \text{ MHz} \sim 100 \text{ MHz}$ L_CMOS = 15 pF $f_0 = 0.67 \text{ MHz} \sim 100 \text{ MHz}$ Measured from the time OE or ST pin crosses 30 V_{CC} Resume Time $f_0 = 0.67 \text{ MHz} \sim 100 \text{ MHz}$ L_CMOS = 15 pF Measured from the time OE or ST pin crosses 30 V_{CC} Resume Time $f_0 = 0.67 \text{ MHz} \sim 100 \text{ MHz}$ Measured from the time OE pin crosses 70 % V_{CC} Start-up time $f_0 = 0.67 \text{ MHz} \sim 100 \text{ MHz}$ Measured from the time V _{CC} reaches its rated	Rise and Fall	Default									
Disable Time t_stp 1		Fast	tr/tf		3.0	ns Max.					
Enable Time t_sta 1 μ s Max. μ cc Enable Time t_sta 1 μ s Max. Measured from the time OE pin crosses 70 % ν cc Resume Time t_res 3 μ s Max. Measured from the time ST pin crosses 70 % ν cc Start-up time t_str 3 μ s Max. Measured from the time ν cc reaches its rated		Slow						f _O = 0.67 MHz ~ 20	MHz		
Resume Time t_res 3 ms Max. Measured from the time ST pin crosses 70 % V _{CC} Start-up time terms 3 ms Max. Measured from the time V _{CC} reaches its rated	Disable Time t_stp		1 µs Max.				Measured from the time OE or ST pin crosses 30 % $V_{\rm CC}$				
Start-up time tetr 3 ms May Measured from the time V _{CC} reaches its rated	Enable Time t_s		t_sta	1 μs Max.				Measured from the time OE pin crosses 70 % V _{CC}			
	Resume Time		t_res		3 r	ns Max.					
minimum value, 1.62 V	Start-up time t		t_str	3 ms Max.			Measured from the time V _{CC} reaches its rated minimum value, 1.62 V				
Frequency aging f_aging This is included in frequency tolerance specification. +25 °C, 10 years	Frequency aging f_aging			This is included in frequency tolerance specification.							

^{*1} Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, load drift and aging (+25 °C, 10 years).

Pin description

Pin	Name	I/O type	Function					
	OE	Input	Output enable	High: Specified frequency output from OUT pin Low: Out pin is low (weak pull down), only output driver is disabled.				
1	ST	Input	Standby	High: Specified frequency output from OUT pin Low: Out pin is low (weak pull down), Device goes to standby mode. Supply current reduces to the least as I_std.				
2	GND	Power	Ground	· · · · · · · · · · · · · · · · · · ·				
3	OUT	Output	Clock output					
4	V_{CC}	Power	Power supply					

® Rise/Fall time

Product Name

$\underline{\mathsf{SG-8018CG}}\,\underline{\mathsf{170.000000MHz}}\,\,\underline{\mathsf{T}}\,\,\,\underline{\mathsf{J}}\,\,\,\underline{\mathsf{H}}\,\,\underline{\mathsf{P}}\,\,\underline{\mathsf{A}}$

1

45678

CA: 7.0 mm x 5.0 mm

②Package type

CB: 5.0 mm x 3.2 mm CE: 3.2 mm x 2.5 mm

CG: 2.5 mm x 2.0 mm

 Supply voltage T: 1.8 V ~ 3.3 V Typ.

⑤Frequency tolerance

J: 50 x 10⁻⁶

H: -40 °C ~ +105 °C

P: Output Enable

S: Standby

@Operating temperature A: Default B: Fast C: Slow ⑦Function

①Model, ②Package type,

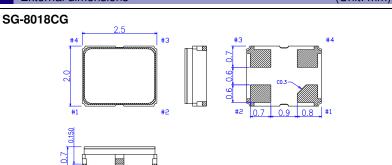
③Frequency, ④Supply voltage,

⑤Frequency tolerance, ⑥Operating temperature,

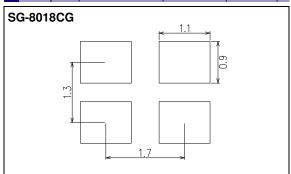
Tunction,

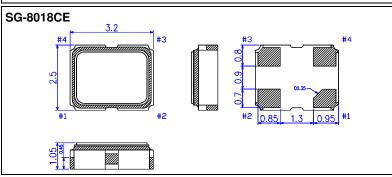
Rise/Fall time

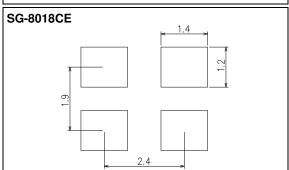
External dimensions (Unit: mm)

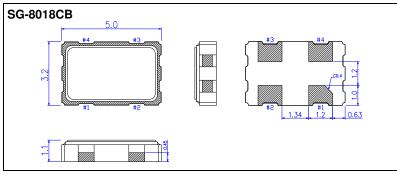


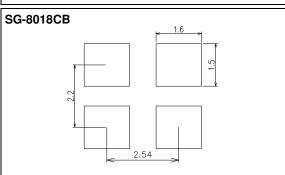
Footprint (Recommended) (Unit: mm)

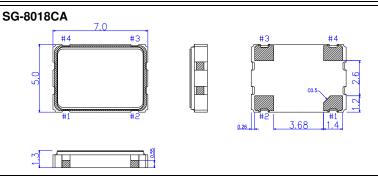


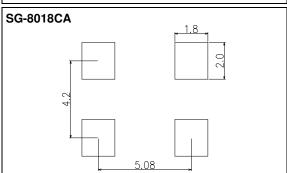










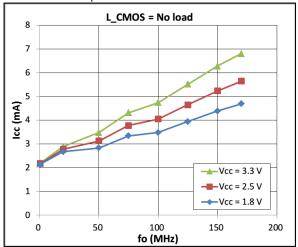


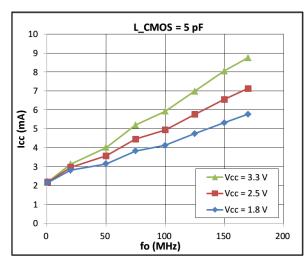
In order to achieve optimum jitter performance, the 0.1 μF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

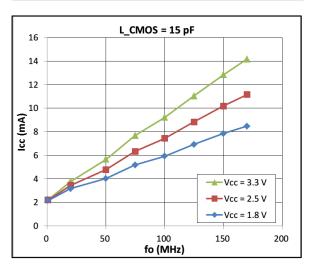


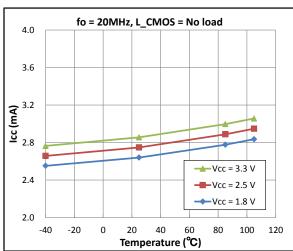
Specification Graph (Typical supplemental specification. Unless otherwise specified T_use = 25 °C, L_CMOS = 15pF)

Current Consumption

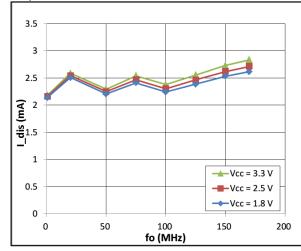


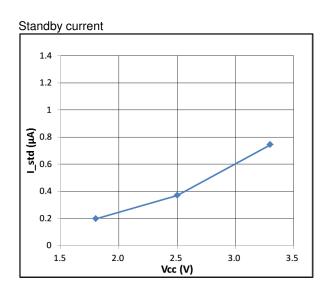






Output disable current



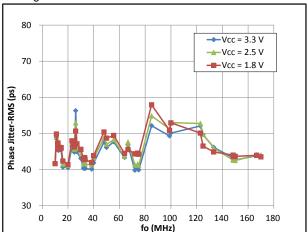


■Notes:

Specification Graph (Typical supplemental specification. Unless otherwise specified T_use = 25 °C, L_CMOS = 15pF)

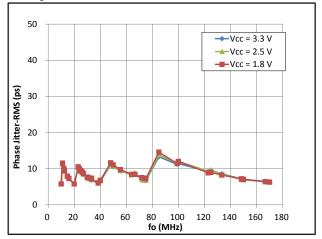
Phase Jitter RMS

(Integration bandwidth 12 k-20 MHz)

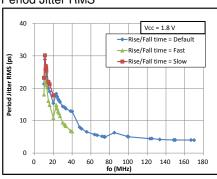


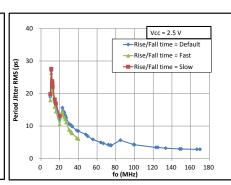
Phase Jitter RMS

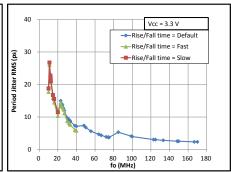
(Integration bandwidth 1.8 M-20 MHz)



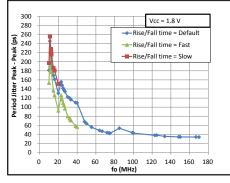
Period Jitter RMS

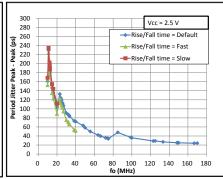


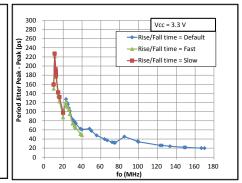




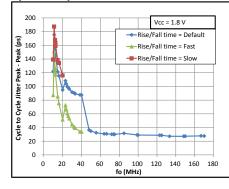
Period Jitter Peak-Peak

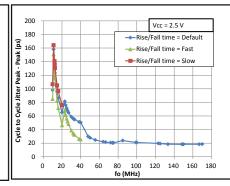


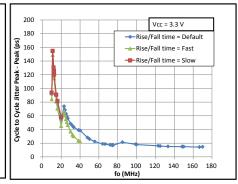




Cycle-to-Cycle Jitter Peak-Peak



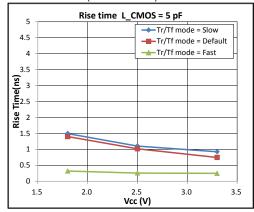


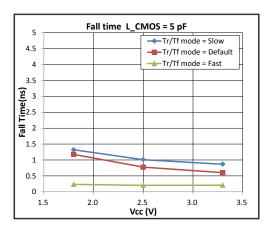


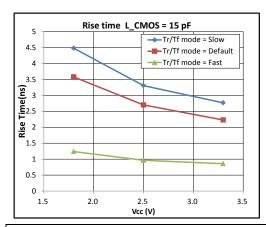
■Notes:

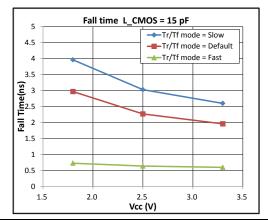
Specification Graph (Typical supplemental specification. Unless otherwise specified T use = 25 °C, L CMOS = 15pF)

Rise/Fall Time (fo = 20 MHz)





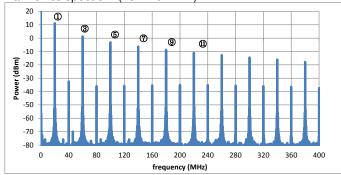


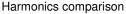


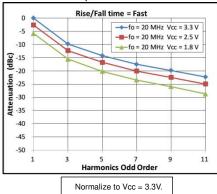
■Notes:

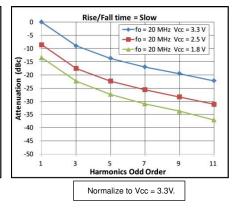
frequency	slow	default	fast	
0.67 M – 20 M	See Slow	See Default	See Fast	
20 M – 40 M	-	See Default	See Fast	
40 M = 170 M	-	See Fast	See Fast	

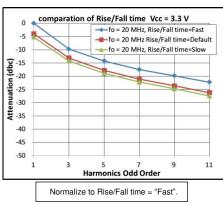
Harmonics spectrum (fo = 20 MHz)











■ Notes:



ESD Rating

Test items	Breakdown voltage
Human Body Model (HBM)	2000V
Machine Model (MM)	250V
Charged Device Model (CDM)	750V

Device Marking (Standard specification)

Model	Factory Programmed Part Marking	Field Programmable Part Marking (Blank Samples)
SG-8018CG	Frequency Product code 170. A2 OA23DM 1pin mark Lot No.	A2 OA23DM Lot No.
SG-8018CE	Frequency 170.0A2 o A23DM Lot No.	A2 o A23DM Lot No.
SG-8018CB	Frequency 170.0 A2 o A23DM Lot No.	A2 Product code A23DM Lot No.
SG-8018CA	Frequency 170.00 A2 O A23DM Lot No.	A2 O A23DM Lot No.

Simulation Model

• IBIS Model is available upon request. Please contact us. Information Required: Oscillator operating condition (i.e. Power Supply, Rise/Fall Time, Temperature)

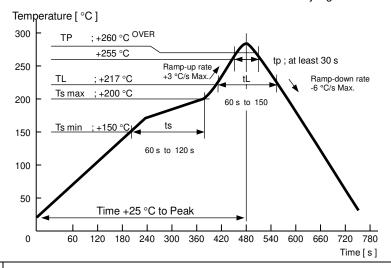


Device Material & Environmental Information

Model	Package	# of	Reference	Terminal	Terminal	Complies	Pb	MSL	Peak
	Dimensions	Pins	Weight	Material	Plating	With EU	Free	Rating	Temp.
			(Typ.)			RoHS			(Max)
SG-8018CG	2.5 x 2.0 x 0.7 mm	4	13 mg	W	Au	Yes	Yes	1	260°C
SG-8018CE	3.2 x 2.5 x 1.0 mm	4	25 mg	W	Au	Yes	Yes	1	260°C
SG-8018CB	5.0 x 3.2 x 1.1 mm	4	51 mg	W	Au	Yes	Yes	1	260°C
SG-8018CA	7.0 x 5.0 x 1.3 mm	4	143 mg	W	Au	Yes	Yes	1	260°C

SMD products Reflow profile(example)

The availability of the heat resistance for reflow conditions of JEDEC-STD-020D.01 is judged individually. Please inquire.





Pb free.



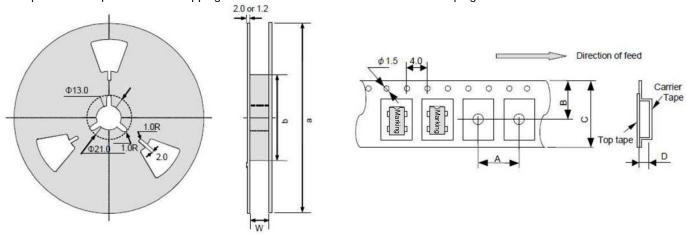
- Complies with EU RoHS directive.
 - About the products without the Pb-free mark.

 Contains Pb in products exempted by EU RoHS directive.

 (Contains Pb in sealing glass, high melting temperature type solder or other.)

Standard Packing Specification

SMD products are packed in the shipping carton as below table in accordance with taping standards EIA-481 and IEC-60286



Standard Packing Quantity & Dimension(Unit mm)

	Quantity	Reel Dimension			Car	Direction of			
Model	(pcs/Reel)	а	b	W	Α	В	С	D	Feed (L= Left Direction)
SG-8018CG	3000	Ф180	Ф60	9	4	5.25	8	1.15	L
SG-8018CE	2000	Ф180	Ф60	9	4	5.25	8	1.4	L
SG-8018CB	1000	Ф180	Ф60	13	8	7.25	12	1.4	L
SG-8018CA	1000	Ф254	Ф100	17.5	8	9.25	16	2.3	L

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs.

Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired ISO/TS 16949 certification that is requested strongly by major automotive manufacturers as standard.

ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



- ► Complies with EU RoHS directive.
 - *About the products without the Pb-free mark.

 Contains Pb in products exempted by EU RoHS directive.

 (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).

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