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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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Dual or Quad Selectable Programmable Crystal Oscillator Output: LV-PECL

# SG-8503CA / SG-8504CA

• Dual frequency Selectable: SG-8503CA,  $7.0 \times 5.0 \times 1.5$  mm (6 pins)

• Quad frequency Selectable: SG-8504CA, 7.0 × 5.0 × 1.5 mm (8 pins)

Frequency range:Supply voltage:50 MHz to 800 MHz2.5 V to 3.3 V

#### Features

- User-specified two (FSEL) or four (FSEL0, FSEL1) startup frequencies
- High frequency fundamental tone crystal, Low jitter PLL technology
- Available field oscillator programmer "SG-Writer II"

#### Application

OTN, BTS, Test Instrument



## Specifications (characteristics)

Item	Item Symbol Specifications		Conditions / Remarks		
Output frequency range	fo	50 MHz to 800 MHz	-		
Supply voltage	V <sub>cc</sub>	2.5 V - 0.125 V to 3.3 V + 0.33 V	-		
Storage temperature	T_stg	-55 °C to +125 °C	Store as bare product after packing		
Operating temperature	T_use	-40 °C to +85 °C	-		
Frequency tolerance *1	f_tol	±31.5 × 10 <sup>-6</sup> , ±50 × 10 <sup>-6</sup>	Includes frequency aging (10 years)		
Current consumption	Icc	90 mA Max.	OE Active, L_ECL = $50 \Omega$		
Disable current	l dia	40 mA Max.	OE Inactive, Output Standby: Hi-Z mode		
Disable current	I_dis	70 mA Max.	OE Inactive, Output Standby: Fix mode		
Symmetry	SYM	45 % to 55 %	At outputs crossing point		
Output valtage	V <sub>OH</sub>	Vcc - 1.025 V Min.	DC characteristics		
Output voltage	V <sub>OL</sub>	Vcc - 1.62 V Max.	DC characteristics		
Output load condition	L_ECL	50 Ω	Termination to Vcc - 2.0 V		
land to the same	$V_{IH}$	70% Vcc Min.	SG-8503CA : OE, FSEL		
Input voltage	V <sub>IL</sub>	30% Vcc Max.	SG-8504CA: OE, FSEL0, FSEL1		
Rise time / Fall time	tr/tf	400 ps Max.	Between 20% and 80% of (V <sub>OH</sub> - V <sub>OL</sub> )		
Start-up time	t_str	10 ms Max.	Time at minimum supply voltage to be 0 s		
Setting time for frequency change	t <sub>SET1</sub>	1.5 ms Max.	SG-8503CA: From setting FSEL pin to output new frequency SG-8504CA: From setting FSEL0 / FSEL1 pin to output new frequency		

<sup>\*1</sup> Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage change, reflow drift and 10 years aging at +25 °C.

Product Name (Standard form)

① Model, ② Package type,

③ Frequeny-0 (50 ~ 800 MHz), ④ Frequency-1 (50 ~ 800 MHz), ⑤ Internal crystal frequency, ⑥ Output enable pin Polarity,

② Supply voltage/Output format, ® Frequency tolerance/Operating temperature, ⑨ Output standby type

Product Name (Standard form)

① Model, ② Package type,

⑤ Frequeny-0 (50 ~ 800 MHz), ⑤ Parameter identifier, ⑤ Internal crystal frequency, ⑥ Output enable pin Polarity,

② Supply voltage/Output format, ® Frequency tolerance/Operating temperature, ⑨ Output standby type

Internal crystal frequencyA 114.1444 MHz

© Output enable						
pin Polarity						
Р	Active High					
Q	Active Low					

7 5	② Supply voltage/						
(	Output format						
R	2.5 V ~ 3.3 V/LVPECL						

Frequency tolerance/					
Operating temperature					
С					

Output standby type					
F	Fix (OUT="L", OUTN="H")				
Z	High-Z				

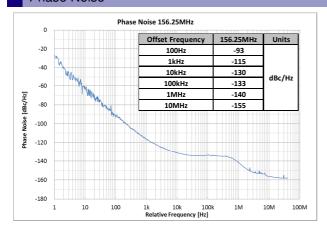
#### Phase Jitter

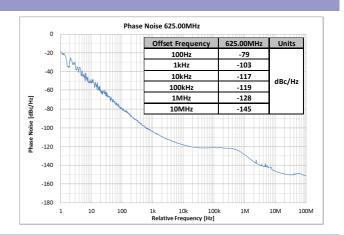
	Offset Frequency	100.00 MHz	125.00 MHz	156.25 MHz	250.00 MHz	312.50 MHz	500.00 MHz	625.00 MHz
Phase jitter *2 Typ.	12 kHz to 20 MHz	0.31 ps	0.30 ps	0.26 ps	0.26 ps	0.29 ps	0.28 ps	0.29 ps

<sup>\*2</sup> In order to achieve optimum jitter performance, it is recommended that the capacitor (0.1 μF + 10 μF) between V<sub>CC</sub> and GND pin should be placed as close to the V<sub>CC</sub> pin as possible.

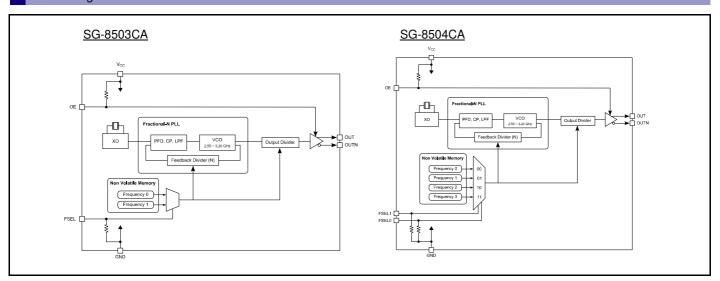


# Phase Noise





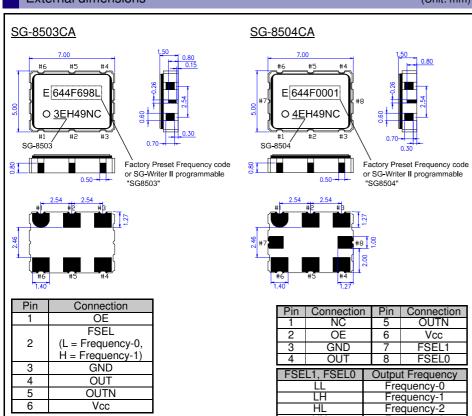
### Block diagram



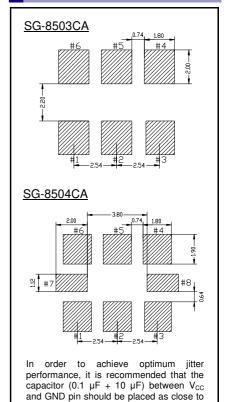
# External dimensions



Frequency-3



#### Footprint (Recommended)(Unit: mm)



the V<sub>CC</sub> pin as possible.



# Simulation Model

• IBIS Model is available. Please contact us.

## ESD Rating

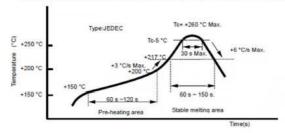
Human Body Model (HBM)	2000V
Machine Model (MM)	200V

# Device Material & Enrivonmental Information

Model	Package Dimensions	# of Pins	Reference Weight (Typ.)	Terminal Material	Terminal Plating	Complies With EU RoHS	Pb Free	MSL Rating	Peak Temp. (Max)
SG-8503CA	7.0 x 5.0 x	6	167 mg	W	۸.,	Yes	Yes	1	260°C
SG-8504CA	1.5 mm	8	168 mg	VV	Au	165	res	1	200 C

SMD products Reflow profile (example)

The availability of the heat resistance for reflow conditions of JEDEC-STD-020D.01 is judged individually. Please inquire





Pb free.



- •Complies with EU RoHS directive.
  - \*About the products without the Pb-free mark.

    Contains Pb in products exempted by EU RoHS directive.

(Contains Pb in sealing glass, high melting temperature type solder or other.)

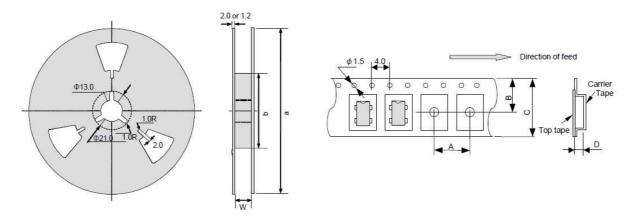


# Device Marking

Model	Factory Programmed Part Marking	Field Programmable Part Marking (Blank Samples)
SG-8503CA	E 644F698L  Logo mark  1 Pin mark  Product Name  #1 #2 #3  Production Lot Number	E SG8503  Logo mark  1 Pin mark  Spec Name  #1 #2 #3  Product Name  Production Lot Number
SG-8504CA	Logo mark  E 644F0001  #8  O 4EH49NC  Product Name  #1 #2 #3  Production Lot Number	#6 #5 #4  Logo mark  E SG8504  #8  1 Pin mark  O 4EA 49NC  Spec Name  #1 #2 #3  Production Lot Number

# Standard Packing Specification

SMD products are packed in the shipping carton as below table in accordance with taping standards EIA-481 and IEC-60286



# Standard Packing Quantity and Dimension (Unit: mm)

Model	Quantity Reel dimension				Ca	reer Tape	Direction of feed		
iviodei	(pcs/Reel)	а	b	W	Α	В	С	D	(L=left direction)
SG-8503CA SG-8504CA	1000	Ф180	Ф60	17	8	9.25	16	2.1	L

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

#### **WORKING FOR HIGH QUALITY**

In order provide high quality and reliable products and services than meet customer needs,

Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired ISO/TS 16949 certification that is requested strongly by major automotive manufacturers as standard.

ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



- ► Complies with EU RoHS directive.
  - \*About the products without the Pb-free mark.

    Contains Pb in products exempted by EU RoHS directive.

    (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc.).

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