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SGB-4333(Z)

DC to 3GHZ ACTIVE BIAS GAIN BLOCK

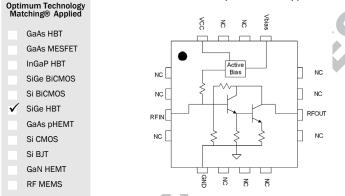


RFMD Green, RoHS Compliant, Pb-Free (Z Part Number) Package: 3x3 QFN, 16-Pin

Product Description

RFMD's SGB-4333 is a high performance SiGe HBT MMIC amplifier utilizing a Darlington configuration with an active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 3V to 5V supply the SGB-4333 does not require a drop resistor as compared to typical Darlington amplifiers. This robust amplifier features a Class 1C ESD rating, low thermal resistance, and unconditional stability. The SGB-4333 product is designed for high linearity 3V gain block applications that require small size and minimal external components. It is on chip matched to 50Ω and an exter-

nal bias inductor choke is required for the application band.



Features

- High Reliability SiGe HBT Technology
- Robust Class 1C ESD
- Simple and Small Size
- P_{1dB}=10.0dBm at 1950MHz
- IP₃=22.5dBm at 1950MHz
- Low Thermal Resistance = 76°C/W

Applications

- 3V Battery Operated Applications
- LO Buffer Amp
- RF Pre-driver and RF Receive Path

Parameter	Specification			Unit	Condition	
Parameter	Min.	Тур.	Max.	Unit	Condition	
Frequency of Operation	DC		3000	MHz		
Small Signal Gain		17.5		dB	Freq=850MHz	
	13.0	14.5	16.0	dB	Freq=1950MHz	
		14.0		dB	Freq=2400MHz	
Output Power at 1dB Compression		11.5		dBm	Freq=850MHz	
	8.5	10.0		dBm	Freq=1950MHz	
		9.5		dBm	Freq=2400MHz	
Output IP ₃		25.0		dB	Freq=850MHz	
	20.0	22.5		dB	Freq=1950MHz	
		21.0		dB	Freq=2400MHz	
Input Return Loss	8.5	10.5		dB	Freq=1950MHz	
Output Return Loss	8.5	10.5		dB	Freq=1950MHz	
Current	48	56	62	mA		
Noise Figure		4.0	5.0	dB	Freq=1950MHz	
Thermal Resistance		76		°C/W	junction - lead	

Test Conditions: $Z_0 = 50\Omega$, $V_{CC} = 3.0V$, $I_C = 56$ mA, T = 30 °C)

SGB-4333(Z)



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Absolute Maximum Ratings

Parameter	Rating	Unit
Current (I _C Total)	120	mA
Device Voltage (V _D)	5	V
Power Dissipation	0.4	W
Operating Lead Temperature (T_L)	-40 to +85	°C
RF Input Power	20	dBm
Storage Temperature Range	-40 to +150	°C
Operating Junction Temp (T _J)	+150	°C

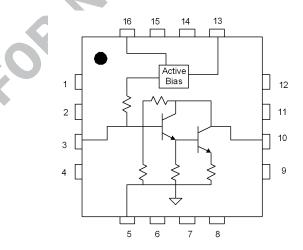
Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one. Bias Conditions should also satisfy the following expression:

 $I_D V_D < (T_J - T_L) / R_{TH}$, j-l

Detailed Performance Table: Vcc=3V. Ic=56mA. T=25°C. Z=50Ω

Parameter	Unit	100	500	850	1950	2400	3500
		MHz	MHz	MHz	MHz	MHz	MHz
Small Signal Gain, G	dB	18.1	17.8	17.5	14.5	14.0	11.8
Output 3rd Order Intercept Point, OIP ₃	dBm		26.5	25.0	22.5	21.0	
Output Power at 1dB Compression, P _{1dB}	dBm		12.0	11.5	10.0	9.5	
Input Return Loss, IRL	dB	17.3	15.6	14.2	10.5	10.7	8.1
Output Return Loss, ORL	dB	13.4	12.7	12.0	10.5	11.0	12.0
Reverse Isolation, S ₁₂	dB	21.1	21.5	21.9	22.7	22.8	22.9
Noise Figure, NF	dB	5.0	3.5	3.5	4.0	4.4	5.0

Simplified Device Schematic



Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical perfor-mance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

RoHS status based on EU Directive 2002/95/EC (at time of this document revision).

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SGB-4333(Z)

1.9

+25c

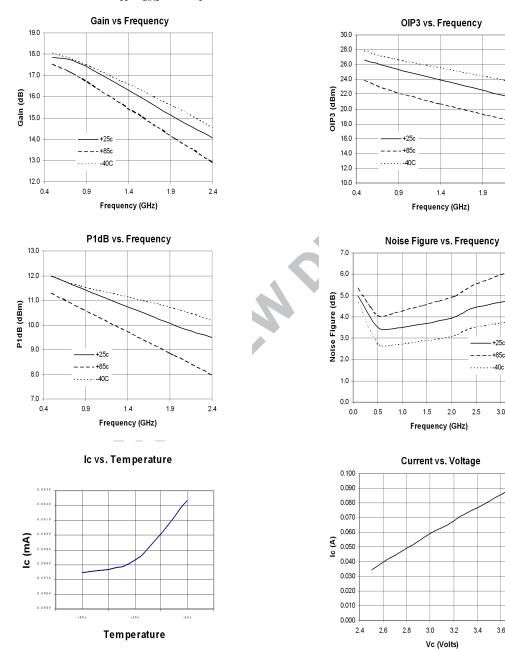
+85c

3.0 3.5

3.4 3.6 3.8

2.4

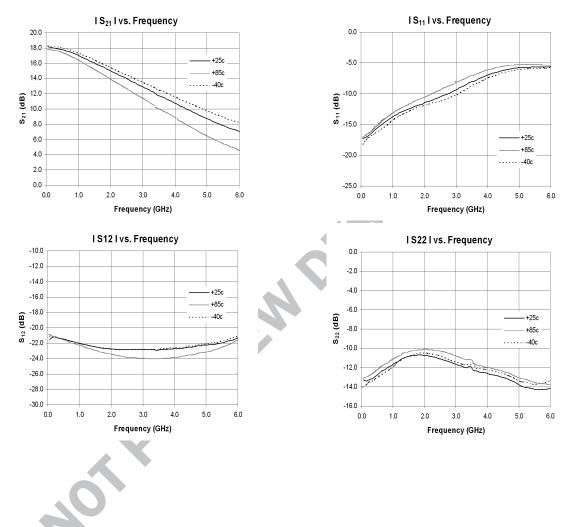
Evaluation Board Data ($V_{CC} = V_{BIAS} = 3.0V$, $I_C = 56$ mA) Bias Tee substituted for DC feed inductor (L1)



SGB-4333(Z)



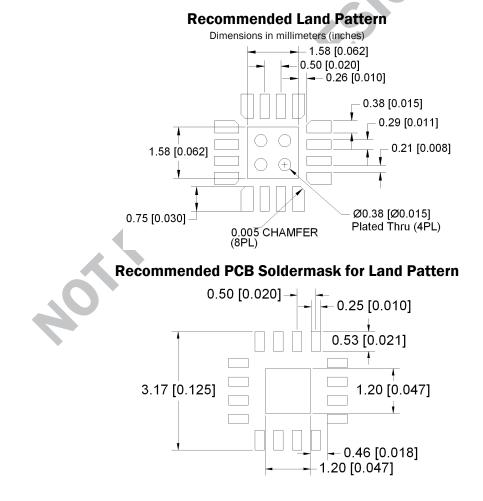
Evaluation Board Data ($V_{CC}=V_{BIAS}=3.0V$, $I_{C}=56$ mA) Bias Tee substituted for DC feed inductor (L1) cont.





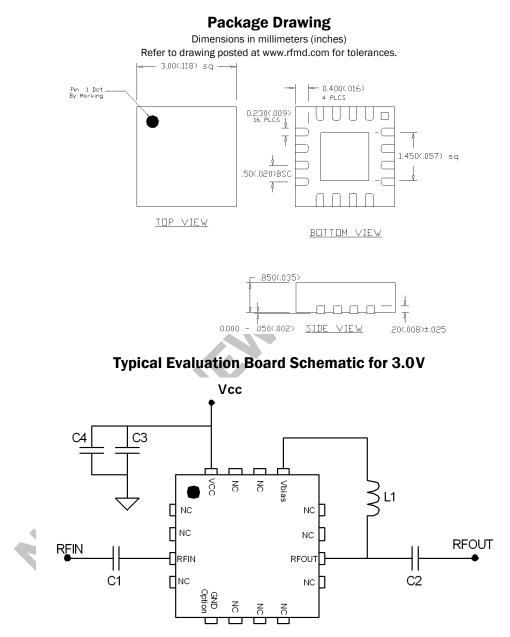
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Pin	Function	Description		
1, 2,	NC	These are no connect pins. Leave them unconnected on the PC board.		
4, 6,				
7, 8,				
11,				
12, 14				
3	RF IN	RF input pin. A DC voltage should not be connected externally to this pin		
5	GND	An extra ground pin that is connected to the backside exposed paddle. Connection is optional.		
10	RF OUT	RF Output pin. Bias is applied to the Darlington stage thru this pin.		
13	VBIAS	This pin sources the current from the active bias circuit. Connect to pin 10 thru an inductor choke.		
16	VCC	This is Vcc for the active bias circuit.		
Back-	GND	The backside exposed paddle is the main electrical GND and requires multiple vias in the PC board to GND. It is also the main thermal path.		
side				

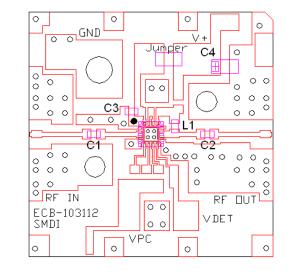












Evaluation Board Layout and Bill of Materials

Board material GETEK, 31mil thick, Dk=4.2, 1oz copper

Component Values By Band

Designator	500MHz	850 MHz	1950MHz	2400 MHz
C3	1000pF	1000pF	1000pF	1000pF
C4*	1uF	1uF	1uF	1uF
C1, C2	220pF	68 p F	43 pF	22 pF
L1	68nH	33nH	22nH	18nH

*C4 is optional depending on application and filtering. Not required for SGB device operation.

Note: The amplifier can be run from a 5V supply by simply inserting a 33Ω resistor in series with V_{CC}.

Part Identification

The part will be symbolized with an "SGB4333" for Sn/Pb plating or "SGB43Z" for RoHS green compliant product. Marking designator will be on the top surface of the package.

Ordering Information

Part Number	Reel Size	Devices/Reel
SGB-4333	13"	3000
SGB-4333Z	13"	3000





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