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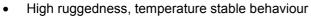




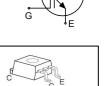
High Speed IGBT in NPT-technology

- 30% lower *E*_{off} compared to previous generation
- Short circuit withstand time 10 μs
- Designed for operation above 30 kHz
- NPT-Technology for 600V applications offers:

 - parallel switching capability
 moderate E_{off} increase with temperature
 - very tight parameter distribution



- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC¹ for target applications
- Complete product spectrum and PSpice Models: http://www.infineon.com/igbt/



Туре	$V_{\sf CE}$	I _C	$E_{ m off}$	$T_{\rm j}$	Marking	Package
SGB15N60HS	600V	15A	200µJ	150°C	G15N60HS	PG-TO-263-3-2

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CE}	600	V
DC collector current	I _C		Α
$T_{\rm C}$ = 25°C		27	
$T_{\rm C} = 100^{\circ}{\rm C}$		15	
Pulsed collector current, t_p limited by T_{jmax}	I _{Cpuls}	60	
Turn off safe operating area	-	60	
$V_{CE} \le 600 \text{V}, \ T_{j} \le 150^{\circ} \text{C}$			
Gate-emitter voltage static transient (t_p <1 μ s, D <0.05)	V _{GE}	±20 ±30	V
Short circuit withstand time ²⁾	tsc	10	μs
$V_{\rm GE}$ = 15V, $V_{\rm CC} \le 400$ V, $T_{\rm j} \le 150$ °C			
Power dissipation	P _{tot}	138	W
$T_{\rm C}$ = 25°C			
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$	-55+150	°C
Time limited operating junction temperature for $t < 150h$	$T_{j(tl)}$	175	
Soldering temperature (reflow soldering, MSL1)	-	245	

 $^{^{\}rm 1}$ J-STD-020 and JESD-022 $^{\rm 2)}$ Allowed number of short circuits: <1000; time between short circuits: >1s.

SGB15N60HS

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				•
IGBT thermal resistance,	R _{thJC}		0.9	K/W
junction – case				
Thermal resistance,	R _{thJA}		62	
junction – ambient				
SMD version, device on PCB ¹⁾	R _{thJA}		40	

Electrical Characteristic, at T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
Parameter			min.	Тур.	max.	Ullit
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{\rm GE}$ =0V, $I_{\rm C}$ =500 μ A	600	-	-	V
Collector-emitter saturation voltage	V _{CE(sat)}	$V_{\rm GE} = 15 \rm V, I_{\rm C} = 15 \rm A$				
		<i>T</i> _j =25°C		2.8	3.15	
		T _j =150°C		3.5	4.00	
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_{\rm C}$ =400 μ A, $V_{\rm CE}$ = $V_{\rm GE}$	3	4	5	
Zero gate voltage collector current	I _{CES}	V _{CE} =600V, V _{GE} =0V				μΑ
		<i>T</i> _j =25°C	-	-	40	
		T _j =150°C	-	-	2000	
Gate-emitter leakage current	I _{GES}	V _{CE} =0V, V _{GE} =20V	-	-	100	nA
Transconductance	g fs	V _{CE} =20V, I _C =15A	-	10		S

Dynamic Characteristic

Input capacitance	Ciss	V _{CE} =25V,	-	810	pF
Output capacitance	Coss	$V_{GE}=0V$,	-	83	
Reverse transfer capacitance	Crss	<i>f</i> =1MHz	-	51	
Gate charge	Q _{Gate}	$V_{\rm CC}$ =480V, $I_{\rm C}$ =15A	-	80	nC
		V _{GE} =15V			
Internal emitter inductance	LE		-	7	nH
measured 5mm (0.197 in.) from case					
Short circuit collector current ²⁾	I _{C(SC)}	V_{GE} =15V, t_{SC} ≤10 μ s V_{CC} ≤ 400V, T_{j} ≤ 150°C	-	135	A

2 **Power Semiconductors** Rev 2.3 Oct 06

 $^{^{1)}}$ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70µm thick) copper area for collector connection. PCB is vertical without blown air. $^{2)}$ Allowed number of short circuits: <1000; time between short circuits: >1s.



Switching Characteristic, Inductive Load, at T_j =25 °C

Parameter	Symbol	Conditions	Value			I Imit
	Symbol		min.	typ.	max.	Unit
IGBT Characteristic	•					
Turn-on delay time	$t_{d(on)}$	$T_{\rm j}$ =25°C, $V_{\rm CC}$ =400V, $I_{\rm C}$ =15A,	-	13		ns
Rise time	t _r		-	14		
Turn-off delay time	$t_{d(off)}$	$V_{\rm GE}$ =0/15V, $R_{\rm G}$ =23 Ω $L_{\sigma}^{1)}$ =60nH, $C_{\sigma}^{1)}$ =40pF Energy losses include "tail" and diode reverse recovery.	-	209		
Fall time	t _f		-	15		
Turn-on energy	Eon		-	0.32		mJ
Turn-off energy	E _{off}		-	0.21		1
Total switching energy	E _{ts}		-	0.53		

Switching Characteristic, Inductive Load, at T_i =150 °C

Parameter	Symbol	Conditions	Value			Linit
	Symbol		min.	typ.	max.	Unit
IGBT Characteristic	·					
Turn-on delay time	$t_{d(on)}$	T _j =150°C	_	11		ns
Rise time	t _r	$V_{\rm CC} = 400 \text{V}, I_{\rm C} = 15 \text{A},$	-	6		
Turn-off delay time	$t_{d(off)}$	$V_{\rm GE} = 0/15 V$, $R_{\rm G} = 3.6 \Omega$	-	72		1
Fall time	t_{f}	$L_{\sigma_{1}}^{(1)} = 60 \text{ nH},$	-	26		
Turn-on energy	Eon	C _o ¹⁾ =40pF Energy losses include "tail" and diode reverse recovery.	-	0.38		mJ
Turn-off energy	E _{off}		-	0.20		
Total switching energy	E _{ts}		-	0.58		
Turn-on delay time	$t_{d(on)}$	T _j =150°C	-	12		ns
Rise time	t _r	$V_{\rm CC} = 400 \text{V}, I_{\rm C} = 15 \text{A},$	-	15		
Turn-off delay time	$t_{d(off)}$	$V_{\rm GE} = 0/15 V$, $R_{\rm G} = 23 \Omega$	-	235		
Fall time	t _f	$L_{\sigma}^{(1)}$ = 60 n H, $C_{\sigma}^{(1)}$ = 40 p F Energy losses include "tail" and diode reverse recovery.	_	17		
Turn-on energy	Eon		-	0.48		mJ
Turn-off energy	E _{off}		-	0.30		
Total switching energy	E _{ts}		_	0.78		

 $^{^{1)}}$ Leakage inductance L $_{\sigma}$ and $\,$ Stray capacity C $_{\sigma}$ due to test circuit in Figure E.





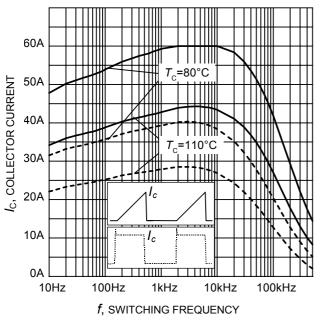


Figure 1. Collector current as a function of switching frequency $(T_j \le 150^{\circ}\text{C}, D = 0.5, V_{\text{CE}} = 400\text{V}, V_{\text{GE}} = 0/+15\text{V}, R_{\text{G}} = 23\Omega)$

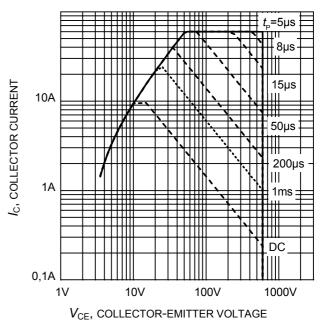


Figure 2. Safe operating area $(D = 0, T_C = 25^{\circ}C, T_i \le 150^{\circ}C; V_{GE} = 15V)$

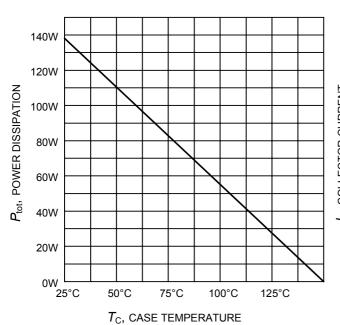


Figure 3. Power dissipation as a function of case temperature $(T_i \le 150^{\circ}\text{C})$

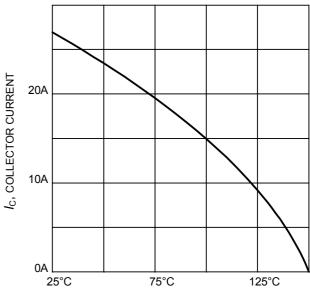


Figure 4. Collector current as a function of case temperature $(V_{GE} \le 15V, T_i \le 150^{\circ}C)$

 $T_{\rm C}$, CASE TEMPERATURE





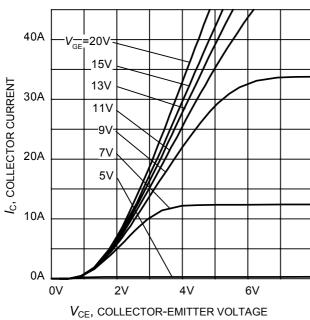


Figure 5. Typical output characteristic $(T_i = 25^{\circ}C)$

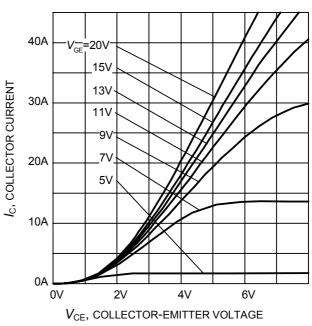


Figure 6. Typical output characteristic $(T_i = 150^{\circ}C)$

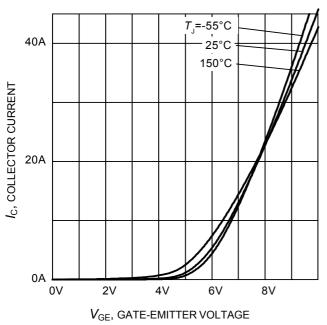


Figure 7. Typical transfer characteristic $(V_{CE}=10V)$

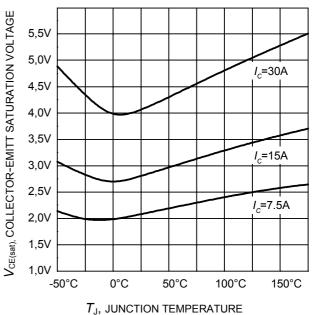


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature $(V_{GE} = 15V)$





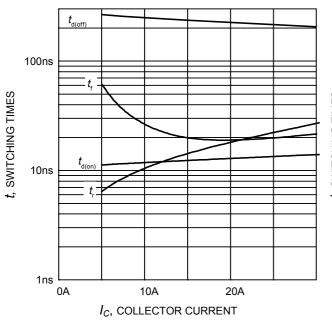


Figure 9. Typical switching times as a function of collector current (inductive load, T_J =150°C, V_{CE} =400V, V_{GE} =0/15V, R_G =23 Ω , Dynamic test circuit in Figure E)

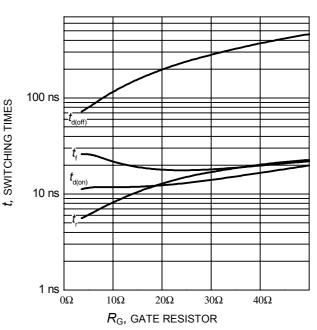


Figure 10. Typical switching times as a function of gate resistor (inductive load, T_J =150°C, V_{CE} =400V, V_{GE} =0/15V, I_C =15A, Dynamic test circuit in Figure E)

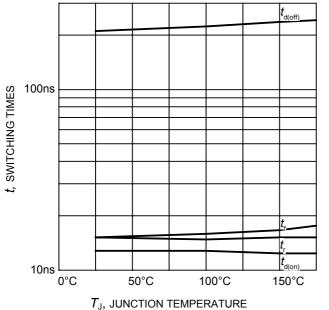


Figure 11. Typical switching times as a function of junction temperature (inductive load, V_{CE} =400V, V_{GE} =0/15V, I_{C} =15A, R_{G} =23 Ω , Dynamic test circuit in Figure E)

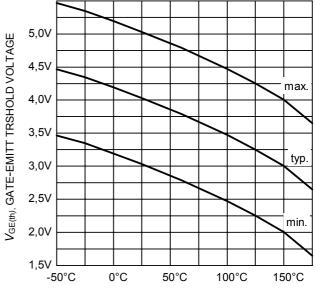


Figure 12. Gate-emitter threshold voltage as a function of junction temperature $(I_C = 0.5 \text{mA})$

 $T_{\rm J}$, JUNCTION TEMPERATURE





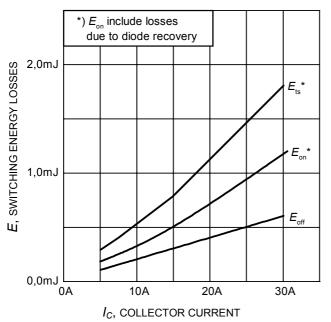


Figure 13. Typical switching energy losses as a function of collector current (inductive load, T_J =150°C, V_{CE} =400V, V_{GE} =0/15V, R_G =23 Ω , Dynamic test circuit in Figure E)

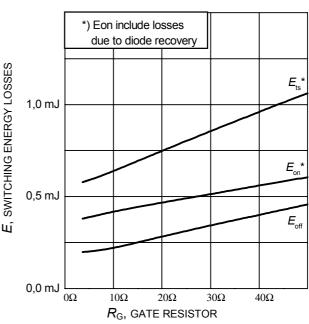


Figure 14. Typical switching energy losses as a function of gate resistor (inductive load, T_J =150°C, V_{CE} =400V, V_{GE} =0/15V, I_C =15A, Dynamic test circuit in Figure E)

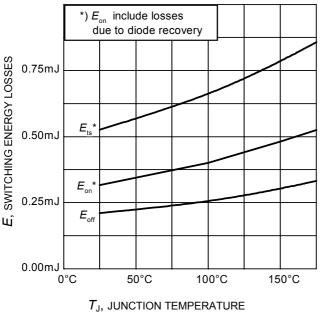
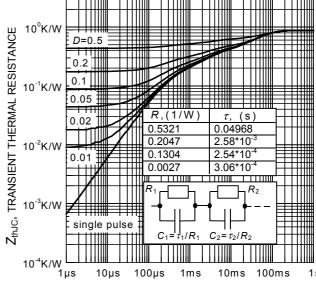


Figure 15. Typical switching energy losses as a function of junction temperature

(inductive load, $V_{\rm CE}$ =400V, $V_{\rm GE}$ =0/15V, $I_{\rm C}$ =20A, $R_{\rm G}$ =23 Ω , Dynamic test circuit in Figure E)



 $t_{\rm P}$, PULSE WIDTH Figure 16. IGBT transient thermal resistance ($D = t_{\rm p} / T$)





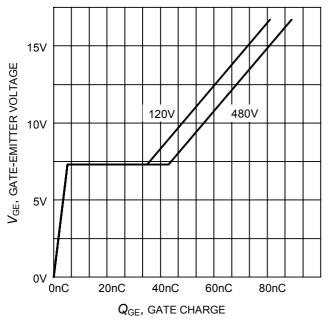


Figure 17. Typical gate charge $(I_C=15 \text{ A})$

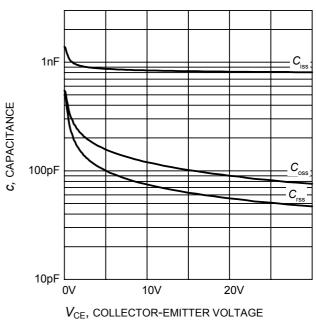


Figure 18. Typical capacitance as a function of collector-emitter voltage $(V_{GE}=0V, f=1 \text{ MHz})$

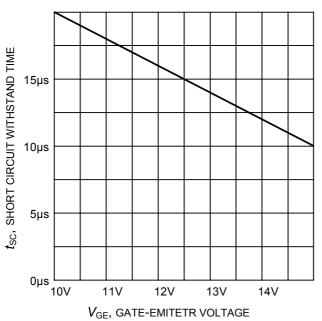


Figure 19. Short circuit withstand time as a function of gate-emitter voltage (V_{CE} =600V, start at T_{J} =25°C)

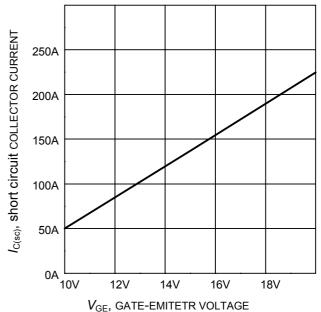
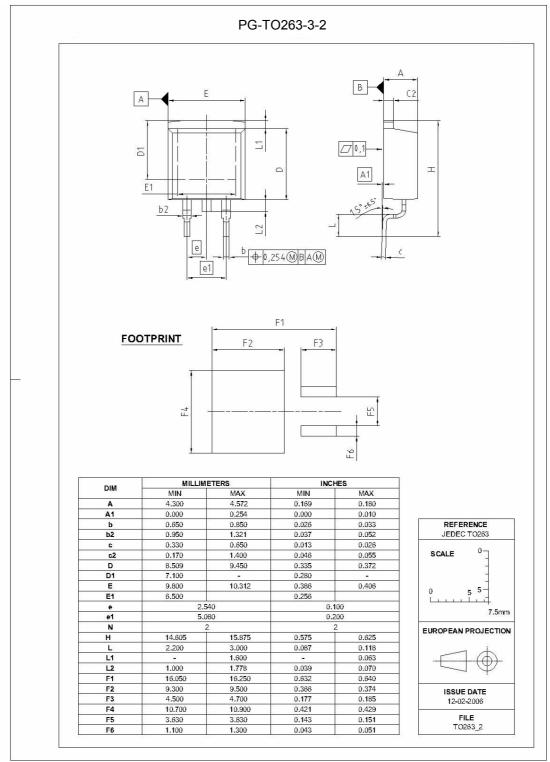
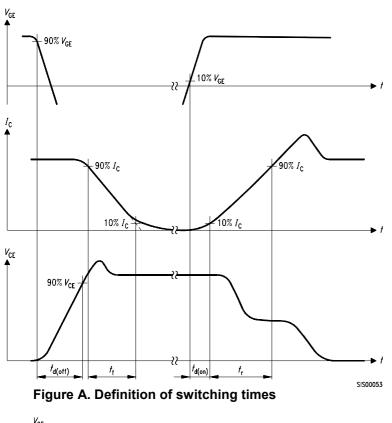


Figure 20. Typical short circuit collector current as a function of gate-emitter voltage $(V_{CE} \le 400 \text{V}, T_i \le 150 ^{\circ}\text{C})$









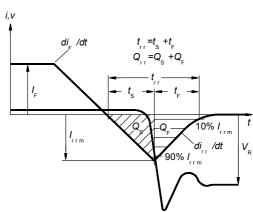
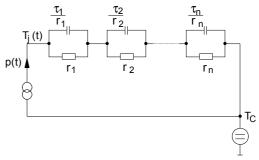


Figure C. Definition of diodes switching characteristics



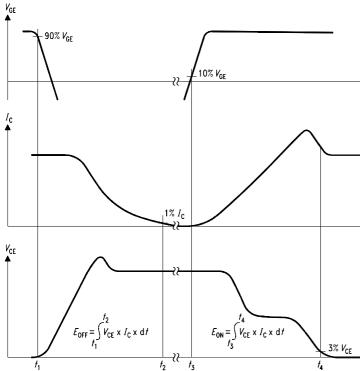


Figure D. Thermal equivalent circuit

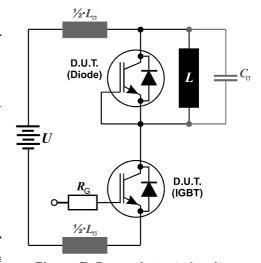


Figure B. Definition of switching losses

Figure E. Dynamic test circuit Leakage inductance L_{σ} =60nH and Stray capacity C_{σ} =40pF.

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