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SGC-6489Z

RFMD + TriQuint = Qorvo

50MHz to 3500MHz SILICON GERMANIUM

ACTIVE BIAS GAIN BLOCK

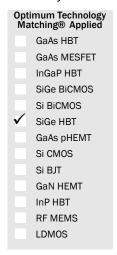
Package: SOT-89

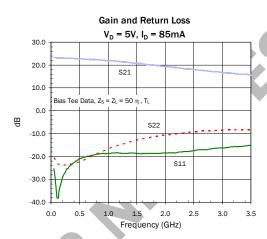




Product Description

RFMD's SGC-6489Z is a high performance SiGe HBT MMIC amplifier utilizing a Darlington configuration with an active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 5V supply, the SGC-6489Z does not require a dropping resistor as compared to traditional Darlington amplifiers. The SGC-6489Z product is designed for high linearity 5V gain block applications that require small size and minimal external components. It is internally matched to 50Ω .





Features

- Single Supply Operation: 5V at I_D = 85mA
- No Dropping Resistor Required
- Patented Self Bias Circuitry
- Gain = 19.5dBm at 1950MHz
- P1dB = 19.2dBm at 1950MHz
- IP3 = 32.8dBm at 1950MHz
- Robust 1000V ESD, Class 1C HBM

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

tion

Test Conditions: $V_D = 5.0V$, $I_D = 82$ mA, $T_L = 25$ °C, OIP3 Tone Spacing = 1MHz, Bias Tee Data, $Z_S = Z_L = 50\Omega$, P_{OUT} per tone = 0dBm



Absolute Maximum Ratings

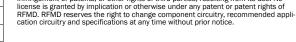
Parameter	Rating	Unit
Max Device Current (I _{CE})	100	mA
Max Device Voltage (V _{CE})	7	V
Max RF Input Power* (See Note)	3	dBm
Max Junction Temperature (T _J)	+150	°C
Operating Temperature Range (T _L)	-40 to +85	°C
Max Storage Temperature	+150	°C
ESD Rating - Human Body Model (HBM)	Class 1C	
Moisture Sensitivity Level	MSL 2	

^{*}Note: Load condition $Z_L = 50\Omega$

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias Conditions should also satisfy the following expression:

 $I_D V_D < (T_J - T_L)/R_{TH}$, j - I and $T_L = T_{LEAD}$



tions is not implied.

RFMD Green: RoHS compliant per EU Directive 2002/95/EC, halogen free per IEC 61249-2-21, < 1000ppm each of antimony trioxide in polymeric materials and red phosphorus as a flame retardant, and <2% antimony in solder.

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions.

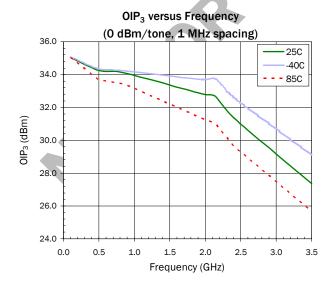
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Typical RF Performance at Key Operating Frequencies (Bias Tee Data)

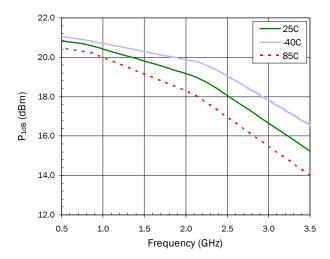
Parameter	Unit	100	500	850	1950	2140	2400	3500
		MHz						
Small Signal Gain (G)	dB	23.1	22.7	22.2	19.5	19.0	18.3	15.7
Output Third Order Intercept Point (OIP ₃)	dBm	35.1	34.3	34.1	32.8	32.7	31.4	27.4
Output Power at 1dB Compression (P _{1dB})	dBm	21.8	20.9	20.6	19.2	19.0	18.4	15.2
input Return Loss (IRL)	dB	37.0	22.0	19.0	18.0	18.0	17.0	16.0
Output Return Loss (ORL)	dB	23.0	22.0	19.0	11.0	11.0	10.0	8.0
Reverse Isolation (S ₁₂)	dB	25.0	25.0	26.0	25.0	25.0	24.0	22.0
Noise Figure (NF)	dB	1.8	2.0	2.1	2.4	2.4	2.5	2.9

Test Conditions: $V_D = 5V$ $I_D = 85\text{mA}$ OIP3 Tone Spacing = 1MHz, P_{OUT} per tone = 0dBm $T_L = 25\,^{\circ}\text{C}$ $Z_S = Z_L = 50\Omega$

Typical Performance with Bias Tees, $V_D = 5V$, $I_D = 82mA$

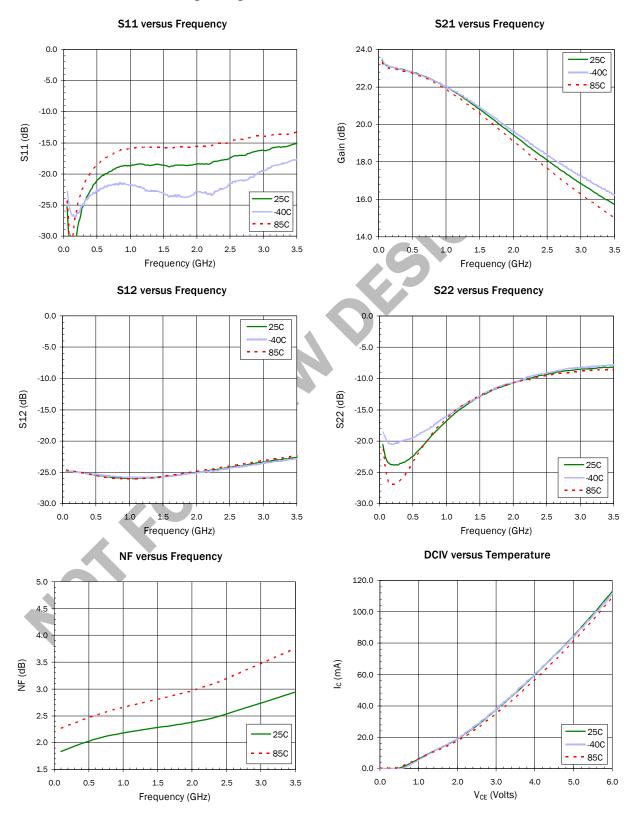


P_{1dB} versus Frequency





Typical Performance with Bias Tees, $V_D = 5V$, $I_D = 82mA$

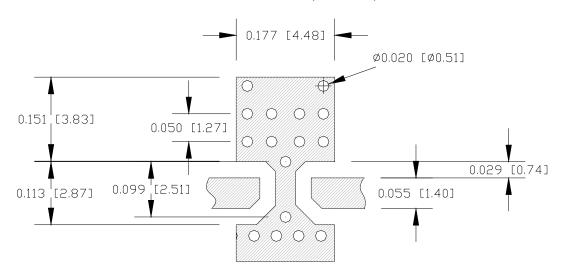




Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. Use via holes as close to the device ground leads as possible to reduce ground inductance and achieve optimum RF performance.
3	RF OUT/ DCBIAS	RF output and bias pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.

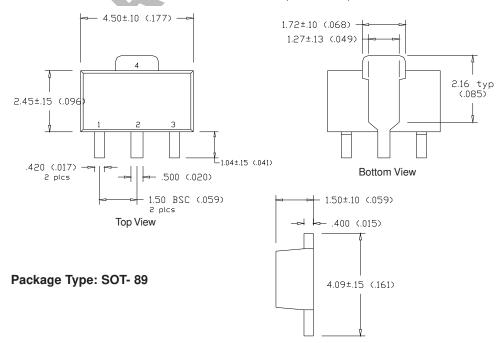
Suggested PCB Pad Layout

Dimensions in inches (millimeters)

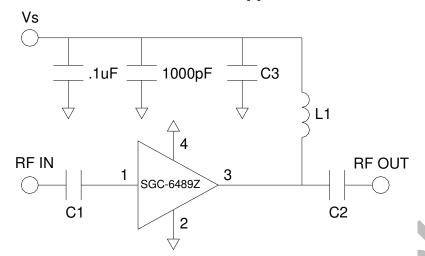


Package Drawing

Dimensions in inches (millimeters)

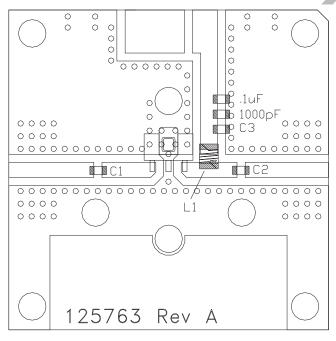


Application Schematic



Reference Designator	500 - 2100 MHz
C1	43pF
C2	43pF
C3	100pF
L1	48nH 0805HQ CC

Evaluation Board

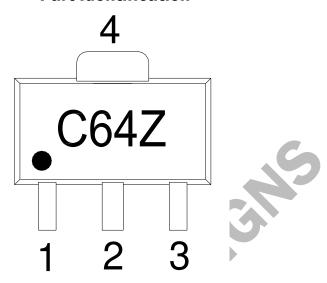


Mounting Instructions

- 1. Solder the copper pad on the backside of the device package to the ground plane.
- 2. Use a large ground pad area with many plated through-holes as shown.
- 3. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.



Part Identification



Alternate marking "SGC6489Z" on line one with Trace Code on line two.

Ordering Information

Part	Number	Package / Lead Composition	Reel Size	Devices / Reel
SGC	C-6489Z	Lead Free, RoHS Compliant	13"	3000
SGC-64	189Z-EVB1	100-1000 MHz Evaluation Board	N/A	N/A
SGC-6489Z-EVB2		500-2100 MHz Evaluation Board	N/A	N/A