imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





V _{DSS}	80V
R _{DS(on)} (Max.)	130mΩ
I _D	±3.4A
P _D	2.0W

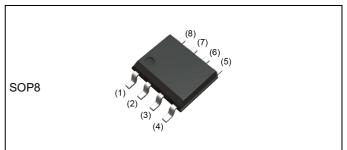
Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating ; RoHS compliant
- 4) Halogen Free

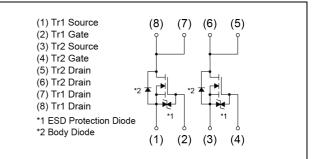
Application

Switching

Outline



Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Basic ordering unit (pcs)	2500
	Taping code	ТВ
	Marking	SH8K41

• Absolute maximum ratings ($T_a = 25^{\circ}C$, unless otherwise specified) <Tr1 and Tr2>

	, ,			
Parameter	Symbol	Value	Unit	
Drain - Source voltage	V _{DSS}	80	V	
Continuous drain current	I _D	±3.4	А	
Pulsed drain current	I _{DP} *1	±13.6	А	
Gate - Source voltage	V _{GSS}	±20	V	
	P _D *2	2.0	۱۸/	
Power dissipation (total)	P _D *3	1.4	W	
Junction temperature	Tj	150	°C	
Operating junction and storage temperature range	T _{stg}	-55 to +150	C°	

•Thermal resistance

Deremeter	Sumbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registered innetion embient (total)	R_{thJA}^{*2}	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	R_{thJA}^{*3}	-	-	89.2	C/VV

•Electrical characteristics (T_a = 25°C) <Tr1 and Tr2>

Deremeter	O: mah al	Conditions	Values			Linit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	80	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	81.3	-	mV/°C	
Zero gate voltage drain current	I _{DSS}			-	1	μA	
Gate - Source eakage current I_{GSS} $V_{DS} = 0V, V_{GS} = \pm 20V$		V _{DS} = 0V, V _{GS} = ±20V	-	-	±10	μA	
Gate threshold voltage	$V_{GS(th)}$ $V_{DS} = 10V, I_D = 1mA$		1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	-4.4	-	mV/°C	
		V _{GS} = 10V, I _D = 3.4A	-	90	130		
Static drain - source on - state resistance	R _{DS(on)}	V _{GS} = 4.5V, I _D = 3.4A	-	110	150	mΩ	
		V _{GS} = 4.0V, I _D = 3.4A	-	120	160		
Gate resistance	R _G	$R_{\rm G}$ f = 1MHz, open drain		5.0	-	Ω	
Forward Transfer Admittance			3.0	-	-	S	

*1 Pw \leq 10µs, Duty cycle \leq 1%

- *2 Mounted on a ceramic board (30×30×0.8mm)
- *3 Mounted on a FR4 (25×25×0.8mm)

*4 Pulsed

•Electrical characteristics ($T_a = 25^{\circ}C$) <Tr1 and Tr2>

Deremeter	Symbol	Conditions	Values			Linit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	600	-		
Output capacitance	C _{oss}	V _{DS} = 10V	-	100	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	40	-		
Turn - on delay time	$t_{d(on)}^{*4}$	$V_{DD} \simeq 40V, V_{GS} = 10V$	-	12	-		
Rise time	t _r *4	I _D = 1.7A	-	15	-		
Turn - off delay time	$t_{d(off)}^{*4}$	R _L = 24Ω	-	40	-	ns	
Fall time	t _f *4	R _G = 10Ω	-	12	-		

•Gate charge characteristics ($T_a = 25^{\circ}C$) <Tr1 and Tr2>

Deremeter	Cumphal	Conditions	Values			l la it
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg ^{*4}		-	6.6	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \simeq 40V, I_D = 3.4A$ $V_{GS} = 5.0V$	-	1.8	-	nC
Gate - Drain charge	Q _{gd} *4		-	2.2	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ _s	T - 25°0	-	-	1.6	^
Pulse forward current	I_{SP}^{*1}	T _a = 25°C	-	-	13.6	A
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 1.6A	-	-	1.2	V



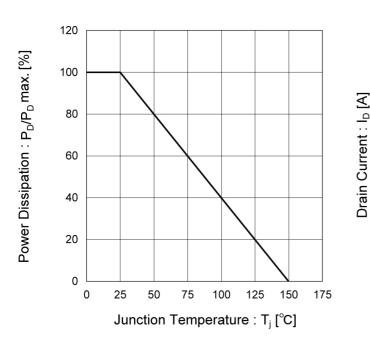
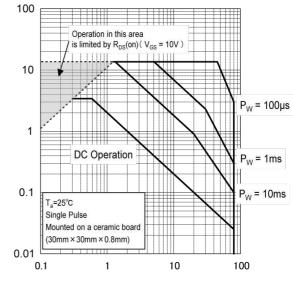


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]

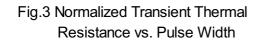
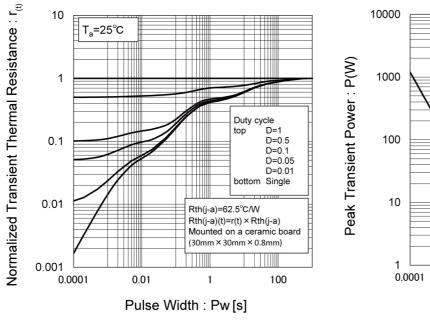
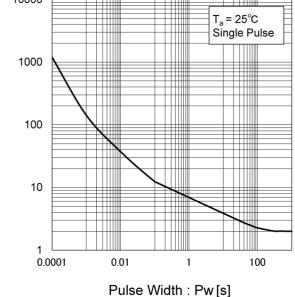


Fig.4 Single Pulse Maximum Power dissipation









3.4

3.2

2.8

2.6

2.4

2.2

2

1.8 1.6

1.4

1.2 1

0.8

0.6

0.4

0.2

0

0

3

Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)

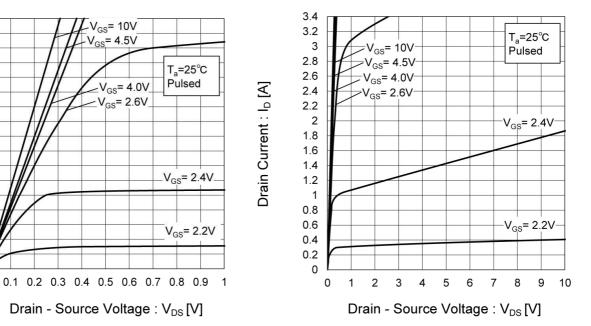
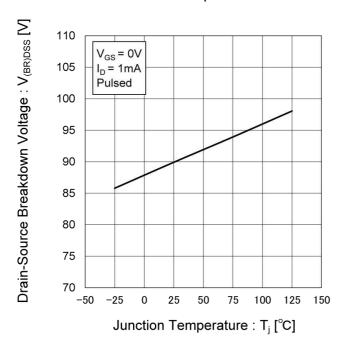


Fig.7 Breakdown Voltage vs. Junction Temperature







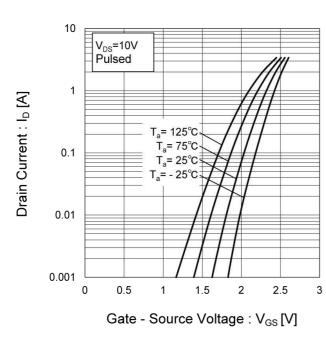
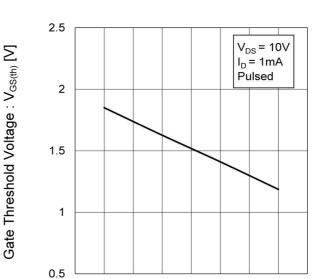


Fig.8 Typical Transfer Characteristics



50

Junction Temperature : T_j [°C]

75

25

-50

-25

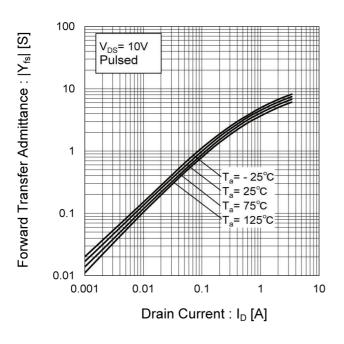
0

100 125

150

Fig.9 Gate Threshold Voltage vs. Junction Temperature

Fig.10 Forward Transfer Admittance vs. Drain Current





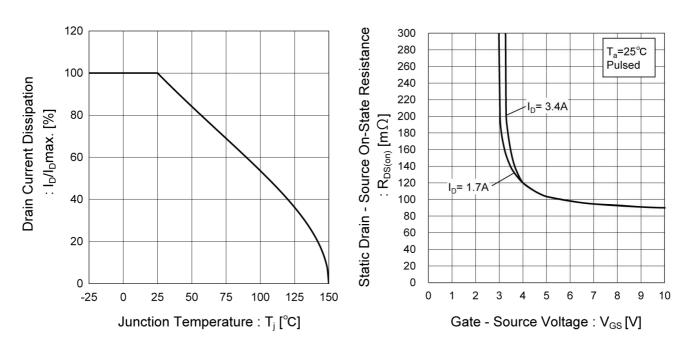
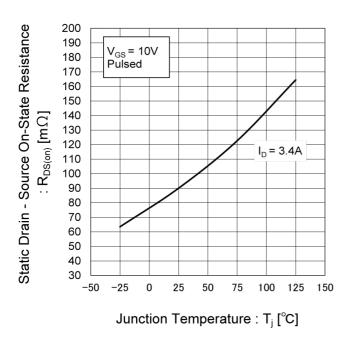


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



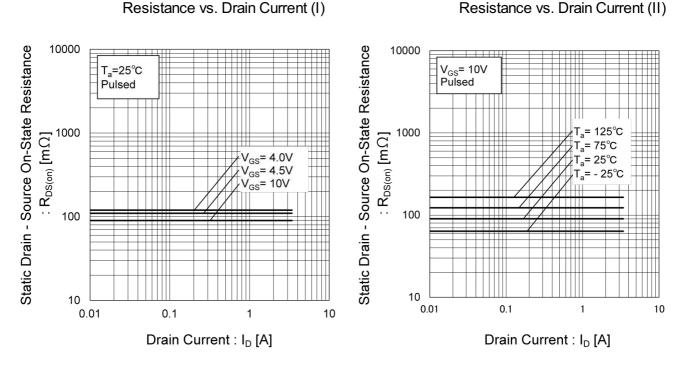


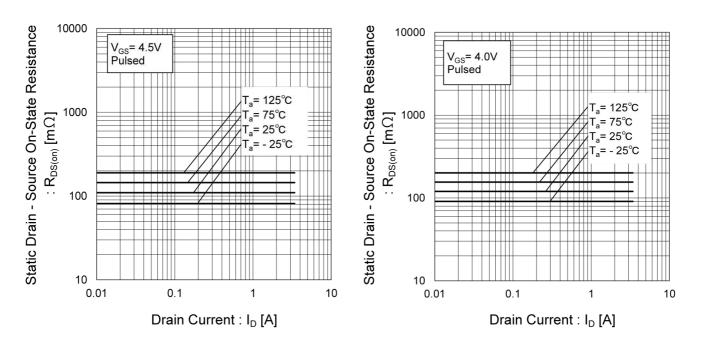
Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

Fig.16 Static Drain - Source On - State

Resistance vs. Drain Current (III)

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

Fig.15 Static Drain - Source On - State





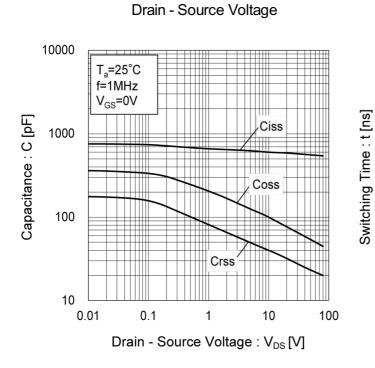


Fig.18 Typical Capacitance vs.

Fig.19 Switching Characteristics

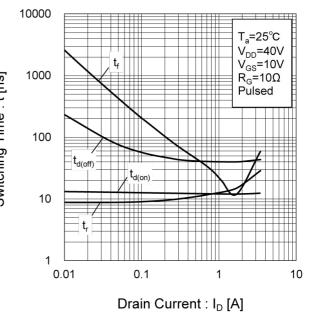
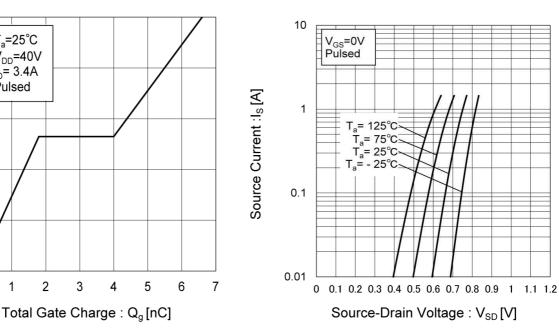


Fig.20 Dynamic Input Characteristics

Fig.21 Source Current vs. Source Drain Voltage



1

2

3

4

5

5

4

3

2

1

0

0

Gate - Source Voltage : V_{GS} [V]

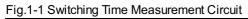
T_a=25°C

V_{DD}=40V

I_D= 3.4A Pulsed



•Measurement circuits <It is the same for the Tr1 and Tr2>



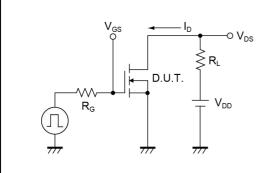


Fig.2-1 Gate Charge Measurement Circuit

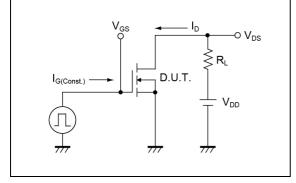
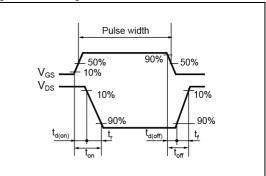
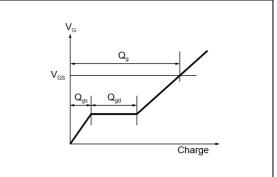


Fig.1-2 Switching Waveforms



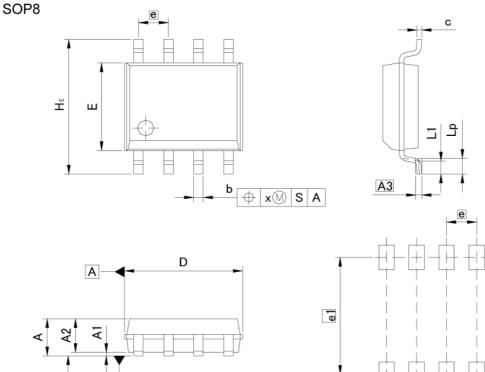


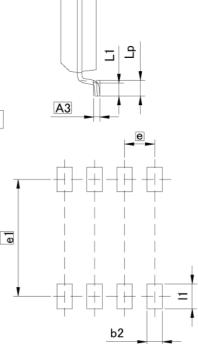






Dimensions





Pattern of terminal position areas [Not a pattern of soldering pads]

	MILIM	ETERS	INC	HES
172373330	MIN	MAX	MIN	MAX
A	<u></u> 23	1.75	-	0.069
A1	0.	15	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.25		0.0	10
b	0.30	0.50	0.012	0.020
с	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1.	27	0.0	50
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
x	0.15		0.0	06
у	0.10		0.0	04

		ETERS	INC	HES
	MIN	MAX	MIN	MAX
b2	, .	0.65	77 4	0.026
e1	5.15		0.1	203
11		1.15	<u>77</u> 3	0.045

Dimension in mm/inches

S y s



Notice

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(Note1) Medical Equipment Classification of the S	pecific Applications
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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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SH8K41 - Web Page

Distribution Inventory

Part Number	SH8K41
Package	SOP8
Unit Quantity	2500
Minimum Package Quantity	2500
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes