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## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



# 4V Drive Nch+Nch MOSFET

## SH8K4

### ●Structure

Silicon N-channel MOSFET

### ●Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small surface Mount Package (SOP8).

### ●Application

Power switching, DC / DC converter.

### ●Packaging specifications

Type	Package	Taping
	Code	TB
	Basic ordering unit (pieces)	2500
SH8K4		○

### ●Absolute maximum ratings (Ta=25°C)

<It is the same ratings for the Tr1 and Tr2.>

Parameter	Symbol	Limits	Unit	
Drain-source voltage	$V_{DSS}$	30	V	
Gate-source voltage	$V_{GSS}$	$\pm 20$	V	
Drain current	Continuous	$I_D$	$\pm 9.0$	A
	Pulsed	$I_{DP}$ *1	$\pm 36$	A
Source current (Body diode)	Continuous	$I_S$	1.6	A
	Pulsed	$I_{SP}$ *1	6.4	A
Total power dissipation	$P_D$ *2	2	W	
Channel temperature	$T_{ch}$	150	°C	
Storage temperature	$T_{stg}$	-55 to +150	°C	

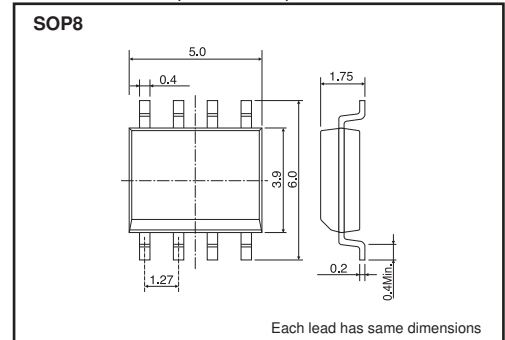
\*1  $P_w \leq 10\mu s$ , Duty cycle  $\leq 1\%$   
\*2 MOUNTED ON A CERAMIC BOARD.

### ●Thermal resistance

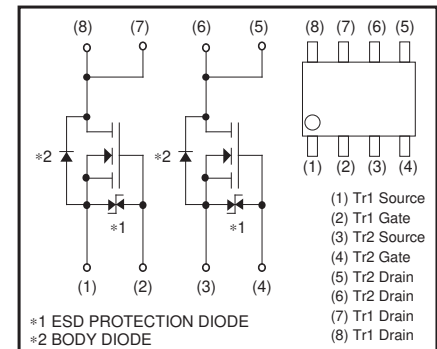
Parameter	Symbol	Limits	Unit
Channel to ambient	$R_{th}(ch-a)$ *	62.5	°C / W

\*MOUNTED ON A CERAMIC BOARD.

### ●Dimensions (Unit : mm)



### ●Inner circuit



\*1 ESD PROTECTION DIODE  
\*2 BODY DIODE  
\*A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

● **Electrical characteristics** (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I <sub>GSS</sub>	–	–	±10	μA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	V <sub>(BR) DSS</sub>	30	–	–	V	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	–	–	1	μA	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS(th)</sub>	1.0	–	2.5	V	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA
Static drain-source on-state resistance	R <sub>DS(on)</sub> *	–	12	17	mΩ	I <sub>D</sub> =9.0A, V <sub>GS</sub> =10V
		–	16	23		I <sub>D</sub> =9.0A, V <sub>GS</sub> =4.5V
		–	17	24		I <sub>D</sub> =9.0A, V <sub>GS</sub> =4V
Forward transfer admittance	Y <sub>fs</sub>  *	7.0	–	–	S	I <sub>D</sub> =9.0A, V <sub>DS</sub> =10V
Input capacitance	C <sub>iss</sub>	–	1190	–	pF	V <sub>DS</sub> =10V
Output capacitance	C <sub>oss</sub>	–	340	–	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	–	190	–	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	–	10	–	ns	I <sub>D</sub> =4.5A, V <sub>DD</sub> ≐15V
Rise time	t <sub>r</sub> *	–	15	–	ns	V <sub>GS</sub> =10V
Turn-off delay time	t <sub>d(off)</sub> *	–	55	–	ns	R <sub>L</sub> =3.33Ω
Fall time	t <sub>f</sub> *	–	22	–	ns	R <sub>G</sub> =10Ω
Total gate charge	Q <sub>g</sub> *	–	15	21	nC	V <sub>DD</sub> ≐15V
Gate-source charge	Q <sub>gs</sub> *	–	3.0	–	nC	V <sub>GS</sub> =5V
Gate-drain charge	Q <sub>gd</sub> *	–	6.1	–	nC	I <sub>D</sub> =9.0A

\*Pulsed

● **Body diode characteristics** (Source-Drain) (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V <sub>SD</sub> *	–	–	1.2	V	I <sub>S</sub> =6.4A, V <sub>GS</sub> =0V

\*Pulsed

●Electrical characteristic curves

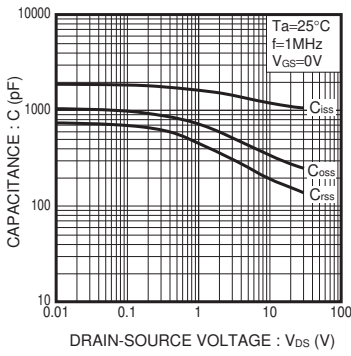


Fig.1 Typical Capacitance vs. Drain-Source Voltage

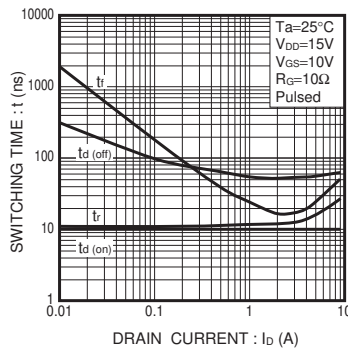


Fig.2 Switching Characteristics

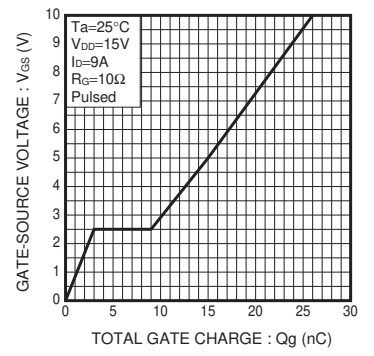


Fig.3 Dynamic Input Characteristics

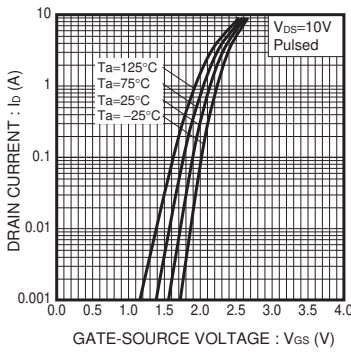


Fig.4 Typical Transfer Characteristics

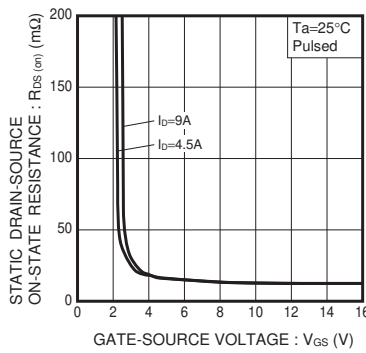


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

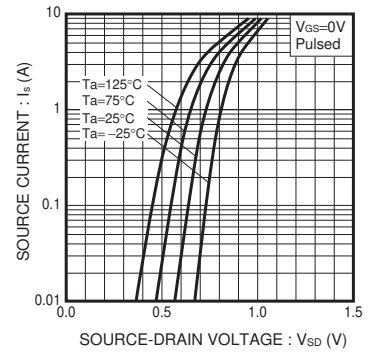


Fig.6 Source Current vs. Source-Drain Voltage

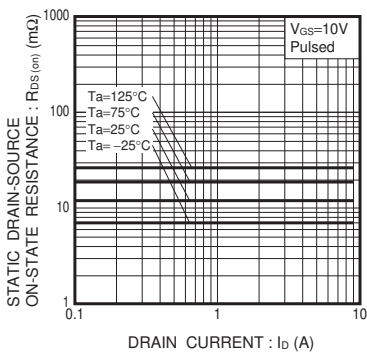


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

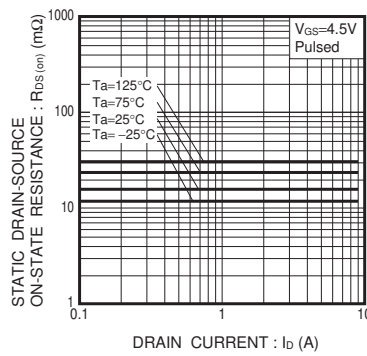


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

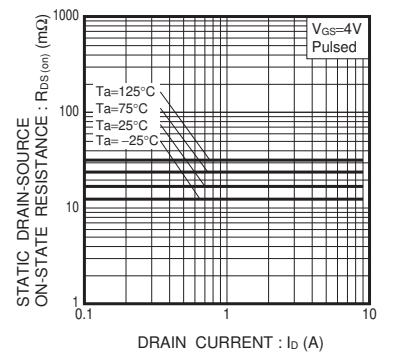


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

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