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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







SI-3000LU Series Surface-Mount, Low Current Consumption, Low Dropout Voltage

■Features

• Compact surface-mount package (SOT89-5)

• Output current: 250 mA

• Low current consumption Iq (OFF) $\leq 1 \mu A$ (Vc = 0 V)

 Low dropout voltage: V_{DIF} ≤ 0.5 V (at Io = 250 mA)

• Output voltage range (1.5V to 15V)

Built-in drooping-type-overcurrent and thermal protection circuits

■Absolute Maximum Ratings

(Ta=25°C)

Parameter	Symbol	Ratings	Unit
DC Input Voltage	VIN	18	V
Output control terminal voltage	Vc	Vin	V
DC Output Current	lo	250	mA
Power Dissipation	Po*1	0.75	W
Junction Temperature	Tj ^{*2}	-40 to +135	°C
Storage Temperature	T _{stg} *2	-40 to +125	°C
Thermal Resistance (Junction to Ambient Air)	θ j-a ^{*1}	146	°C/W

^{*1:} When mounted on glass-epoxy board 40 \times 40 mm (copper laminate area 2%).

■Applications

- Auxiliary power supplies for PC
- · Battery-driven electronic equipment

■Recommended Operating Conditions

Parameter	Symbol	Rat	11.5	
		min.	max.	Unit
Input Voltage	Vin	*2, *3	Vo+2*1	V
DC Output Current	lo	0	250	mA
Operating Ambient Temperature	Top	-20	85	°C

^{*1:} V_{IN} (max) and Io (max) are restricted by the relation P_D = (V_{IN} - V_O) × Io. Calculate these values referring to the reference data on page 69.

■Electrical Characteristics

(Ta=25°C, Vc=2V, unless otherwise specified)

				(Ta=25 G, VC=2V, unite	33 Other Wise Specified		
Parameter		Ratings					
	Symbol	SI-3012LU(Variable)			Unit		
		min.	typ.	max.			
Reference Voltage	VADJ	1.210	1.250	1.290	v		
	Conditions		v				
Dropout Voltage	VDIF			0.3			
	Conditions	lo=100mA(Vo=3.3V)					
				0.5	V		
	Conditions		lo=250mA(Vo=3.3V)				
Line Regulation	ΔVLINE			10			
	O - m distingue	V _{IN} =V _{O+} 1 to V _{O+} 5V,			mV		
	Conditions		Io=10mA(Vo=3.3V)				
	ΔVLOAD			20			
Load Regulation	Conditions		Vin=Vo+1V,		mV		
	Conditions		lo=1 to 250mA(Vo=3.3V)				
Temperature Coefficient of	ΔVο/ΔΤα		±0.3		mV/°C		
Reference Voltage	Conditions		T _j =0 to 100°C		mv/°C		
Ripple Rejection	RREJ		55		dB		
	Conditions	V _{IN} =Vo+1V, f=100 to 120Hz(Vo=3.3V)					
	Conditions						
Current Cor	Iq			150	μΑ		
	Conditions	VIN=Vo+1V, Io=0mA					
			Vc=2V, R2=100kΩ				
Circuit Current at Output OFF	Iq(OFF)		VIN=Vo+1V, Vc=0V	1	μΑ		
	Conditions		μΛ				
Overcurrent Protection Is1		260			mA		
Starting Current*1	Conditions		V _{IN} =V _O +1V		IIIA		
Control Voltage (Output ON)	^{*2} Vc, IH	2.0			v		
Control Voltage (Output OFF)*	Vc, IL			0.8	v		
Vc Terminal Control Current (Output ON)	Ic, IH			40			
	Conditions	Vc=2V			μΑ		
Control Current (Output OFF)	Ic, IL		0	- 5			
	Conditions		Vc=0V		μΑ		

^{*1:} Is 1 is specified at the 5% drop point of output voltage Vo on the condition that $V_{IN} = 3.3 \text{ V}$, and $I_{O} = 10 \text{ mA}$.

^{*2:} Thermal protection circuits may operate if the junction temperature exceeds 135°C.

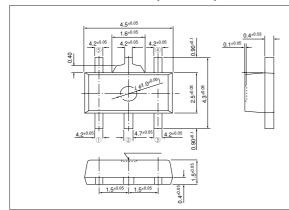
^{*2:} Refer to the Dropout Voltage parameter.

^{*3:} For the SI-3012LU, set the input voltage to Vin ≥ 2.4 V, and secure the minimum voltage as explained in "Setting DC Input Voltage" section in Linear Regulator Application Note.

^{*2:} Output is OFF when the output control terminal (Vc terminal) is open. Each input level is equivalent to LS-TTL level. Therefore, the device can be driven directly by LS-TTLs.

■External Dimensions (SOT89-5)

(Unit:mm)

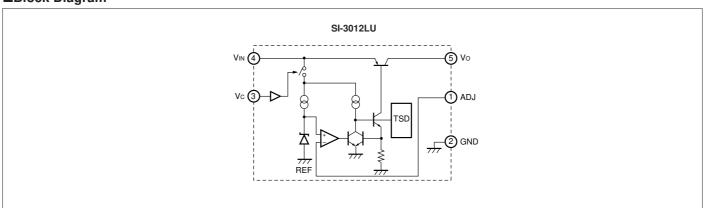


Pin Assignment

- ① ADJ
- ② GND
- 3 Vc
- 4 VIN
- ⑤ Vo

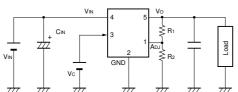
Plastic Mold Package Type Flammability: UL94V-0 Product Mass: Approx. 0.05g

■Block Diagram



■Typical Connection Diagram





Co: Output capacitor (10 μ F or larger)

For SI-3000LU series, Co has to be a low ESR capacitor such as a ceramic capacitor.

C_{IN}: Input capacitor (10 μ F approx.)

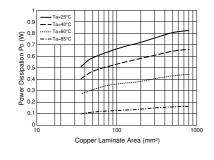
• Setting of SI-3012LU output voltage (recommended voltage: 1.5 V to 15 V) R1 and R2: Resistors for output setting

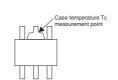
The output voltage can be set by connecting R1 and R2 as shown in the diagram on the left.

R2: 100 k Ω is recommended R1=(Vo-VaDJ)/(VaDJ/R2)

■Reference Data

Copper Laminate Area vs Power Dissipation T_j=100°C PCB size 40×40





- A monolithic ICs mounts an inner frame stage that is connected to the GND pin (pin 2). Therefore, enlarging the copper laminate area connected to the GND pin improves heat radiation effect.
- Obtaining the junction temperature

Measure the temperature Tc at the lead part of the GND pin (pin 2) with a thermocouple, etc. Then, substitute this value in the following formula to obtain the junction temperature.

 $T_{j}=P_{D}\times\theta j-c+Tc$ $(\theta j-c=5^{\circ}C/W)$