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SI-3000LSA Series Surface-Mount, Low Current Consumption, Low Dropout Voltage

■Features

- · Compact surface-mount package (SOP8)
- · Output current: 1 A
- Low circuit current at output OFF: $Iq(OFF) \le 1$ $\mu A (Vc = 0 V)$
- Low dropout voltage: VDIF≤0.8 V (at Io = 1 A) VDIF ≤ 1.2 V (IO = 1 A) for SI-3018LSA
- 4 types of output voltages (1.8 V, 2.5 V, 3.3 V, 5.0 V) available
- Output ON/OFF control terminal voltage compatible with LS-TTL
- · Built-in foldback-type-overcurrent and thermal protection circuits

Applications

- · Auxiliary power supplies for PC
- · Battery-driven electronic equipment

Recommended Operating Conditions

Parameter	Symbol					
		SI-3018LSA	SI-3025LSA	SI-3033LSA	SI-3050LSA	Unit
DC Input Voltage Range	Vin	3.1 to 3.5 ^{*1}	*2 to 3.5*1	*2 to 5.2*1	*2 to 8.0	V
DC Output Current Range	lo		A			
Operating Junction Temperature	Tjop		°C			
Operating Ambient Temperature	Taop		°C			

*1: VIN (max) and Io (max) are restricted by the relation $P_{D} = (V_{IN} - V_{O}) \times I_{O}$.

Please calculate these values referring to the reference data on page 71.

*2: Refer to the Dropout Voltage parameter.

Electrical Characteristics

		Ratings													
Parameter	Symbol	SI-3018LSA		SI-3025LSA		SI-3033LSA			SI-3050LSA			Unit			
		min.	typ.	max.	min.	typ.	max.	min.	typ.	max.	min.	typ.	max.		
Output Voltage	Vo	1.764	1.800	1.836	2.450	2.500	2.550	3.234	3.300	3.366	4.90	5.00	5.10	v	
Output voltage	Conditions	VIN=3.3V, IO=0.5A		VIN=3.3V, IO=0.5A		VIN=5V, IO=0.5A		VIN=6V, IO=0.5A			V				
Dropout Voltage	VDIF		-				0.4			0.4			0.4	_	
	Conditions	-		lo≤0.5A		lo≤0.5A		lo <i>≤</i> 0.5A			v				
			0.6	1.2			0.8			0.8			0.8	Ň	
	Conditions						lo≤	1A							
Line Regulation	ΔVLINE		2	10		2	10		3	10		3	15	- mV	
Line negulation	Conditions	VIN=3.1	to 3.5V,	lo=0.3A	VIN=3.	1 to 3.5V, I	o=0.3A	VIN=4.	5 to 5.5V, I	o=0.3A	Vin=	6 to 7V, lo	=0.3A	111 V	
Load Regulation			10	20		10	20		10	20		10	30	- mV	
	Conditions	Vin=3	3.3V, lo=0	to 1A	VIN=3	3.3V, lo=0	to 1A	Vin=	5V, lo=0 t	o 1A	Vin=	=6V, lo=0 t	o 1A		
Temperature Coefficient of	ΔVο/ΔTa		±0.3			±0.3			±0.3			±0.5		mV/°C	
Output Voltage	Conditions	s VIN=3.3V, Io=5mA, Tj=0		0 to 100°C	VIN=3.3V, Io=5mA, Tj=0 to 100°C		VIN=5V, Io=5mA, Tj=0 to 100°C		VIN=6V, Io=5mA, Tj=0 to 100°C						
Ripple Rejection	RREJ		60			57			55			55		dB	
	Conditions	VIN=3.3	V, f=100 t		VIN=3.3	V, f=100 t		VIN=5	/, f=100 to	-	VIN=6V, f=100 to 120Hz		45		
Quiescent Circuit Current	lq		1.7	2.5		1.7	2.5		1.7	2.5		1.7	2.5	mA	
	Conditions	Vin	=3.3V, Io=	=0A	Vin	=3.3V, lo=	0A	V	N=5V, Io=0)A	V	IN=6V, IO=	0A		
Circuit Current at Output OFF	Iq(OFF)			1			1			1			1	μA	
Circuit Guirein at Output OFF	Conditions	VIN=3.3	3V, lo=0A,	Vc=0V	VIN=3.3	3V, lo=0A,	Vc=0V	VIN=5	V, Io=0A, V	Vc=0V	VIN=6	V, Io=0A,	Vc=0V	μη	
Overcurrent Protection	ls1	1.2			1.2			1.2			1.2			A	
Starting Current*1,3 Conditions		VIN=3.3V		VIN=3.3V		VIN=5V		VIN=6V							
Control Voltage (Output ON)*	Vc, IH	2.0			2.0			2.0			2.0			v	
Control Voltage (Output OFF)	² Vc, IL			0.8			0.8			0.8			0.8	, v	
Vc Terminal Control Current (Output ON)	Ic, IH		40	80		40	80		40	80		40	80	μΑ	
	Conditions							=2V			-			μ	
Control Current (Output OFF)	Ic, IL		0	-5		0	-5		0	-5		0	-5	μΑ	
	Conditions						Vc=	=0V						μη	

*1: Is1 is specified at the 5% drop point of output voltage Vo on the condition that VIN = 3.3 V (5 V for SI-3033LSA), and Io = 0.5 A.

*2: Output is OFF when the output control terminal VC is open. Each input level is equivalent to LS-TTL level. Therefore, the device can be driven directly by LS-TTLs.

*3: These products cannot be used in the following applications. Because these applications require a certain current at start-up and so the built-in foldback-type overcurrent protection may cause errors during start-up stage.

(1) Constant current load (2) Positive and negative power supply (3) Series-connected power supply (4) Vo adjustment by raising ground voltage

Absolute Maximum Ratings (Ta=25°						
Parameter	Symbol	Ratings	Unit			
DC Input Voltage	VIN	16	V			
Output control terminal voltage	Vc	VIN	V			
DC Output Current	lo	1	A			
Power Dissipation	PD1 ^{*1}	1.16	W			
	PD2 ^{*2}	1.1	W			
Junction Temperature	Tj*3	-30 to +150	°C			
Operating Ambient Temperature	Top	-30 to +150	°C			
Storage Temperature	Tstg	-30 to +150	°C			
Thermal Resistance (Junction to Lead (pin 8))	θj-L	36	°C/W			
Thermal Resistance (Junction to Ambient Air)	θ_{j-a}^{*2}	100	°C/W			

(Ta=25°C, Vc=2V, unless otherwise specified)

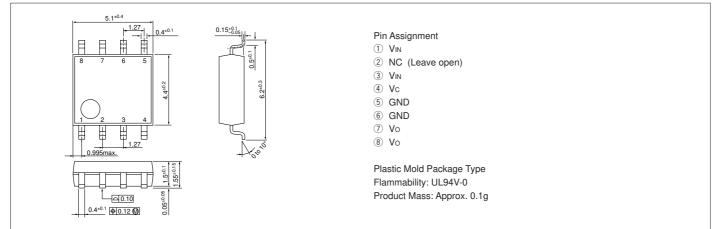
*1: When mounted on glass-epoxy board 56.5 × 56.5 mm (copper laminate area 100%).

*2: When mounted on glass-epoxy board 40 × 40 mm (copper laminate area 100%).

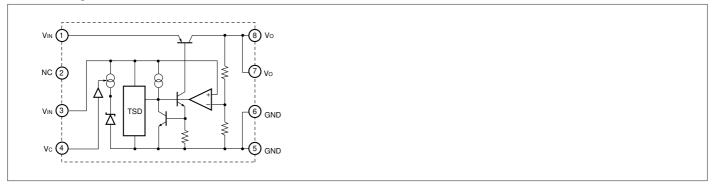
*3: Thermal protection circuits may be activated if the junction temperature exceeds 135°C.

(Unit : mm)

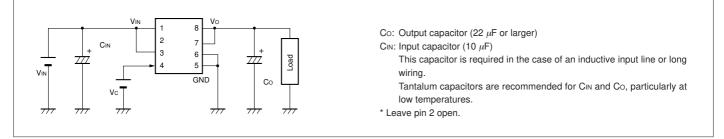
■External Dimensions (SOP8)



Block Diagram



■Typical Connection Diagram



■Reference Data

