

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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PATENTED: THIS PRODUCT IS COVERED BY U.S. PATENT 5,736,910

DOCUMENT / PART NO.: SI-52008-F

TITLE: 10/100BT,PoE; TAB UP, SHIELDED

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| ISSUE | | DE | SCRI | PTIOI | N OF (| CHAN | IGE | | ı | REVIE | WED | BY | Al | PPRO | VED | BY | EC | N# |
| A0 | USE NE | W FORM | IAT AND | PRODU | CTION RI | ELEASE | | | | David | d Cha | n | D | anny | Kwa | n | 062 | 57 |
| В0 | Update | drawing | format,re | emove P | IN#6 fron | ı footpri | nt | | | Bai | n Liu | | S | teve ' | Welle | er | 063 | 46 |
| B1 | Update the circuit drawing | | | | | | Bain Liu | | | Steve Weller | | | 06383 | | | | | |
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RJ45

ELECTRICAL CHARACTERISTICS @ 25°C

1.0 TURNS RATIO: (P1-P3): (J1-J2) (P4-P5): (J3-J6)

1CT : 1CT ±2% 1CT : 1CT ±2%

2.0 DC RESISTANCE:

3.0 INDUCTANCE:

 (P1−P3)
 350uH MIN. @ 0.1V, 100KHz, 8mA DC BIAS

 (P4−P5)
 350uH MIN. @ 0.1V, 100KHz, 8mA DC BIAS

4.0 LEAKAGE INDUCTANCE:

(P4−P5) WITH J6 AND J3 SHORT 0.3uH MAX. @ 1MHz (P1−P3) WITH J2 AND J1 SHORT 0.3uH MAX. @ 1MHz

5.0 INTERWINDING CAPACITANCE:

(P4,P5) TO (J3,J6) 60pF TYP @ 1MHz (P1,P2,P3) TO (J1,J2) 60pF TYP @ 1MHz

6.0 RETURN LOSS: (P1-P3)=100 OHMS REF. AND (P4-P5)=100 OHM REF.

 1MHz
 TO 30MHz

 30MHz
 TO 60MHz

 60MHz
 TO 80MHz

 12dB MIN.
 12dB MIN.

7.0 DIELECTRIC WITHSTAND:

(J3,J6) T0 (P4,P5) 1500Vms (J1,J2) T0 (P1,P3) 1500Vrms

8.0 INSERTION LOSS: RS=RL=100 OHMS

1MHz-65MHz -1dB MAX

9.0 RISE TIME: RS=RL=100 OHMS OUTPUT VOLTAGE = 1V PEAK

OUTPUT VOLTAGE = 1V PEAK 3.0nS MAX PULSE WIDTH = 112nS 3.0nS MAX

10.0 CROSS TALK:

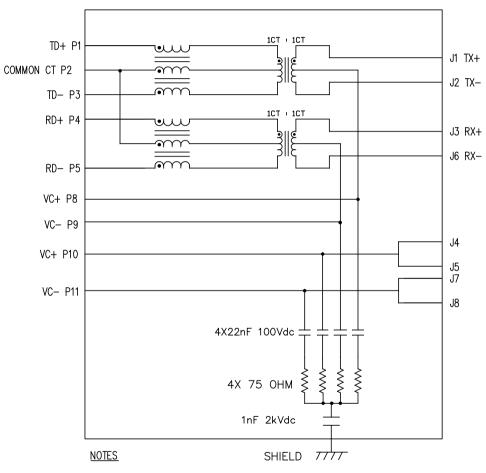
IHz-65MHz -35dB MIN.

TITLE

11.0 CM TO CM ATTENUATION:

12.0 BALANCED DC LINE CURRENT: 35

350mA MAX @ 57 VDC CONTINUOUS 500mA MAX @ 57 VDC FOR 200 MILLSECONDS



SCHEMATIC

1.0 DESIGNED TO SUPPORT IEEE 802.3

| ORIGINATED BY | DATE |
|---------------|------------|
| Zeng Xiaochun | 2012-04-18 |
| DRAWN BY | DATE |
| Duan Jun | 2012-04-18 |

MagJack®
10/100BT,PoE
TAB UP,SHIELDED
PATENTED

| PART NO. / DRAWING NO. | STANDA | RD DIM. | [] METRIC DIM. AS REFERENCE | | | |
|------------------------|--------------|---------|-----------------------------|----------|--|--|
| SI-52008-F | TOL. IN INCH | | UNIT : INCH [mm] | REV.: B1 | | |
| FILE NAME | .X | | SCALE: N/A | SIZE: A4 | | |
| SI-52008-F.DWG | .XX | | DOALE: IUA | | | |
| 31-32008-F.DVVG | .XXX | ±0.005 | | PAGE: 2 | | |



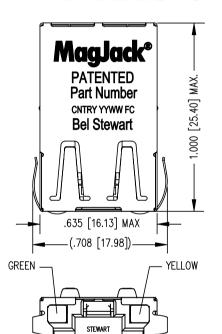
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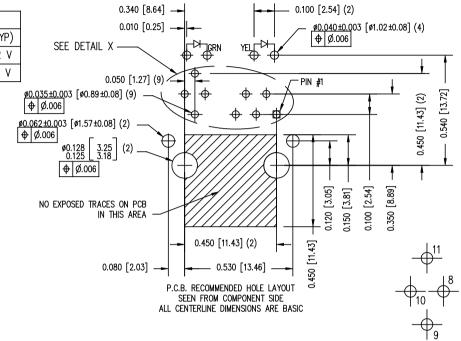
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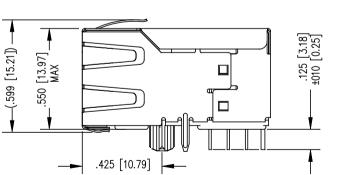


| LED SPECIFICATION | | | | | | | |
|-------------------|------------|----------------|---------|--|--|--|--|
| STANDARD LED | WAVELENGTH | FOWARD V (MAX) | * (TYP) | | | | |
| GREEN | 565 nm | 2.5 V | 2.2 V | | | | |
| YELLOW | 590 nm | 2.5 V | 2.1 V | | | | |

*WITH A FORWARD CURRENT OF 20 mA (TYP)







NOTES:

1. CONNECTOR MATERIALS: HOUSING: THERMOPLASTIC UL94 V-0 CONTACT/SHIELD: COPPER ALLOY SHIELD PLATING: NICKEL OR TIN

CONTACT PLATING: 50 MICRO-INCH SELECTIVE HARD GOLD PLATING OR EQUIVALENT IN CONTACT AREA

DETAIL X

- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- 5. REFLOW AND WAVE SOLDER COMPATIBLE -260°C FOR 10 SECONDS MAX.

| ORIGINATED BY | DATE |
|---------------|------------|
| Zeng Xiaochun | 2012-04-18 |
| DRAWN BY | DATE |
| Duan Jun | 2012-04-18 |

MagJack® 10/100BT,PoE **TAB UP.SHIELDED** PATENTED

PIN: J1

TITLE

PART NO. / DRAWING NO. STANDARD DIM. [] METRIC DIM. AS REFERENCE SI-52008-F **TOL. IN INCH** UNIT: INCH [mm] X. FILE NAME SCALE: N/A XX. SI-52008-F.DWG XXX. ±0.005



В1

3

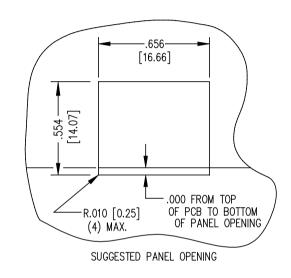
REV.:

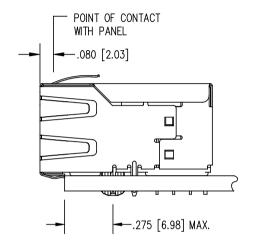
SIZE: A4

PAGE:

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- THE SUGGESTED PANEL OPENING IS INTENDED
 TO GIVE THE USER THE ABILITY TO HAVE
 REASONABLE JACK / PANEL CLEARANCES
 YET MAINTAIN RELIABLE GROUNDING
 CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

| ORIGINATED BY | DATE | TITLE |
|---------------|------------|-------|
| Zeng Xiaochun | 2012-04-18 | |
| DRAWN BY | DATE | |
| Duan Jun | 2012-04-18 | |

MagJack®
10/100BT,PoE
TAB UP,SHIELDED
PATENTED

| PART NO. / DRAWING NO. | STANDA | RD DIM. | [] METRIC DIM. AS REFERENCE | | | |
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