

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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RJ45

J1 TX+

J2 TX-

J3 RX+

J6 RX-

J4

J5

J7

J8

ELECTRICAL CHARACTERISTICS @ 25°C

OPERATING TEMPERATURE RANGE: -40° C TO +85° C

1.0 TURNS RATIO: (P4-P5-P6): (J3-J6) (P1-P2-P3) : (J1-J2) : 1CT : 1CT ± 3% : 1CT : 1CT ± 3%

2.0 INDUCTANCE: (P4-P6) (P3-P1) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias TD-P3 : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT): 0.3uH MAX. @ 1MHz P3-P1 (WITH J2 AND J1 SHORT): 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P6,P5,P4) TO (J6,J3) : 30pF MAX. @ 1MHz (P3.P2.P1) TO (J2.J1) : 30pF MAX. @ 1MHZ

5.0 DC RESISTANCE: (J6-J3)=(J2-J1)

: 1.5 ohms Max.

6.0 RETURN LOSS: (P4-P6)=100 OHMS AND (P1-P3)=100 OHM REF.

1MHz TO 30MHz 60MHz TO 80MHz

: 18dB MIN. : 12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J3-J6).

7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3)

: 1500 Vrms (J3, J6) TO (P4, P6) : 1500 Vrms

8.0 INSERTION LOSS: RS=RL=100 ohms

100KHz TO 100MHz

: 1.1 dB TYP

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS

OUTPUT VOLTAGE = 1 V peak

TITLE

: 3.0 nS MAX

PULSE WIDTH= 112nS

: 3.0 nS MAX

10.0CROSS TALK: 1-100 MHz

: 40 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION:

NOTES

CHASSIS GROUND P8

1.0 COMPLIES WITH THE EUROPEAN DIRECTIVE 2002/95/EC, CALLING FOR THE

1000pF 2kV

4X 75 OHMS

30MHz TO 100MHz: 35dB TYP

ELIMINATION OF LEAD AND OTHER HAZARDOUS SUBSTANCES FROM ELECTRONIC PRODUCTS.

SCHEMATIC

2.0 UNUSED PIN P7 IS OMITTED.

PINS

TD+ P1

TCT P2

RD+ P4

RCT P5

RD- P6

ORIGINATED BY	DATE
Zeng Xiao Chun	2011-02-23
DRAWN BY	DATE

10/100BT TAB UP. SHIELDED

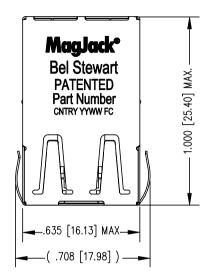
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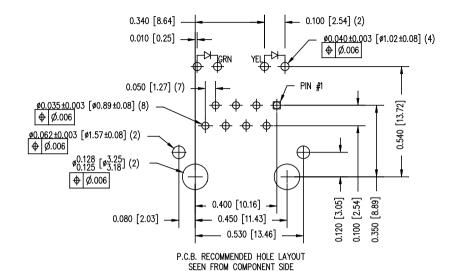
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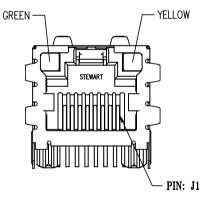
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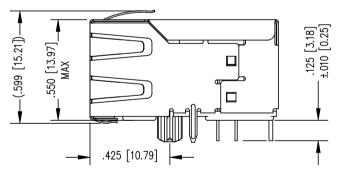






ALL CENTERLINE DIMENSIONS ARE BASIC





LED SPECIFICATION				
STANDARD LED WAVELENGTH FORWARD V (MAX) *(TYP)				
GREEN 565 nm 2.5 V 2.2 V				
YELLOW	590 nm	2.5 V	2.1 V	

*WITH A FORWARD CURRENT OF 20 mA (TYP)

NOTES:

- 1. CONNECTOR MATERIALS: HOUSING: THERMOPLASTIC UL94 V-0 CONTACT/SHIELD: COPPER ALLOY SHIELD PLATING: NICKEL OR TIN CONTACT PLATING: SELECTIVE GOLD,
 - 50 MICRO-INCHES MIN. IN CONTACT AREA.
- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- 5. REFLOW AND WAVE SOLDER COMPATIBLE-260°C FOR 10 SECONDS MAX.

ORIGINATED BY	DATE	TITLE
Zeng Xiao Chun	2011-02-23	
DRAWN BY	DATE	
Zhang Ru Sheng	2011-02-23	

MagJack[®]

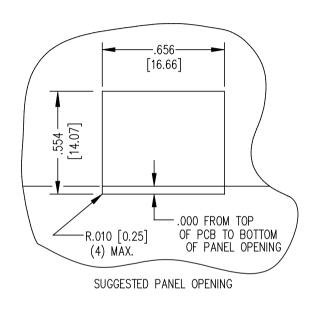
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PATENTED				

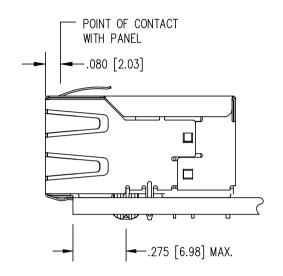
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- 1. THE SUGGESTED PANEL OPENING IS INTENDED

 TO GIVE THE USER THE ABILITY TO HAVE

 REASONABLE JACK / PANEL CLEARANCES

 YET MAINTAIN RELIABLE GROUNDING

 CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED

 TO BE ±.005 [0.13]

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DRAWN BY	DATE	
Zhang Ru Sheng	2011-02-23	

MagJack®
10/100BT
TAB UP, SHIELDED
PATENTED

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SI-55004-F	TOL. IN INCH		UNIT : INCH [mm]	REV.: 02
FILE NAME	x		SCALE: N/A	SIZE: A4
SI-55004-F.DWG	.XX			SIZE: A4
31-33004-F.DWG	.xxx	±0.005		PAGE: 4

