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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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Micro Commercial Components 20736 Marilla Street Chatsworth

CA 91311

Phone: (818) 701-4933 Fax: (818) 701-4939 **SI2302** 

# Halogen free available upon request by adding suffix "-HF"

- 20V,3.0A,  $R_{DS(ON)}=55m \Omega @V_{GS}=4.5V$  $R_{DS(ON)}$ =82m  $\Omega$  @ $V_{GS}$ =2.5V
- High dense cell design for extremely low  $R_{DS(ON)}$
- Rugged and reliable
- Lead free product is acquired
- SOT-23 Package

**Features** 

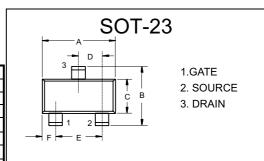
- Marking Code: S2 Epoxy meets UL 94 V-0 flammability rating

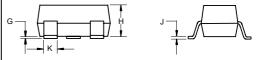
Moisture Sensitivity Level 1

## Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit
$V_{DS}$	Drain-source Voltage	20	V
$I_D$	Drain Current-Continuous	3	Α
I <sub>DM</sub>	Drain Current-Pulsed <sup>a</sup>	10	Α
$V_{GS}$	Gate-source Voltage	±8	V
$P_{D}$	Total Power Dissipation	1.25	W
R <sub>+JA</sub>	Thermal Resistance Junction to Ambient <sup>b</sup>	100	°C/W
$T_J$	Operating Junction Temperature	-55 to +150	$^{\circ}$
T <sub>STG</sub>	Storage Temperature	-55 to +150	$^{\circ}\mathbb{C}$

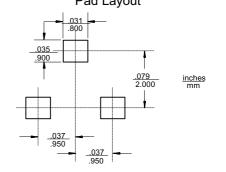
# **N-Channel Enhancement Mode Field Effect Transistor**



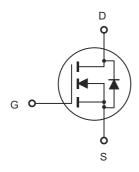


DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.110	.120	2.80	3.04	
В	.083	.104	2.10	2.64	
С	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
Е	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
Η	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	

## Suggested Solder Pad Layout



## **Internal Block Diagram**





# **SI2302**

## $\textbf{Electrical Characteristics} \quad \textbf{T}_{A} = 25^{\circ} \textbf{C} \text{ unless otherwise noted}$

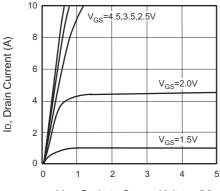
Parameter	Symbol	Test Condition	Min	Тур	Max	Units
Off Characteristics	•					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS} = 0V, I_{D} = 10\mu A$	20			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 20V, V_{GS} = 0V$			1	μA
Gate Body Leakage Current, Forward	I <sub>GSSF</sub>	$V_{GS} = 8V, V_{DS} = 0V$			100	nA
Gate Body Leakage Current, Reverse	Igssr	$V_{GS}$ = -8V, $V_{DS}$ = 0V			-100	nA
On Characteristics °						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{GS} = V_{DS}$ , $I_D = 50\mu A$	0.65		1.2	V
Static Drain-Source		$V_{GS} = 4.5V, I_D = 3.6A$		55	72	mΩ
On-Resistance	R <sub>DS(on)</sub>	$V_{GS} = 2.5V, I_D = 3.1A$		82	110	mΩ
Forwand Transconductance	9 <sub>FS</sub>	$V_{DS} = 5V, I_{D} = 3.6A$		8.5		S
Dynamic Characteristics d						
Input Capacitance	C <sub>iss</sub>			237		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0  MHz		120		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			45		pF
Switching Characteristics d						
Turn-On Delay Time	t <sub>d(on)</sub>			23	45	ns
Turn-On Rise Time	t <sub>r</sub>	$V_{DD} = 10V, I_D = 3.6A,$		11	30	ns
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS} = 4.5V$ , $R_{GEN} = 6\Omega$		34	70	ns
Turn-On Fall Time	t <sub>f</sub>			36	70	ns
Total Gate Charge	Qg	Q <sub>g</sub>		6	10	nC
Gate-Source Charge	Q <sub>gs</sub>			1.4		nC
Gate-Drain Charge	Q <sub>gd</sub>	.65		1.8		nC
Drain-Source Diode Characteristics and Maximun Ratings						
Drain-Source Diode Forward Current <sup>b</sup>	Is				0.94	Α
Drain-Source Diode Forward Voltage °	V <sub>SD</sub>	$V_{GS} = 0V, I_{S} = 0.94A$			1.2	V

Notes:

a.Repetitive Rating: Pulse width limited by maximum junction temperature.
b.Surface Mounted on FR4 Board, t s 10 sec.
c.Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
d.Guaranteed by design, not subject to production testing.



# SI2302



VDS, Drain-to-Source Voltage (V)

Figure 1. Output Characteristics

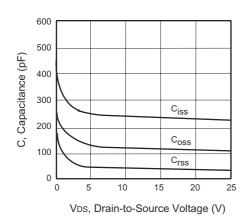


Figure 3. Capacitance

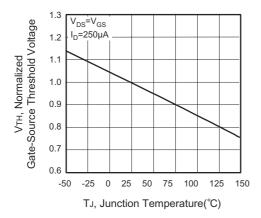
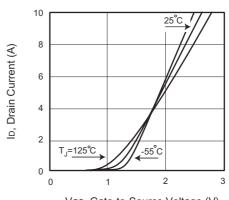


Figure 5. Gate Threshold Variation with Temperature



VGS, Gate-to-Source Voltage (V)

Figure 2. Transfer Characteristics

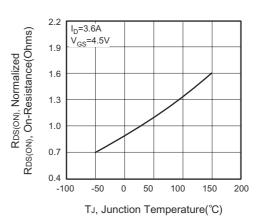
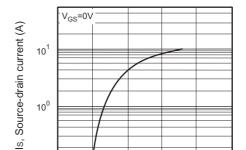


Figure 4. On-Resistance Variation with Temperature



10

0.2

Vsp, Body Diode Forward Voltage (V)

Figure 6. Body Diode Forward Voltage Variation with Source Current

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# SI2302

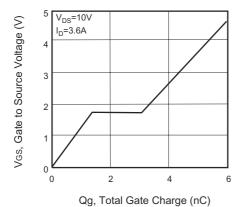


Figure 7. Gate Charge

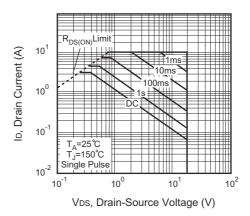


Figure 8. Maximum Safe Operating Area

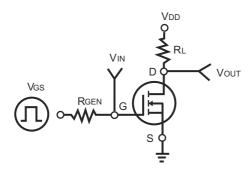


Figure 9. Switching Test Circuit

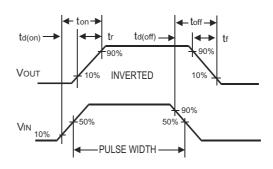


Figure 10. Switching Waveforms

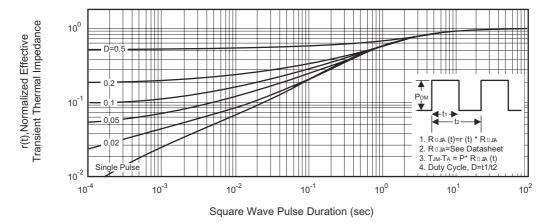


Figure 11. Normalized Thermal Transient Impedance Curve



## **Ordering Information:**

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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