# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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**Micro Commercial Components** 



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### **Features**

- Halogen free available upon request by adding suffix "-HF"
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- High power and current handing capability
- Surface mount package
- Marking Code: S10

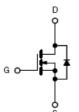
### SI2310

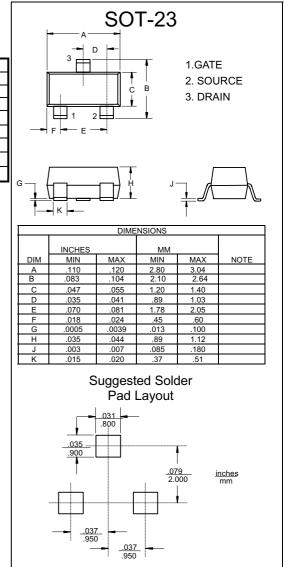
### N-Channel Enhancement Mode Field Effect Transistor

#### Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit	
V <sub>DS</sub>	Drain-source Voltage	60	V	
ID	Continuous Drain Current	3.0	A	
I <sub>DM</sub>	Pulsed Drain Current (note1)	10	A	
V <sub>GS</sub>	Gate-source Voltage	±20	V	
PD	Total Power Dissipation	0.35	W	
R <sub>☉JA</sub>	Thermal Resistance Junction to Ambient	357	°C/W	
TJ	Operating Junction Temperature	-55 to +150	0 °C	
T <sub>STG</sub>	Storage Temperature	-55 to +150	°C	

#### **Internal Block Diagram**







## SI2310

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
STATIC CHARACTERISTICS		·				
Drain-source breakdown voltage	V (BR)DSS	Vgs = 0V, Id =250µA	60			V
Zero gate voltage drain current	IDSS	V <sub>DS</sub> =60V,V <sub>GS</sub> = 0V			1	μA
Gate-body leakage current	lgss				±100	nA
Gate threshold voltage (note 3)	VGS(th)	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	0.5		2	V
	Desk	Vgs =10V, Id =3A			105	mΩ
Drain-source on-resistance (note 3)	RDS(on)	Vgs =4.5V, Id =3A			125	mΩ
Forward tranconductance (note 3)	<b>g</b> fs	VDS =15V, ID =2A	1.4			S
Diode forward voltage (note 3)	V <sub>SD</sub>	I <sub>S</sub> =3A, V <sub>GS</sub> = 0V			1.2	V
DYNAMIC CHARACTERISTICS (no	te 4)					
Input Capacitance	C <sub>iss</sub>			247		pF
Output Capacitance	Coss	VDS =30V,VGS =0V,f =1MHz		34		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			19.5		pF
SWITCHING CHARACTERISTICS (I	note 4)	·				
Turn-on delay time	td(on)			6		ns
Turn-on rise time	tr	$V_{GS}$ =10V, $V_{DD}$ =30V,		15		ns
Turn-off delay time	td(off)	$I_D$ =1.5A, $R_{GEN}$ =1 $\Omega$		15		ns
Turn-off fall time	tr			10		ns
Total Gate Charge	Qg			6		nC
Gate-Source Charge	Q <sub>gs</sub>	VDS =30V,VGS =4.5V,ID =3A		1		nC
Gate-Drain Charge	$Q_{gd}$			1.3		nC

#### ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Notes :

1. Repetitive rating : Pulse width limited by junction temperature.

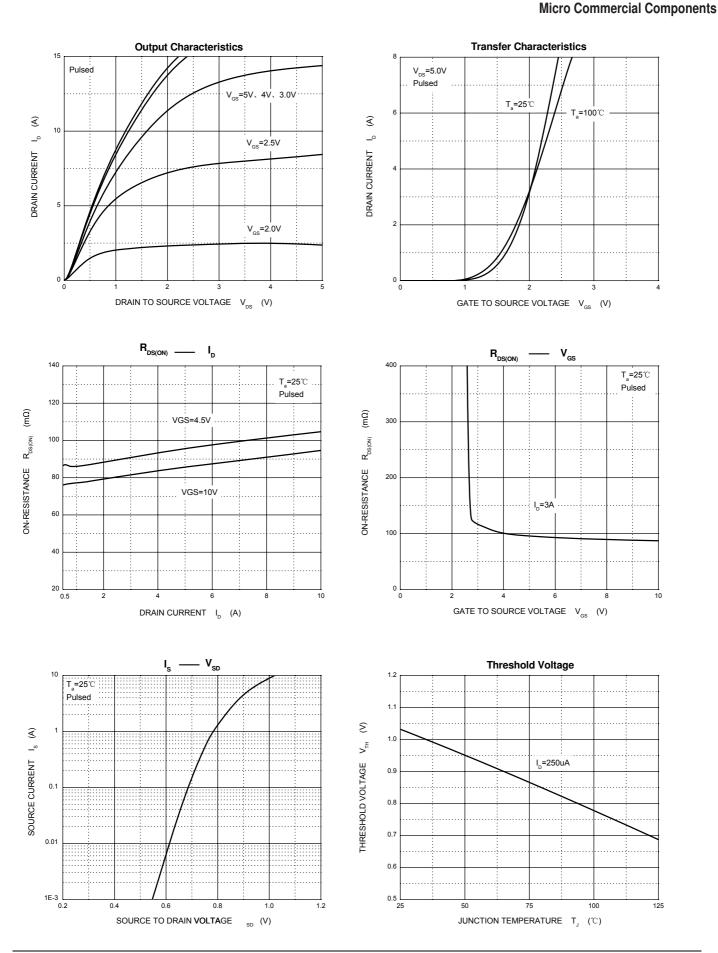
2. Surface mounted on FR4 board , t≤10s.

3. Pulse Test : Pulse Width≤300µs, Duty Cycle≤0.5%.

4. Guaranteed by design, not subject to producting.

# SI2310

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#### **Ordering Information :**

Device	Packing		
Part Number-TP	Tape&Reel: 3Kpcs/Reel		

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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