

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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SI3404

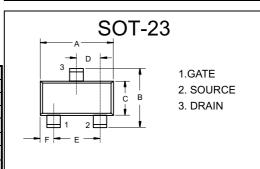
Features

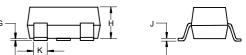
- High dense cell design for extremely low R_{DS(ON)}
- Rugged and reliable
- Lead free product is acquired
- SOT-23 Package
- Marking Code: R4
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Halogen free available upon request by adding suffix "-HF"

Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit	
V_{DS}	Drain-source Voltage	30	V	
I_D	Drain Current-Continuous	5.8	Α	
I _{DM}	Drain Current-Pulsed ^a	30	Α	
V_{GS}	Gate-source Voltage	±20	V	
P _D	Total Power Dissipation	0.35 W		
R⊕JA	Thermal Resistance Junction to Ambient ^b	357	°C/W	
T_J	Operating Junction Temperature	ınction Temperature -55 to +150 °C		
T _{STG}	Storage Temperature	-55 to +150 °C		

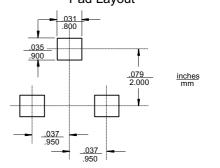
N-Channel Enhancement Mode Field Effect Transistor



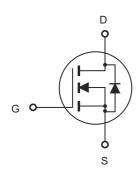


DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.110	.120	2.80	3.04	
В	.083	.104	2.10	2.64	
С	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
Е	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
Н	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	015	020	37	51	

Suggested Solder Pad Layout



Internal Block Diagram





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Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
STATIC PARAMETERS						
Drain-source breakdown voltage	V (BR) DSS	V _G S = 0V, I _D =250μA	30			V
Zero gate voltage drain current	Ipss	V _{DS} =30V,V _{GS} = 0V			1	μA
Gate-body leakage current	Igss	V _{GS} =±20V, V _{DS} = 0V			±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1		3	V
Drain source on registance (note 1)	Pro/)	V _{GS} =10V, I _D =5.8A			28	mΩ
Drain-source on-resistance (note 1)	RDS(on)	V _{GS} =4.5V, I _D =4.8A			42	mΩ
Forward tranconductance (note 1)	g FS	V _{DS} =5V, I _D =5.8A	5			S
Diode forward voltage	V_{SD}	I _S =1A			1	V
DYNAMIC PARAMETERS (note 2)	•					
Input capacitance	Ciss				820	pF
Output capacitance	Coss	V _{DS} =15V,V _{GS} =0V,f =1MHz		118		pF
Reverse transfer capacitance	Crss			85		pF
Gate resistance	Rg	V _{DS} =0V,V _{GS} =0V,f =1MHz			1.5	Ω
SWITCHING PARAMETERS(note 2)	•					
Turn-on delay time	td(on)				6.5	ns
Turn-on rise time	tr	V _{GS} =10V,V _{DS} =15V,		3.1		ns
Turn-off delay time	td(off)	R_L =2.6 Ω , R_{GEN} =3 Ω		15.1		ns
Turn-off fall time	tf			2.7		ns

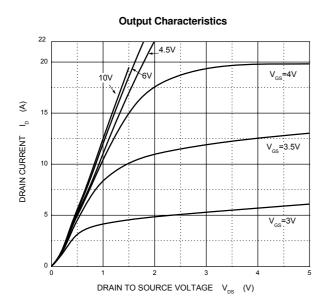
Note:

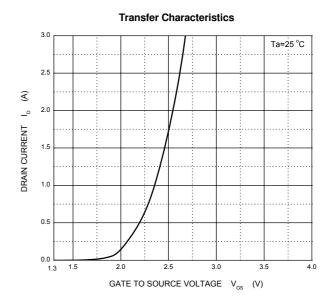
- 1. Pulse Test : Pulse width≤300µs, duty cycle≤0.5%.
- 2. These parameters have no way to verify.

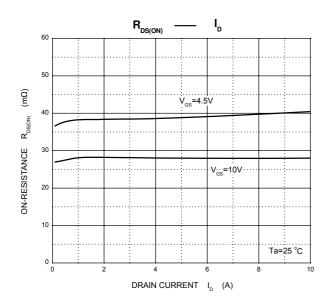


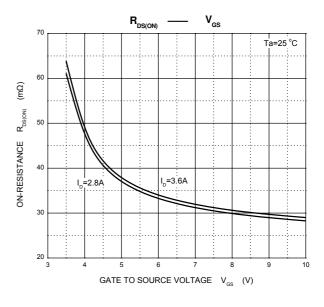
SI3404

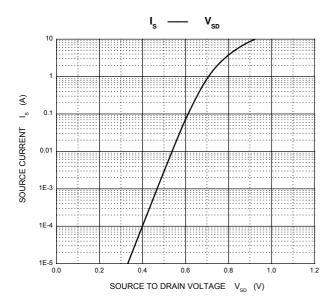
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Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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