

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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SI3434

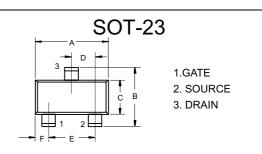
Features

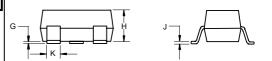
- High dense cell design for extremely low R_{DS(ON)}
- Rugged and reliable
- Lead free product is acquired
- SOT-23 Package
- Marking Code: R34
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Halogen free available upon request by adding suffix "-HF"

Maximum Ratings @ 25°C Unless Otherwise Specified

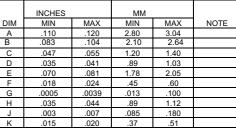
Symbol	Parameter	Rating	Unit	
V_{DS}	Drain-source Voltage	30	V	
I _D	Drain Current-Continuous	5.0	Α	
I _{DM}	Drain Current-Pulsed ^a	20	Α	
V_{GS}	Gate-source Voltage	±10	V	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient ^b	417	°C/W	
T_J	Operating Junction Temperature	-55 to +150	$^{\circ}\mathbb{C}$	
T _{STG}	Storage Temperature	-55 to +150	$^{\circ}$ C	

N-Channel Enhancement Mode Field Effect Transistor

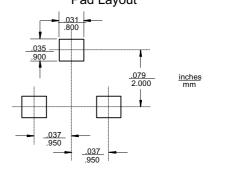




DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.110	.120	2.80	3.04	
В	.083	.104	2.10	2.64	
С	.047	.055	1.20	1.40	
О	.035	.041	.89	1.03	
Е	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
Ð	.0005	.0039	.013	.100	
Τ	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	



Suggested Solder Pad Layout



Internal Block Diagram





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Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	V(BR)DSS	V _{GS} = 0V, I _D =250µA	30			V
Zero gate voltage drain current	IDSS	V _{DS} =30V,V _{GS} = 0V			1	μA
Gate-body leakage current	Igss	V _{GS} =±10V, V _{DS} = 0V			±10	μA
Gate threshold voltage(note 1)	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.6		1	V
		V _{GS} =10V, I _D =5A			42	mΩ
Drain-source on-resistance (note 1)	RDS(on)	V _G S =4.5V, I _D =5A			44	mΩ
		Vgs =2.5V, ID =4A			50	mΩ
Forward tranconductance (note 1)	g FS	V _{DS} =5V, I _D =4A		15		S
Diode forward voltage (note 1)	V_{SD}	I _S =1A, V _{GS} = 0V			1	V
DYNAMIC PARAMETERS (note2)						
Input Capacitance	C_{iss}			245		pF
Output Capacitance	C_{oss}	V _{DS} =15V,V _{GS} =0V,f =1MHz		35		pF
Reverse Transfer Capacitance	C_{rss}			20		pF
SWITCHING PARAMETERS (note 2)					
Turn-on delay time	td(on)			2		ns
Turn-on rise time	tr	V _{DD} =15V,V _{GS} =10V		3.5		ns
Turn-off delay time	td(off)	R_L =3.75 Ω , R_{GEN} =3 Ω		22		ns
Turn-off fall time	tf			3.5		ns
Total Gate Charge	Qg				10	nC
Gate-Source Charge	Q _{gs}	V _{DS} =15V,V _{GS} =10V,I _D =4A		0.5		nC
Gate-Drain Charge	Q_{gd}			1		nC

Notes: 1. Pulse Test: Pulse width≤300µs, duty cycle≤0.5%.

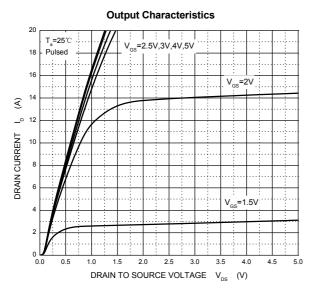
2. Guaranteed by design, not subject to production testing.

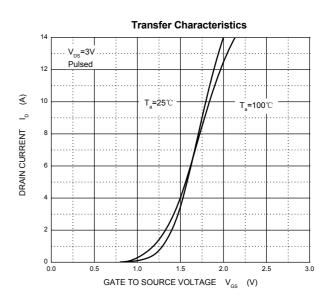
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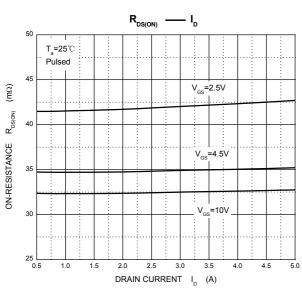


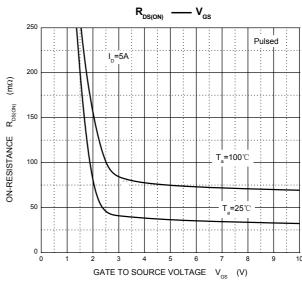
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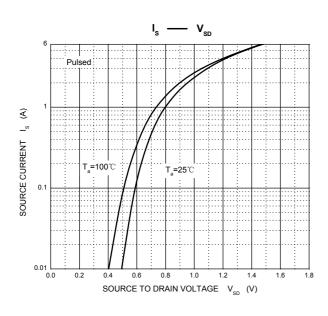
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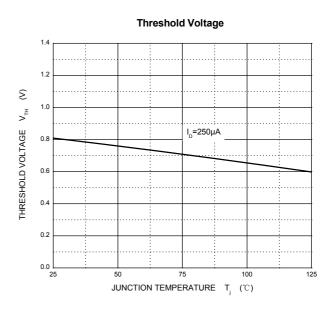














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Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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