# imall

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Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





Vishay Siliconix

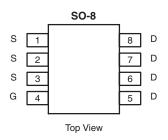
### N-Channel 20-V (D-S) MOSFET

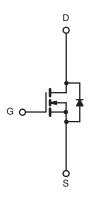
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)		
20	0.025 at V <sub>GS</sub> = 4.5 V	± 8.5		
	0.035 at V <sub>GS</sub> = 2.5 V	± 7.1		

### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC







Ordering Information: Si4426DY-T1-E3 (Lead (Pb)-free) Si4426DY-T1-GE3 (Lead (Pb)-free and Halogen free)

N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \degree C$ , unless otherwise noted							
Parameter		Symbol	10 s	Steady State	Unit		
Drain-Source Voltage		V <sub>DS</sub>	20		V		
Gate-Source Voltage		V <sub>GS</sub>	± 12		v		
Continuous Drain Current (T 150 °C) <sup>8</sup>	T <sub>A</sub> = 25 °C	1-	± 8.5	± 6.5	Α		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C	- <sup>I</sup> D	± 6.8	± 5.2			
Pulsed Drain Current (10 µs Pulse Width)		I <sub>DM</sub>	± 40		A		
Continuous Source Current (Diode Conduction) <sup>a</sup>		ا <sub>S</sub>	2.1	2.1			
Maximum Dawar Dissinctiona	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.5	1.5	W		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	'D	1.6	0.9	vv		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 t	o 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	R <sub>thJA</sub>	38	50	
Maximum Junction-to-Ambient	Steady State	' 'thJA	70	85	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	20	25	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	•		•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.6		1.4	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			1	μΑ
Zero Gate Voltage Drain Current	DSS	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS} = 4.5$ V	40			Α
	в	$V_{GS} = 4.5 \text{ V}, I_D = 8.5 \text{ A}$	3.5 A		0.025	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 7.1 A		0.025	0.035	Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 8.5 \text{ A}$		27		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 2.1 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V
Dynamic <sup>b</sup>			•			
Total Gate Charge	Qg			25	50	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 8.5 \text{ A}$		6.5		nC
Gate-Drain Charge	Q <sub>gd</sub>			4		
Turn-On Delay Time	t <sub>d(on)</sub>			40	60	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 10 $\Omega$		40	60	ns
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$		90	150	
Fall Time	t <sub>f</sub>			40	60	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.1 A, dl/dt = 100 A/μs		40	60	

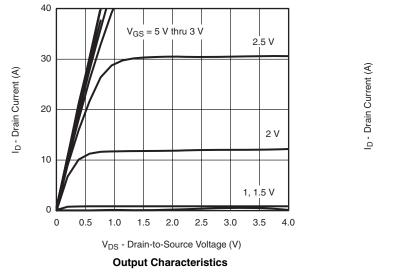
Notes:

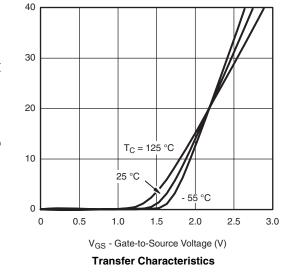
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

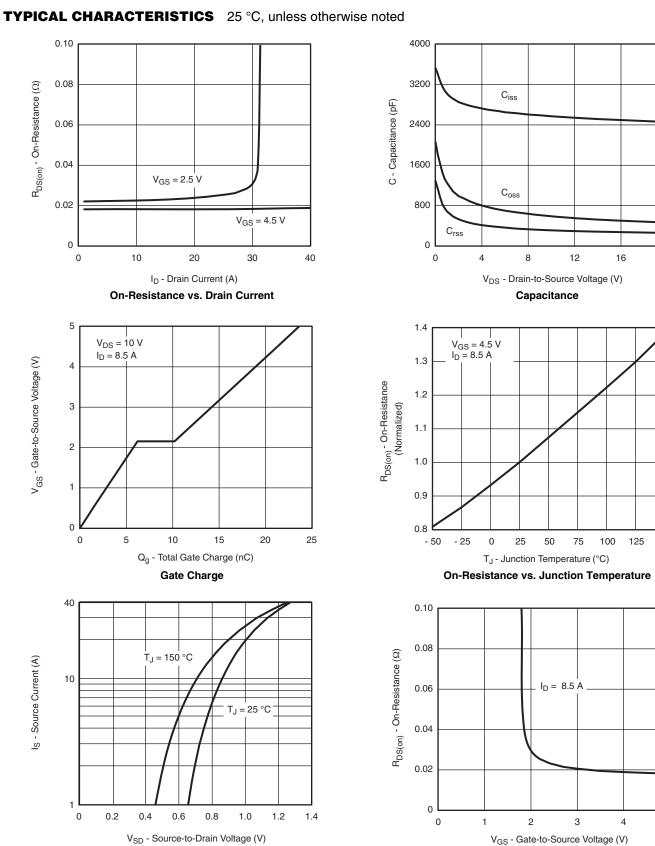
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Source-Drain Diode Forward Voltage

Document Number: 71107 S09-0767-Rev. D, 04-May-09 On-Resistance vs. Gate-to-Source Voltage

5

20

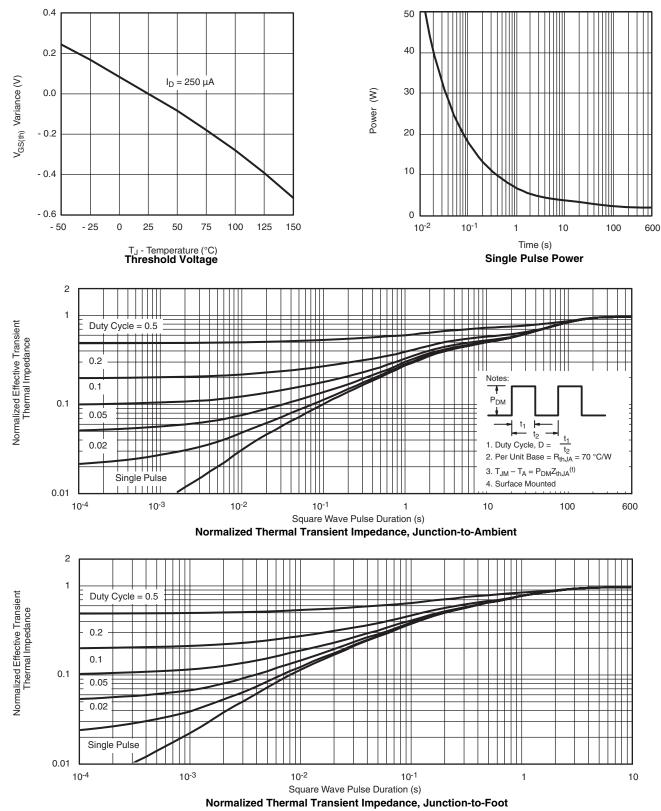
150

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### **Vishay Siliconix**





Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?71107">www.vishay.com/ppg?71107</a>.





## Package Information

Vishay Siliconix

## SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INCHES			
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

### **Application Note 826**

Vishay Siliconix



**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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