# imall

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## Contact us

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Vishay Siliconix

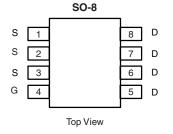
## N-Channel Reduced $Q_g$ , Fast Switching MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
30	0.007 at V <sub>GS</sub> = 10 V	16		
	0.010 at V <sub>GS</sub> = 4.5 V	13		

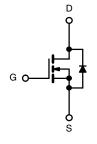
#### FEATURES

- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFET
- High-Efficiency PWM Optimized
- 100 % R<sub>g</sub> Tested









N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \text{ °C}$ , unles Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	16	11	٨	
	T <sub>A</sub> = 70 °C		13	8		
Pulsed Drain Current		I <sub>DM</sub>	± 50		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	3.0	1.40		
	T <sub>A</sub> = 25 °C	– P <sub>D</sub>	3.5	1.6	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		2.2	1.0	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R <sub>thJA</sub>	29	35	°C/W
Maximum Junction-to-Ambient (MOSFET) <sup>a</sup>	Steady State		65	80	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	15	18	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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SPECIFICATIONS T <sub>J</sub> = 25 $^{\circ}$	C, unless	otherwise noted					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.80		1.6	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	$_{\rm S} = 0 \text{ V}, \text{ V}_{\rm GS} = \pm 20 \text{ V}$		± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$\begin{tabular}{ c c c c c } \hline V_{DS} = 30 \ V, \ V_{GS} = 0 \ V \\ \hline V_{DS} = 30 \ V, \ V_{GS} = 0 \ V, \ T_J = 70 \ ^\circ C \\ \hline \end{tabular}$		1			
					5	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS} = 10$ V	40			А	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 16 A		0.0058	0.007	Ω	
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 13 A		0.008	0.010		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 16 \text{ A}$		38		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 3 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		0.74	1.1	V	
Dynamic <sup>b</sup>			•				
Total Gate Charge	Qg			16.3	24	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 5.0 V, $I_{D}$ = 16 A		4			
Gate-Drain Charge	Q <sub>gd</sub>			5.9		1	
Gate Resistance	Rg		0.5	1.5	2.6	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			14	20		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		10	15	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong \text{1}$ A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 6 $\Omega$		44	70		
Fall Time	t <sub>f</sub>			20	30		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = 3 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		40	70		

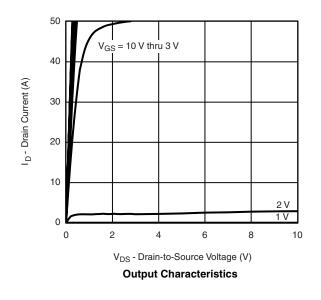
Notes:

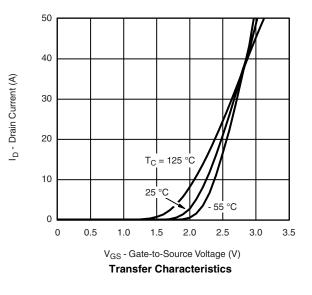
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



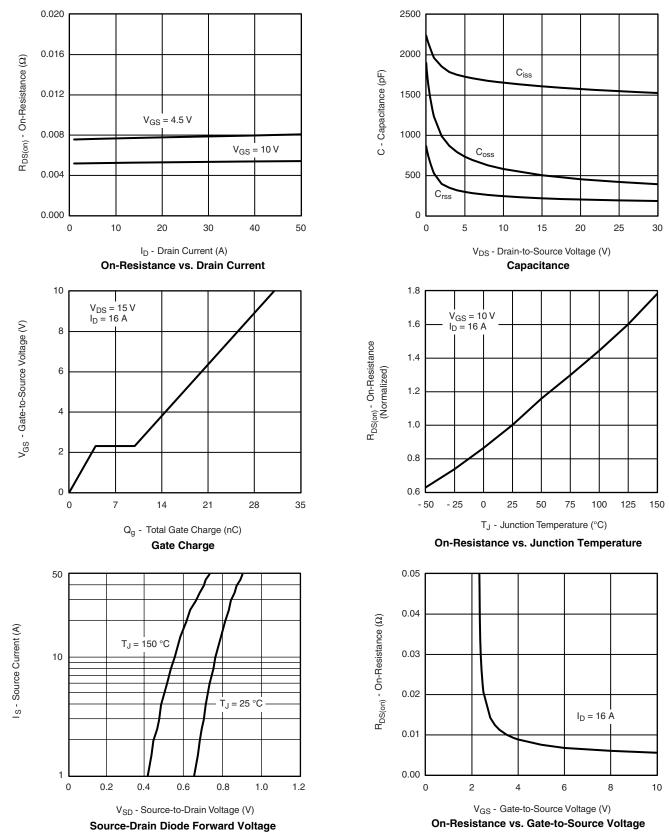


Si4888DY

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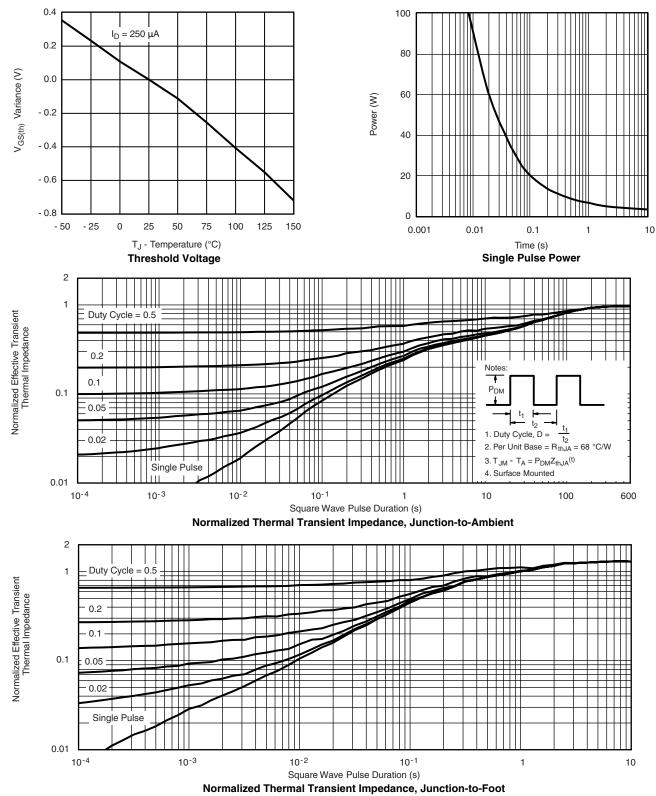


Document Number: 71336 S09-0221-Rev. F, 09-Feb-09

## Si4888DY

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?71336">www.vishay.com/ppg?71336</a>.





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