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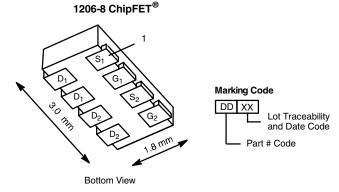
Dual P-Channel 12 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
	0.086 at V _{GS} = - 4.5 V	- 4.1		
- 12	0.127 at V _{GS} = - 2.5 V	- 3.4		
	0.164 at V _{GS} = - 1.8 V	- 3.0		

FEATURES

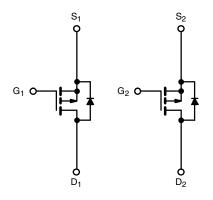
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs: 1.8 V Rated
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si5975DC-T1-E3 (Lead (Pb)-free)

Si5975DC-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_{A}	= 25 °C, unles	ss otherwise n	oted			
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 12		V	
Gate-Source Voltage		V_{GS}	± 8			
Continuous Drain Current (T _{.I} = 150 °C) ^a	T _A = 25 °C	- I _D	- 4.1	- 3.1		
Continuous Diam Current (1) = 150 °C)	T _A = 85 °C		- 3.0	- 2.2	Α	
Pulsed Drain Current		I _{DM}	- 10		^	
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.8	- 0.9		
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	2.1	1.1	W	
Maximum Fower Dissipation	T _A = 85 °C		1.1	0.6	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	
Soldering Recommendations (Peak Temperature) ^{b, c}			260		C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	t ≤ 5 s	R _{thJA}	50	60	°C/W	
Maximum Junction-to-Ambient	Steady State		90	110		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	30	40		

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. See reliability manual for profile. The ChipFET is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- c. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

Vishay Siliconix



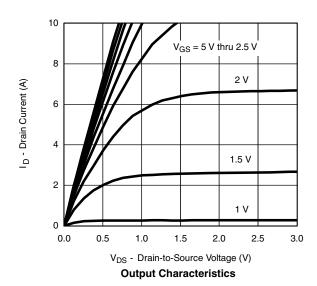
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•	•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -1$ mA	- 0.45			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = - 9.6 V, V _{GS} = 0 V			- 1	— uA	
	I _{DSS}	$V_{DS} = -9.6 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 10			Α	
		V _{GS} = - 4.5 V, I _D = - 3.1 A		0.070	0.086		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 2.5 A		0.100	0.127	Ω	
		V _{GS} = - 1.8 V, I _D = - 1.0 A		0.131	0.164		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 3.1 A		8		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 0.9 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			5.7	9	nC	
Gate-Source Charge	Q _{gs}	V _{DS} = -6 V, V _{GS} = -4.5 V, I _D = -3.1 A		1.2			
Gate-Drain Charge	Q_{gd}			1.2			
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r			20	30	ns	
Turn-Off Delay Time	t _{d(off)}			31	45		
Fall Time	t _f			26	40		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 0.9 A, dI/dt = 100 A/μs		40	60		

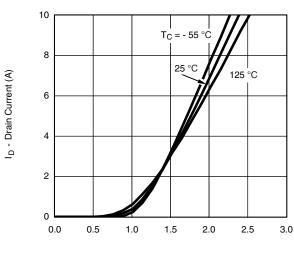
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





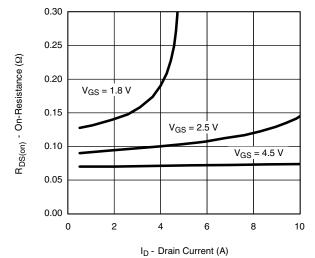
V_{GS} - Gate-to-Source Voltage (V) **Transfer Characteristics**



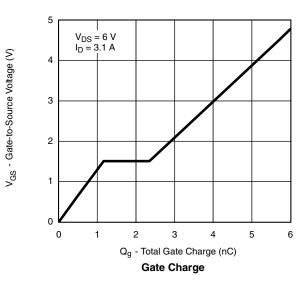


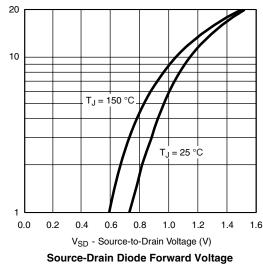


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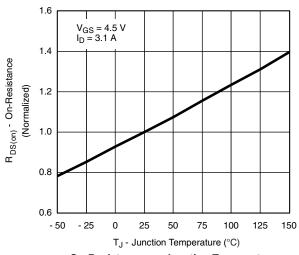
On-Resistance vs. Drain Current



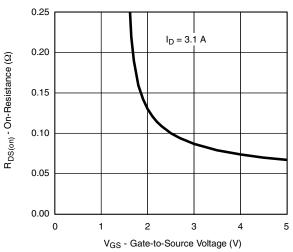


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V_{DS} - Drain-to-Source Voltage (V) **Capacitance**



On-Resistance vs. Junction Temperature



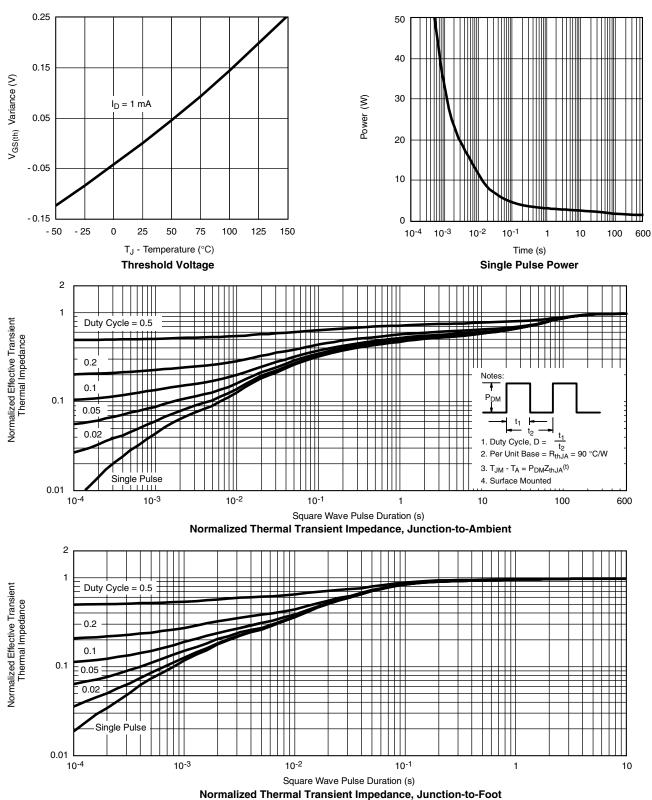
On-Resistance vs. Gate-to-Source Voltage

Is - Source Current (A)

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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