imall

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Vishay Siliconix

Dual N-Channel 2.5-V (G-S) MOSFET

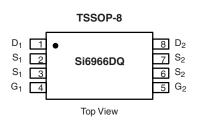
| PRODUCT SUMMARY | | | | |
|---------------------|----------------------------------|--------------------|--|--|
| V _{DS} (V) | R_{DS(on)} (Ω) | I _D (A) | | |
| 20 | 0.030 at V_{GS} = 4.5 V | 4.5 | | |
| | 0.040 at V _{GS} = 2.5 V | 3.9 | | |

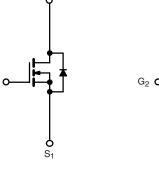
FEATURES

- Halogen-free Option Available
- TrenchFET[®] Power MOSFETs: 2.5 V Rated



RoHS*







 D_2

| Ordering Information: | Si6966DQ-T1 |
|-----------------------|---|
| | Si6966DQ-T1-GE3 (Lead (Pb)-free and Halogen-free) |

N-Channel MOSFET

Dı

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted Parameter Symbol 10 s **Steady State** Unit **Drain-Source Voltage** V_{DS} 20 ٧ V_{GS} Gate-Source Voltage ± 12 T_A = 25 °C 4.5 4.0 Continuous Drain Current (T_J = 150 °C)^a I_D T_A = 70 °C 3.6 3.0 А **Pulsed Drain Current** I_{DM} 30 1.25 0.75 I_S Continuous Source Current (Diode Conduction)^a T_A = 25 °C 1.14 0.83 P_D w Maximum Power Dissipation^a T_A = 70 °C 0.73 0.53 T_J, T_{stg} Operating Junction and Storage Temperature Range - 55 to 150 °C

| THERMAL RESISTANCE RATINGS | | | | | | | | |
|--|--------------|--|---------|---------|------|--|--|--|
| Parameter | | Symbol | Typical | Maximum | Unit | | | |
| | t ≤ 10 s | - R _{thJA} R _{thJF} | 86 | 110 | | | | |
| Maximum Junction-to-Ambient ^a | Steady State | | 124 | 150 | °C/W | | | |
| Maximum Junction-to-Foot (Drain) | Steady State | | 52 | 65 | | | | |

Notes:

a. Surface Mounted on FR4 board.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

Vishay Siliconix



| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|---------------------|---|------|-------|-------|------------|--|
| Static | | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$ | 0.6 | | 1.4 | V | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 V, V_{GS} = \pm 12 V$ | | | ± 100 | nA | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = 20 V, V_{GS} = 0 V$ | | | 1 | 1 25 μΑ | |
| | | V_{DS} = 20 V, V_{GS} = 0 V, T_{J} = 55 °C | | | 25 | | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \geq 5$ V, V_{GS} = 4.5 V | 30 | | | А | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | $V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 4.5 \text{ A}$ | | 0.021 | 0.030 | Ω | |
| | | $V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 3.9 \text{ A}$ | | 0.030 | 0.040 | | |
| Forward Transconductance ^a | 9 _{fs} | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 4.5 \text{ A}$ | | 20 | | S | |
| Diode Forward Voltage ^a | V _{SD} | $I_{S} = 1.25 \text{ A}, V_{GS} = 0 \text{ V}$ | | 0.65 | 1.2 | V | |
| Dynamic ^b | | | | | | | |
| Total Gate Charge | Qg | | | 11.5 | 20 | nC | |
| Gate-Source Charge | Q _{gs} | V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 4.5 A | | 1.9 | | | |
| Gate-Drain Charge | Q _{gd} | | | 3.6 | | | |
| Turn-On Delay Time | t _{d(on)} | | | 11 | 20 | | |
| Rise Time | t _r | V_{DD} = 10 V, R_L = 10 Ω | | 9 | 15 | ns | |
| Turn-Off Delay Time | t _{d(off)} | $\text{I}_\text{D}\cong \text{1}$ A, V_GEN = 10 V, R_G = 6 Ω | | 36 | 55 | | |
| Fall Time | t _f | | | 11 | 20 | | |
| Source-Drain Reverse Recovery Time | t _{rr} | I _F = 1.25 A, dl/dt = 100 A/μs | | 30 | 60 | | |

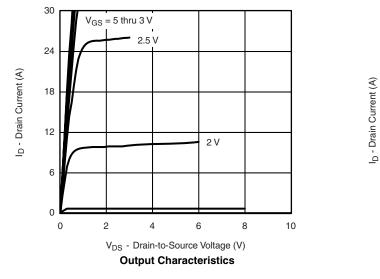
Notes:

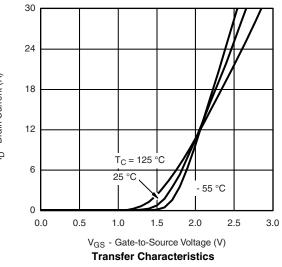
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

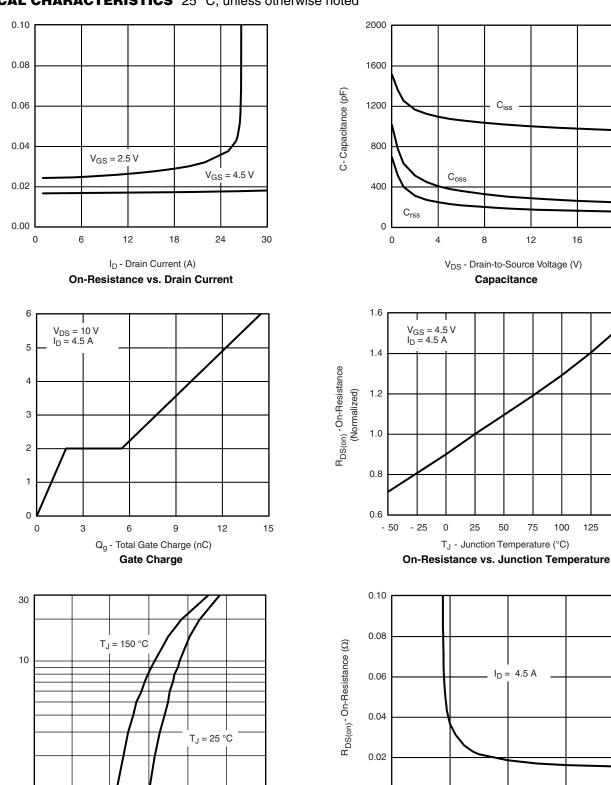
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







0.00

0

2

4

V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

VISHAY

R $_{DS(on)}$ - On-Resistance (Ω)

V_{GS} - Gate-to-Source Voltage (V)

Is- Source Current (A)

Document Number: 71808 S-81221-Rev. C, 02-Jun-08

1 0.0

0.2

0.4

0.6

0.8

V_{SD} - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

1.0

1.2

1.4

8

6

125

150

Si6966DQ

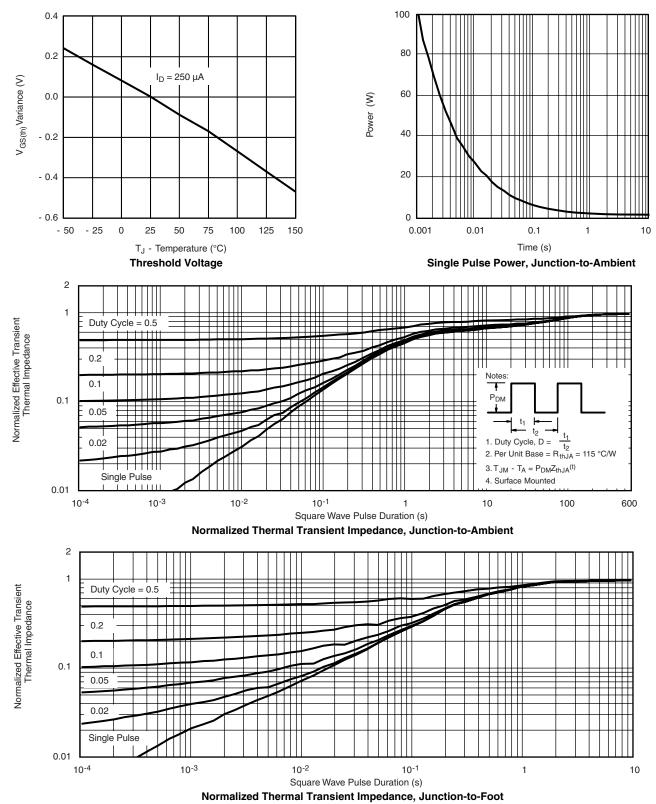
20

Vishay Siliconix

Si6966DQ

Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?71808.



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