

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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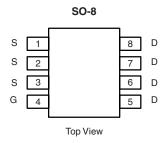
P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
- 20	$0.025 \text{ at V}_{GS} = -4.5 \text{ V}$	- 7.1		
	0.033 at V _{GS} = - 2.5 V	- 6.1		

FEATURES

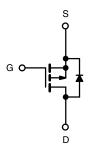
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si9424BDY-T1-E3 (Lead (Pb)-free)

Si9424BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 9			
Continuous Drain Current /T 150 °C\a	T _A = 25 °C	- I _D	- 7.1	- 5.6	^	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 5.6	- 4.5		
Pulsed Drain Current		I _{DM}	- 30		Α	
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.7	- 1.0		
Mariana Barra Biratiantina	T _A = 25 °C	- P _D	2.0	1.25	W	
Maximum Power Dissipation ^a	T _A = 70 °C		1.3	0.8		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maninum Lunction to Australia	t ≤ 10 s	- R _{thJA}	50	62.5	°C/W
Maximum Junction-to-Ambient ^a	Steady State		80	100	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	30	40	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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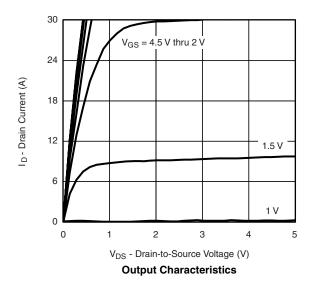
SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min. Typ.		Max.	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 0.45		- 0.85	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 9 \text{ V}$			± 100	nA	
Zava Cata Valtaga Drain Current		V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 V$, $V_{GS} = -4.5 V$	- 30			Α	
D : 0	D	V _{GS} = - 4.5 V, I _D = - 7.1 A		0.014	0.025	0	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 6.6 A		0.019	0.033	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 7.7 A		25		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 2.3 A, V _{GS} = 0 V		- 0.7	- 1.2	V	
Dynamic ^b			•	•	•		
Total Gate Charge	Q_g			24	40		
Gate-Source Charge	Q_{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -7.1 \text{ A}$		3.5		nC	
Gate-Drain Charge	Q_{gd}			5.8			
Turn-On Delay Time	t _{d(on)}			30	45		
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		40	60		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_g = 6 Ω		130	200	ns	
Fall Time	t _f			70	105		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.3 A, dl/dt = 100 A/μs		50	100		

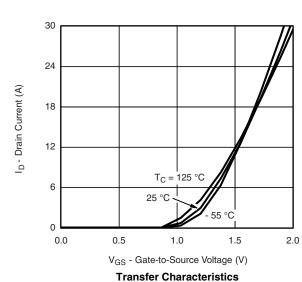
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

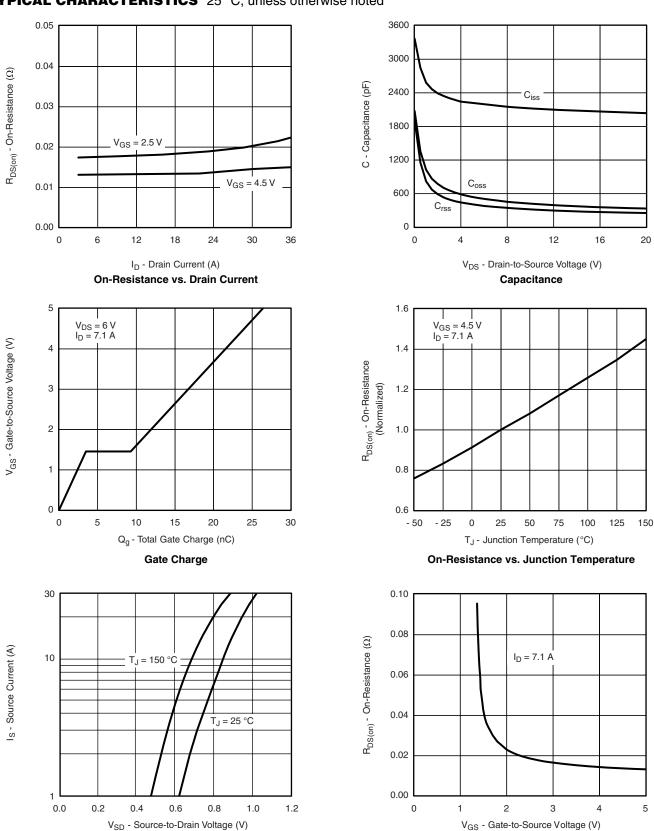








TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



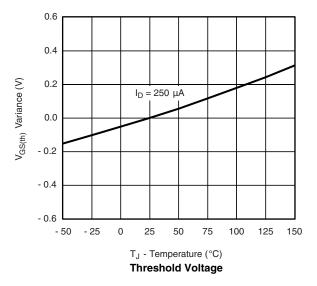
Source-Drain Diode Forward Voltage

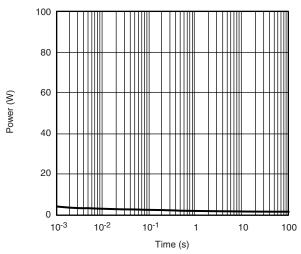
On-Resistance vs. Gate-to-Source Voltage

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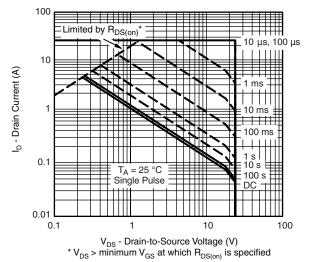
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

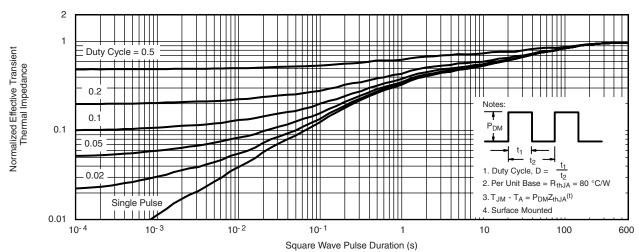




Single Pulse Power



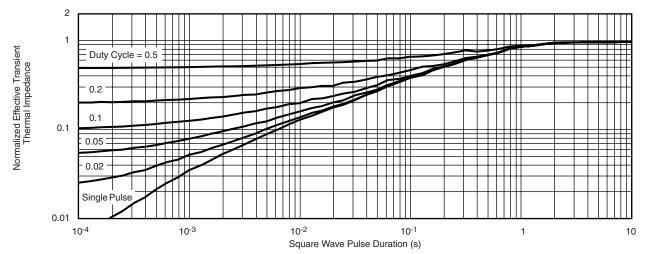
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72015.



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