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# PC Card (PCMCIA) Interface Switch 

FEATURES

- Single SO-8 Package
- CMOS-Logic Compatible Inputs
- Slow $\mathrm{V}_{\mathrm{CC}}$ Ramp Time
- Smart Switching
- Extremely Low R RON
- Reverse Blocking Switches
- Low Power Consumption
- Safe Power Up


## DESCRIPTION

The Si9706DY offers an integrated solution for PC Card power interfaces that only require $\mathrm{V}_{\mathrm{CC}}$ switching. This part is ideal for systems that operate at 5 V and provide $\mathrm{V}_{\mathrm{PP}}$ from the main supply or from a dedicated Flash RAM 12-V supply.

The Si9706DY operates off the 5-V supply and has built-in level shifting for gate drive. Internal logic protects against a control logic error that would short 5 V to the $3.3-\mathrm{V}$ supply. This protection logic also allows the Si9706DY to be configured for
positive or negative control logic for compatibility with a variety of PC Card controllers. These control inputs are CMOS logic compatible and can be driven to 3.3 V or 5 V .

The Si9706DY PC Card interface switch is packaged in a narrow body SO-8 package and is rated over the industrial temperature range -40 to $85^{\circ} \mathrm{C}$. The Si9706DY is available in lead free.

## FUNCTIONAL BLOCK DIAGRAM



## ABSOLUTE MAXIMUM RATINGS

$\qquad$$\mathrm{S}_{1}, \mathrm{~S}_{2}$ (CMOS Inputs) ........................................................... 7 V
All Pins ..... $-0.5 \mathrm{~V}$
out $\mathrm{V}_{\mathrm{CC}}{ }^{\mathrm{a}}$ ..... 4 A
RECOMMENDED OPERATING CONDITIONS

| $+5 \mathrm{~V}_{\text {IN }}$ (must be present) | $5 \mathrm{~V} \pm 10 \%$ |
| :---: | :---: |
| +3.3 $\mathrm{V}_{\text {IN }}$ | $3.3 \vee \pm 10 \%$ |
| $\mathrm{C}_{\text {SR }}$ | 33 nF |
| lout $\mathrm{V}_{\text {CC }}{ }^{\text {a }}$ | 2 A |

PD Max ${ }^{\text {b }} . ~\left(T_{A}=25^{\circ} \mathrm{C}\right)$ ..... 1.59 W
( $\mathrm{T}_{\mathrm{A}}=85^{\circ} \mathrm{C}$ ) ..... 0.63 W
Junction Temperature ..... $125^{\circ} \mathrm{C}$
Thermal Ratings ${ }^{\mathrm{b}}: \mathrm{R}_{\Theta J A}$ ..... $63^{\circ} \mathrm{C} / \mathrm{W}$
Notes
a. Pins 2, 3 connected together externallyb. Mounted on $1-\mathrm{IN}^{2}$, FR4 PC Board.
$V_{C C}$ Load Capacitance ..... $150 \mu \mathrm{~F}$ Max
Notesa. Pins 2, 3 connected together externally.

## SPECIFICATIONS

| Parameter | Symbol | Test Conditions Unless Otherwise Specified$\begin{gathered} \mathrm{C}_{\mathrm{SR}}=33 \mathrm{nF},+5 \mathrm{~V} \mathrm{IN}=5 \mathrm{~V} \\ +3.3 \mathrm{~V} \mathrm{IN}=3.3 \mathrm{~V}, \text { Low } \leq 0.8 \mathrm{~V}, \text { High } \geq 2.2 \mathrm{~V} \end{gathered}$ | Limits |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Mina ${ }^{\text {a }}$ | Typ ${ }^{\text {b }}$ | Max ${ }^{\text {a }}$ |  |

Switch SW ${ }_{1}$

| On-Resistance | $\mathrm{R}_{\mathrm{ON}}$ | $\begin{gathered} \mathrm{I}=500 \mathrm{~mA}, \mathrm{~S}_{1}=\text { High } \\ \mathrm{S}_{2}=\text { Low } \end{gathered}$ | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  | 58 | 70 | $\mathrm{m} \Omega$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\mathrm{T}_{\mathrm{A}}=85^{\circ} \mathrm{C}$ |  | 73 | 90 |  |
| Off Current ( $\mathrm{V}_{\mathrm{CC}}$ ) | IOFF | $\begin{gathered} +5 \mathrm{~V}_{\mathrm{IN}}=5.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{CC}}=0 \mathrm{~V} \\ \mathrm{~S}_{1}=\mathrm{S}_{2}=\text { Low } \end{gathered}$ | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | 1 | $\mu \mathrm{A}$ |
|  |  |  | $\mathrm{T}_{\mathrm{A}}=85^{\circ} \mathrm{C}$ |  |  | 10 |  |
| Rise Time | $\mathrm{t}_{\text {S1 }}$ (on) | $\mathrm{S}_{2}=$ Low, See Figure 1 |  | 0.2 | 1.7 | 5 | ms |
| Fall Time | $\mathrm{t}_{\text {S1 (off) }}$ |  |  | 10 | 30 | 50 |  |

Switch SW $_{2}$

| On-Resistance | $\mathrm{R}_{\mathrm{ON}}$ | $\begin{gathered} \mathrm{I}=500 \mathrm{~mA}, \mathrm{~S}_{2}=\text { High } \\ \mathrm{S}_{1}=\text { Low } \end{gathered}$ | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  | 44 | 55 | $\mathrm{m} \Omega$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\mathrm{T}_{\mathrm{A}}=85^{\circ} \mathrm{C}$ |  | 55 | 70 |  |
| Off Current ( $+3.3 \mathrm{~V}_{\mathrm{IN}}$ ) | loff | $\begin{gathered} +3.3 \mathrm{~V}_{\mathrm{IN}}=3.6 \mathrm{~V}, \mathrm{~V}_{\mathrm{CC}}=0 \mathrm{~V} \\ \mathrm{~S}_{1}=\mathrm{S}_{2}=\text { Low } \end{gathered}$ | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | 1 | $\mu \mathrm{A}$ |
|  |  |  | $\mathrm{T}_{\mathrm{A}}=85^{\circ} \mathrm{C}$ |  |  | 10 |  |
| Rise Time | ts2(on) | $\mathrm{S}_{1}=$ Low, See Figure 1 |  | 0.1 | 0.9 | 5 | ms |
| Fall Time | $\mathrm{t}_{\text {S2(off) }}$ |  |  | 5 | 20 | 40 |  |

Switch SW3

| On-Resistance | $\mathrm{R}_{\mathrm{ON}}$ | $\mathrm{I}=2 \mathrm{~mA}, \mathrm{~S}_{1}=\mathrm{S}_{2}=\mathrm{Low}$ | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  | 140 | 400 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  | $\mathrm{~T}_{\mathrm{A}}=85^{\circ} \mathrm{C}$ |  | 200 | 500 |  |  |

## Power Supply

| +5 $\mathrm{V}_{\text {IN }}$ Current Input (on) | $\mathrm{I}_{+5 \mathrm{VIN}(1)}$ | $\mathrm{S}_{1}=0 \mathrm{~V}, \mathrm{~S}_{2}=3 \mathrm{~V}$ |  | 20 | 50 | $\mu \mathrm{A}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\mathrm{I}_{+5 \mathrm{VIN}(2)}$ | $\mathrm{S}_{1}=3 \mathrm{~V}, \mathrm{~S}_{2}=0 \mathrm{~V}$ |  | 20 | 50 |  |
| $+5 \mathrm{~V}_{\text {IN }}$ Current Input (off) | $\mathrm{I}_{+5 \mathrm{VIN}(3)}$ | $\mathrm{S}_{1}=\mathrm{S}_{2}=0 \mathrm{~V}$ |  | <1 | 10 |  |
| Input Voltage High | $V_{1(H)}$ | $+5 \mathrm{~V}_{\text {IN }}=5.5 \mathrm{~V}$ | 2.2 | 1.8 |  | V |
|  |  | $+5 \mathrm{~V}_{\text {IN }}=4.5 \mathrm{~V}$ | 2.2 | 1.6 |  |  |
| Input Voltage Low | $V_{\text {I (L) }}$ | $+5 \mathrm{~V}_{\text {IN }}=5.5 \mathrm{~V}$ |  | 1.6 | 0.8 |  |
|  |  | $+5 \mathrm{~V}_{\text {IN }}=4.5 \mathrm{~V}$ |  | 1.4 | 0.8 |  |
| Input Current High | $I_{1(H)}$ | $\mathrm{S}_{1}, \mathrm{~S}_{2}=5 \mathrm{~V}$ |  |  | 1.0 | $\mu \mathrm{A}$ |
| Input Current Low | $1{ }_{1}(\mathrm{~L})$ | $\mathrm{S}_{1}, \mathrm{~S}_{2}=\mathrm{GND}$ | -1.0 |  |  |  |

## Notes

a. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum.
b. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.


FIGURE 1. Switch Ramp

| TRUTH TABLE |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| $\mathbf{S}_{\mathbf{1}}$ | $\mathbf{S}_{\mathbf{2}}$ | Switch 1 | Switch 2 | Switch 3 |
| 0 | 0 | Off | Off | On |
| 0 | 1 | Off | On | Off |
| 1 | 0 | On | Off | Off |
| 1 | 1 | Off | Off | On |

Notes
a. The smart switching of the Si9706DY avoids potential host damage by defaulting to off during error conditions.

## PIN CONFIGURATION AND DESCRIPTION



| ORDERING INFORMATION |  |
| :---: | :---: |
| Part Number | Temperature Range |
| Si9706DY-T1 | -40 to $85^{\circ} \mathrm{C}$ |
| Si9706DY-T1-E3 (Lead Free) |  |


| Pin | Function | Description |
| :---: | :---: | :--- |
| 1 | GND | Ground connection. |
| 2,3 | $\mathrm{~V}_{\mathrm{CC}}$ | Supply voltage to slot. |
| 4 | $\mathrm{~S}_{2}$ | Control input for selecting $+3.3 \mathrm{~V}_{\text {IN }}$ to $\mathrm{V}_{\mathrm{CC}}$. |
| 5 | $\mathrm{~S}_{1}$ | Control input for selecting $+5 \mathrm{~V}_{\text {IN }}$ to $\mathrm{V}_{\mathrm{CC}}$. |
| 6 | $+3.3 \mathrm{~V}_{\mathrm{IN}}$ | $+3.3-\mathrm{V}$ supply. |
| 7 | $+5 \mathrm{~V}_{\mathrm{IN}}$ | $+5-\mathrm{V}$ supply. |
| 8 | SR | Slew rate control pin. |

## TYPICAL CHARACTERISTICS ( $25^{\circ}$ C UNLESS OTHERWISE NOTED)




## Vishay Siliconix

## TYPICAL CHARACTERISTICS ( $25^{\circ} \mathrm{C}$ UNLESS OTHERWISE NOTED)




$\mathrm{C}_{\mathrm{SR}}$ - Capacitor Value ( $\mu \mathrm{F}$ )




## SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012


| DIM | MILLIMETERS |  | INCHES |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Max | Min | Max |  |  |  |
| A | 1.35 | 1.75 | 0.053 | 0.069 |  |  |  |
| $\mathrm{~A}_{1}$ | 0.10 | 0.20 | 0.004 | 0.008 |  |  |  |
| B | 0.35 | 0.51 | 0.014 | 0.020 |  |  |  |
| C | 0.19 | 0.25 | 0.0075 | 0.010 |  |  |  |
| D | 4.80 | 5.00 | 0.189 | 0.196 |  |  |  |
| E | 3.80 | 4.00 | 0.150 | 0.157 |  |  |  |
| e | 1.27 BSC |  |  |  |  | 0.050 BSC |  |
| H | 5.80 | 6.20 | 0.228 | 0.244 |  |  |  |
| h | 0.25 | 0.50 | 0.010 | 0.020 |  |  |  |
| L | 0.50 | 0.93 | 0.020 | 0.037 |  |  |  |
| q | $0^{\circ}$ | $8{ }^{\circ}$ | $00^{\circ}$ | $8^{\circ}$ |  |  |  |
| S | 0.44 | 0.64 | 0.018 | 0.026 |  |  |  |
| ECN: C-06527-Rev. I, 11-Sep-06 <br> DWG: 5498 |  |  |  |  |  |  |  |

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