imall

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Vishay Siliconix

Bi-Directional N-Channel 20-V (D-S) MOSFET

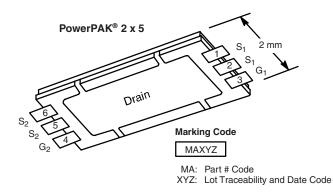
| PRODUCT SUMMARY | | | | | |
|---------------------|----------------------------------|--------------------|-----------------------|--|--|
| V _{DS} (V) | R_{DS(on)} (Ω) | I _D (A) | Q _g (Typ.) | | |
| 20 | 0.022 at V _{GS} = 4.5 V | 10.3 | | | |
| | 0.023 at V _{GS} = 4.0 V | 10.0 | 9.1 | | |
| | 0.026 at V _{GS} = 3.1 V | 9.4 | 9.1 | | |
| | 0.028 at V _{GS} = 2.5 V | 9.0 | | | |

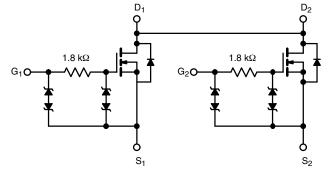
FEATURES

- Halogen-free
- TrenchFET[®] Power MOSFET: 2.5 V Rated
- ESD Protected: 4000 V

APPLICATIONS

- Battery Protection Circuitry
 - Cell Li-Ion LiB/LiP Battery Packs





Ordering Information: SiF902EDZ-T1-GE3 (Lead (Pb)-free and Halogen-free)

| ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted | | | | | | |
|--|------------------------|-----------------------------------|-------------|--------------|------|--|
| Parameter | | Symbol | 10 s | Steady State | Unit | |
| Drain-Source Voltage | | V _{DS} | 20 | | V | |
| Gate-Source Voltage | | V _{GS} | ± 12 | | | |
| | T _A = 25 °C | - I _D | 10.3 | 7.0 | | |
| Continuous Drain Current (T _J = 150 °C) ^a | T _A = 85 °C | | 7.4 | 5.1 | | |
| Pulsed Drain Current (V _{GS} = 8 V) | | I _{DM} | 40 | | A | |
| Continuous Diode Current (Diode Conduction) ^a | | ۱ _S | 3.1 | 1.5 | | |
| | T _A = 25 °C | - P _D | 3.5 | 1.6 | W | |
| Maximum Power Dissipation ^a | T _A = 85 °C | | 1.8 | 0.86 | vv | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 150 | | °C | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|--|--------------|-------------------|---------|---------|------|--|
| Parameter | | Symbol | Typical | Maximum | Unit | |
| | t ≤ 10 s | R _{thJA} | 30 | 36 | | |
| Maximum Junction-to-Ambient ^a | Steady State | | 61 | 76 | °C/W | |
| Maximum Junction-to-Case (Drain) | Steady State | R _{thJC} | 4.8 | 6.0 | | |

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



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| SPECIFICATIONS T ₁ = 25 °C, unless otherwise noted | | | | | | | |
|--|---------------------|---|-----|-------|-------|------|--|
| Parameter | Symbol | Test Conditions Min. | | Тур. | Max. | Unit | |
| Static | · · | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$ | 0.6 | | 1.5 | V | |
| Gate-Body Leakage | I _{GSS} - | $V_{DS} = 0 V, V_{GS} = \pm 4.5 V$ | | | ± 10 | | |
| | | $V_{DS} = 0$ V, $V_{GS} = \pm 12$ V | | | ± 500 | μΑ | |
| Zero Gate Voltage Drain Current | I _{DSS} - | $V_{DS} = 20 V, V_{GS} = 0 V$ | | | 1 | | |
| | | V_{DS} = 20 V, V_{GS} = 0 V, T_{J} = 85 °C | | | 5 | | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} = 5 V, V_{GS} = 4.5 V$ | 40 | | | А | |
| | R _{DS(on)} | $V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 7.0 \text{ A}$ | | 0.018 | 0.022 | Ω | |
| | | $V_{GS} = 4.0 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}$ | | 0.019 | 0.023 | | |
| Drain-Source On-State Resistance ^a | | $V_{GS} = 3.1 \text{ V}, I_D = 4.0 \text{ A}$ | | 0.021 | 0.026 | | |
| | | $V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 3.5 \text{ A}$ | | 0.023 | 0.028 | | |
| Forward Transconductance ^a | 9 _{fs} | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 7.0 \text{ A}$ | | 38 | | S | |
| Diode Forward Voltage ^a | V _{SD} | $I_{\rm S} = 3.1$ A, $V_{\rm GS} = 0$ V | | 0.76 | 1.1 | V | |
| Dynamic ^b | · · | | | | | | |
| Total Gate Charge | Qg | | | 9.1 | 14 | nC | |
| Gate-Source Charge | Q _{gs} | V_{DS} = 10 V, V_{GS} = 4.5 V, I_D = 7.0 A | | 1.9 | | | |
| Gate-Drain Charge | Q _{gd} | | | 2.7 | | | |
| Turn-On Delay Time | t _{d(on)} | | | 1.7 | 2.6 | | |
| Rise Time | t _r | V_{DD} = 10 V, R_L = 10 Ω | | 2.3 | 3.5 | μs | |
| Turn-Off Delay Time | t _{d(off)} | $\text{I}_\text{D}\cong\text{1}$ A, V_GEN = 4.5 V, R_g = 6 Ω | | 1.1 | 1.7 | | |
| Fall Time | t _f | | | 4.4 | 6.6 | | |

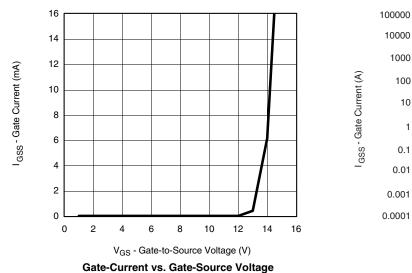
Notes:

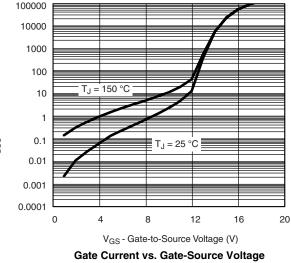
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

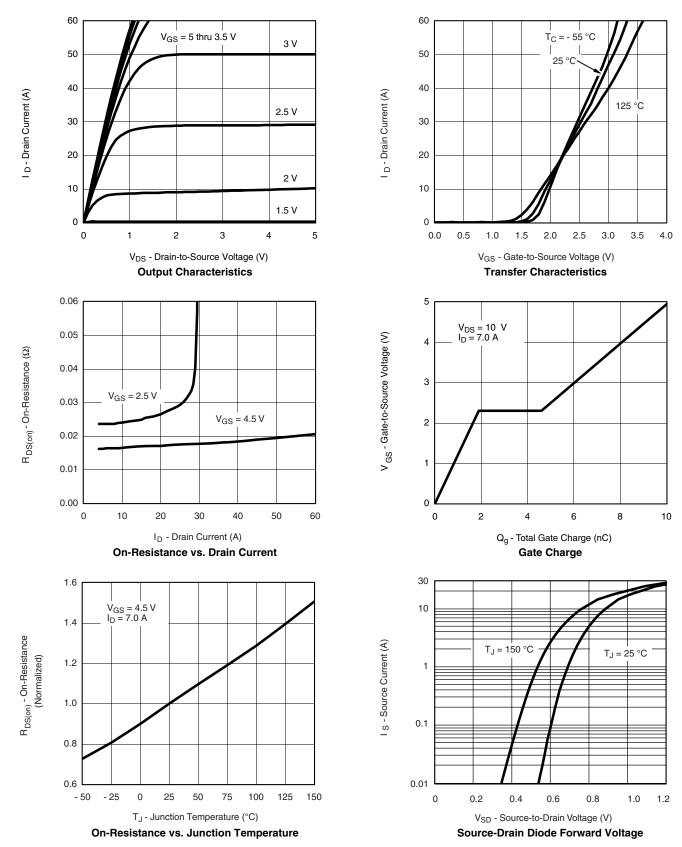






SiF902EDZ Vishay Siliconix

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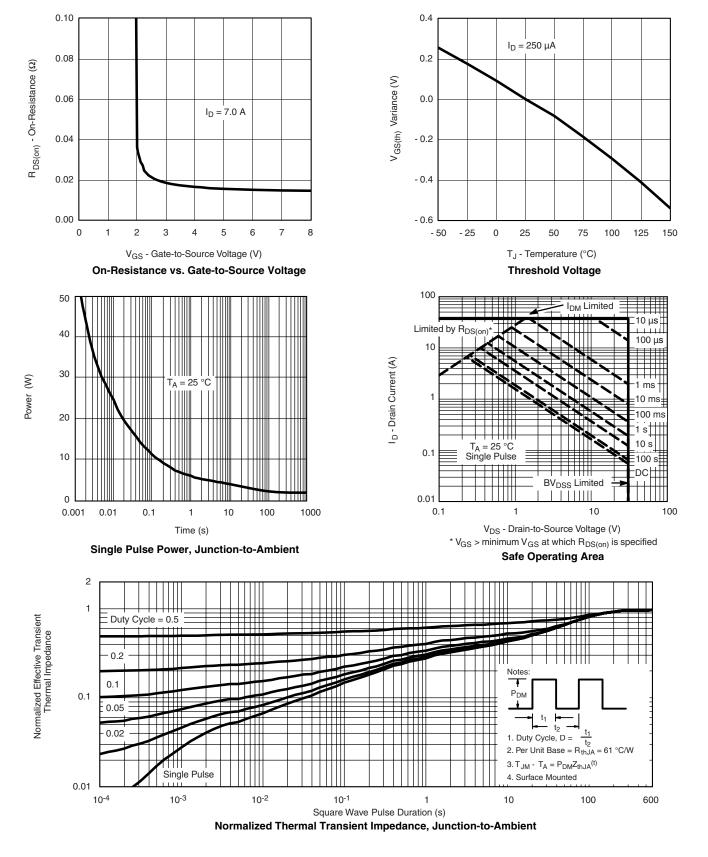
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SiF902EDZ





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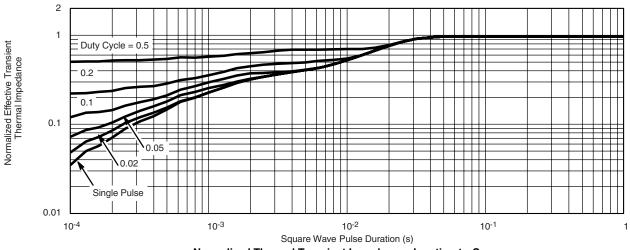




SiF902EDZ

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72987.



Vishay

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