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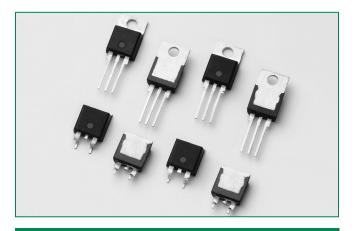




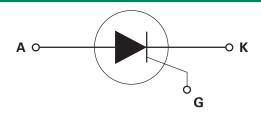


### SJxx16xx series





#### **Schematic Symbol**



#### **Main Features**

Symbol	Value	Unit
I <sub>T(RMS)</sub>	16	А
V <sub>DRM</sub> /V <sub>RRM</sub>	400 or 600	V
I <sub>GT</sub>	6 to 30	mA

#### **Description**

This SJxx16xx high junction temperature SCR series is ideal for uni-directional switch applications such as phase control in heating, motor speed controls, converters/rectifiers and inrush current controllers.

These SCRs have a low gate current trigger level of 6mA,10mA or 30mA maximum at approximately 1.5V

#### **Features & Benefits**

- Halogen free and RoHS compliant
- 150°C maximum junction temperature
- Surge capability up to 225A at 60 Hz half cycle

#### **Applications**

Typical applications include AC Generator (ACG) rectifiers, battery voltage regulators, generic converters and inrush current controller in various AC to DC applications. Additional applications include controls for power tools, home/brown good and white goods appliances.

Internally constructed isolated packages are offered for ease of heat sinking with high isolation voltage.

#### Absolute Maximum Ratings — Standard SCRs

Symbol	Parameter	Test Conditions		Value	Unit	
$V_{DRM}/V_{RSM}$	Peak non-repetitive blocking voltage	Pw=100 μs		$V_{DRM}/V_{RRM}+100$	V	
		SJxx16Lx	T <sub>C</sub> = 110°C	16		
I <sub>T(RMS)</sub>	RMS on-state current	SJxx16Rx SJxx16Nx	T <sub>c</sub> = 135°C	16	А	
		SJxx16Lx	$T_{c} = 110^{\circ}C$	10		
I <sub>T(AV)</sub>	Average on-state current	SJxx16Rx SJxx16Nx	T <sub>c</sub> = 135°C	10	А	
	Peak non-repetitive surge current	single half cycle; $f = 50Hz$ ; $T_J$ (initial) = 25°C		188	А	
I <sub>TSM</sub>	reak non-repetitive surge current	single half cycle; $f = 60Hz$ ; $T_J$ (initial) = 25°C		225	,,	
l²t	l²t Value for fusing	$t_{p} = 8.3$	3 ms	210	A <sup>2</sup> s	
di/dt	Critical rate of rise of on-state current	f = 60 Hz ; T <sub>_</sub> = 150°C		125	A/µs	
I <sub>GM</sub>	Peak gate current	$T_J = 150$ °C		3	А	
P <sub>G(AV)</sub>	Average gate power dissipation	T <sub>J</sub> = 150°C		0.6	W	
T <sub>stg</sub>	Storage temperature range			-40 to 150	°C	
T	Operating junction temperature range			-40 to 150	°C	

Note: xx=voltage/10, x=sensitivity

# **Thyristors**16 Amps High Junction Temperature SCRs

#### Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise specified)

Symbol	Test Conditions	SJxx16x	SJxx16x1	SJxx16x2	Unit	
		MIN.	8	2	5	mA
I <sub>GT</sub>	$V_D = 12V; R_L = 60 \Omega$	MAX.	30	6	10	IIIA
V <sub>GT</sub>		MAX.	1.5	1.5	1.5	V
dv/dt	$V_D = 67\%V_{DRM}$ ; gate open; $T_J = 125$ °C	125°C MIN.		70	500	V/µs
αν/αι	$V_D = 67\% V_{DRM}$ ; gate open; $T_J = 150$ °C	IVIIIV.	400	-	200	ν/μ3
$V_{\sf GD}$	$V_D = V_{DRM} R_L = 3.3 \text{ k}\Omega T_J = 110^{\circ}\text{C}$	MIN.	0.2	0.2	0.2	V
I <sub>H</sub>	$I_{T} = 200 \text{mA (initial)}$	MAX.	75	15	35	mA
t <sub>q</sub>	$I_{T}=2A; t_{p}=50\mu s; dv/dt=5V/\mu s; di/dt=-30A/\mu s$	MAX.	40	40	40	μs
t <sub>gt</sub>	$I_{\rm G} = 2 \times I_{\rm GT}$ PW = 15 $\mu$ s $I_{\rm T} = 32$ A	TYP.	2	2	2	μs

Note: xx=voltage/10, x=package

#### **Static Characteristics**

Symbol	Test Conditions			Value	Unit
V <sub>TM</sub>	Com	ponent $I_{T} = 32A; t_{p} = 380 \mu s$	1.6	V	
	$I_{DRM}/I_{RRM}$ $V_{DRM} = V_{RRM}$	T <sub>J</sub> = 25°C	MAX.	10	μΑ
I <sub>DRM</sub> / I <sub>RRM</sub>		T <sub>J</sub> = 125°C		1000	
	T <sub>J</sub> = 150°C		3000		

#### **Thermal Resistances**

Symbol	Parameter		Value	Unit
$R_{\theta(JC)}$	Junction to case (AC)	SJxx16Rx SJxx16Nx	1.0	°C/W
' 'θ(JC)	Te(JC)	SJxx16Lx	2.5	5,

Note: xx=voltage/10, x=sensitivity

Figure 1: Normalized DC Gate Trigger Current vs. Junction Temperature

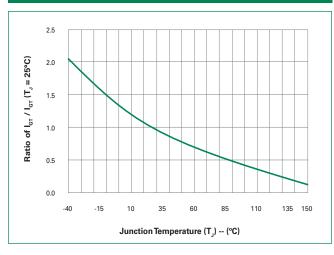
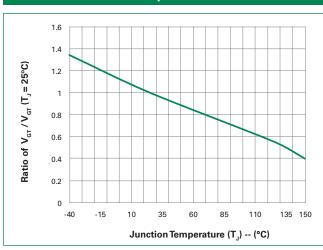


Figure 2: Normalized DC Gate Trigger Voltage vs. Junction Temperature





# Figure 3: Normalized DC Holding Current vs. Junction Temperature

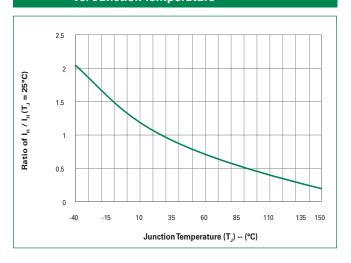


Figure 5: Power Dissipation (Typical) vs. RMS On-State Current

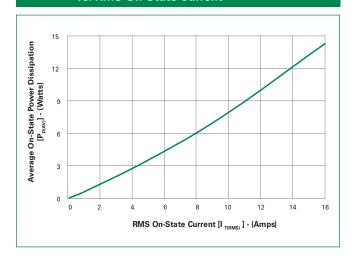


Figure 7: Maximum Allowable Case Temperature vs. Average On-State Current

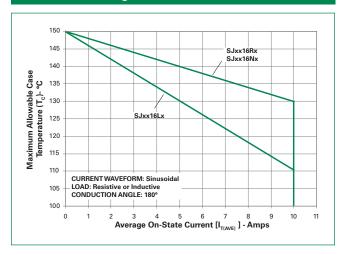


Figure 4: On-State Current vs. On-State Voltage (Typical)

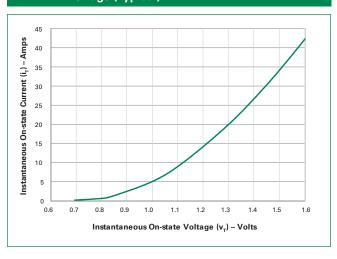
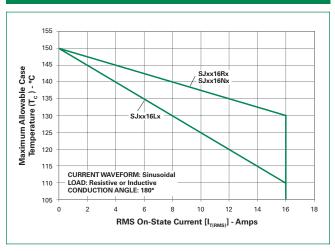
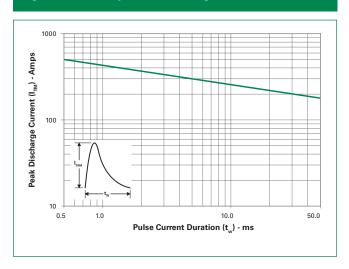


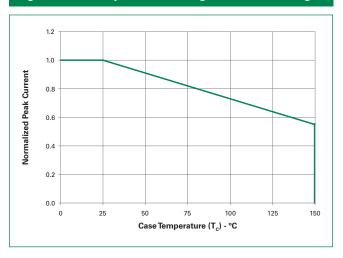
Figure 6: Maximum Allowable Case Temperature vs. RMS On-State Current



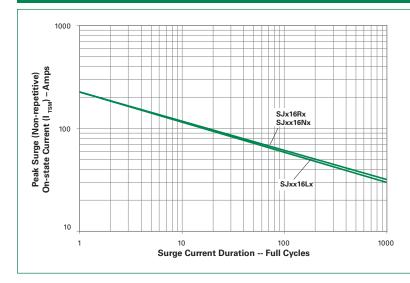
**Figure 8: Peak Capacitor Discharge Current** 



#### Figure 9: Peak Capacitor Discharge Current Derating



#### Figure 10: Surge Peak On-State Current vs. Number of Cycles



SUPPLY FREQUENCY: 60 Hz Sinusoidal LOAD: Resistive

RMS On-State Current: [I<sub>T(RMS)</sub>]: Maximum Rated Value at Specified Case Temperature

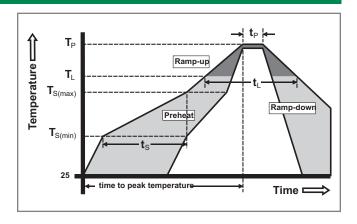
#### Notes:

- 1. Gate control may be lost during and immediately following surge current interval.
- Overload may not be repeated until junction temperature has returned to steady-state rated value.

# 16 Amps High Junction Temperature SCRs

#### **Soldering Parameters**

Reflow Co	ndition	Pb – Free assembly
	-Temperature Min (T <sub>s(min)</sub> )	150°C
Pre Heat	-Temperature Max (T <sub>s(max)</sub> )	200°C
	-Time (min to max) (t <sub>s</sub> )	60 – 180 secs
Average ra (T <sub>L</sub> ) to pea	amp up rate (LiquidusTemp) k	5°C/second max
T <sub>S(max)</sub> to T <sub>L</sub>	- Ramp-up Rate	5°C/second max
Reflow	-Temperature (T <sub>L</sub> ) (Liquidus)	217°C
nellow	-Time (t <sub>L</sub> )	60 – 150 seconds
PeakTemp	erature (T <sub>P</sub> )	260 <sup>+0/-5</sup> °C
Time with Temperate	in 5°C of actual peak ure (t <sub>p</sub> )	20 – 40 seconds
Ramp-dov	vn Rate	5°C/second max
Time 25°C to peakTemperature (T <sub>P</sub> )		8 minutes Max.
Do not exc	ceed	280°C



#### **Physical Specifications**

Terminal Finish	100% Matte Tin-plated			
Body Material	UL recognized compound meeting flammability rating V-0.			
Lead Material	Copper Alloy			

#### **Design Considerations**

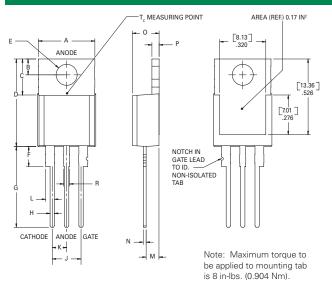
Careful selection of the correct component for the application's operating parameters and environment will go a long way toward extending the operating life of the Thyristor. Good design practice should limit the maximum continuous current through the main terminals to 75% of the component rating. Other ways to ensure long life for a power discrete semiconductor are proper heat sinking and selection of voltage ratings for worst case conditions. Overheating, overvoltage (including dv/dt), and surge currents are the main killers of semiconductors. Correct mounting, soldering, and forming of the leads also help protect against component damage.

#### **Environmental Specifications**

Test	Specifications and Conditions
AC Blocking	MIL-STD-750, M-1040, Cond A Applied Peak AC voltage @ 150°C for 1008 hours
Temperature Cycling	MIL-STD-750, M-1051, 100 cycles; -55°C to +150°C; 15-min dwell-time
Temperature/ Humidity	EIA / JEDEC, JESD22-A101 1008 hours; 160V - DC: 85°C; 85% rel humidity
High Temp Storage	MIL-STD-750, M-1031, 1008 hours; 150°C
Low-Temp Storage	1008 hours; -40°C
Resistance to Solder Heat	MIL-STD-750 Method 2031
Solderability	ANSI/J-STD-002, category 3, Test A
Lead Bend	MIL-STD-750, M-2036 Cond E
Moisture Sensitivity Level	Level 1, JEDEC-J-STD-020D

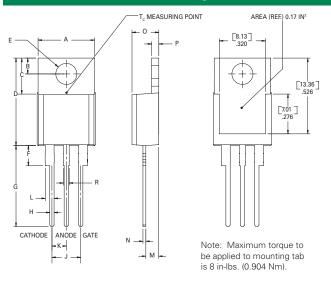
# 16 Amps High Junction Temperature SCRs

#### Dimensions — TO-220AB (R-Package) — Non-Isolated Mounting Tab Common with Center Lead



Dimension	Inc	hes	Millin	neters
Dimension	Min	Max	Min	Max
А	0.380	0.420	9.65	10.67
В	0.105	0.115	2.67	2.92
С	0.230	0.250	5.84	6.35
D	0.590	0.620	14.99	15.75
Е	0.142	0.147	3.61	3.73
F	0.110	0.130	2.79	3.30
G	0.540	0.575	13.72	14.61
Н	0.025	0.035	0.64	0.89
J	0.195	0.205	4.95	5.21
K	0.095	0.105	2.41	2.67
L	0.060	0.075	1.52	1.91
М	0.085	0.095	2.16	2.41
N	0.018	0.024	0.46	0.61
0	0.178	0.188	4.52	4.78
Р	0.045	0.060	1.14	1.52
R	0.038	0.048	0.97	1.22

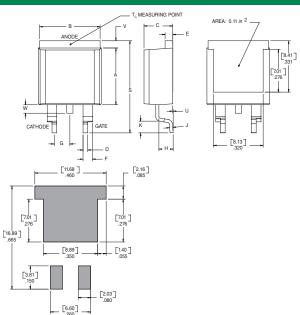
#### Dimensions - TO-220AB (L-Package) - Isolated Mounting Tab



Dimension	Inc	hes	Millin	neters
Difficusion	Min	Max	Min	Max
А	0.380	0.420	9.65	10.67
В	0.105	0.115	2.67	2.92
С	0.230	0.250	5.84	6.35
D	0.590	0.620	14.99	15.75
Е	0.142	0.147	3.61	3.73
F	0.110	0.130	2.79	3.30
G	0.540	0.575	13.72	14.61
Н	0.025	0.035	0.64	0.89
J	0.195	0.205	4.95	5.21
K	0.095	0.105	2.41	2.67
L	0.060	0.075	1.52	1.91
М	0.085	0.095	2.16	2.41
N	0.018	0.024	0.46	0.61
0	0.178	0.188	4.52	4.78
Р	0.045	0.060	1.14	1.52
R	0.038	0.048	0.97	1.22

# 16 Amps High Junction Temperature SCRs

#### Dimensions - TO- 263AB (N-package) - D2-Pak Surface Mount



Dimension	Inches		Millin	neters
Dimension	Min	Max	Min	Max
А	0.360	0.370	9.14	9.40
В	0.380	0.420	9.65	10.67
С	0.178	0.188	4.52	4.78
D	0.025	0.035	0.64	0.89
Е	0.045	0.060	1.14	1.52
F	0.060	0.075	1.52	1.91
G	0.095	0.105	2.41	2.67
Н	0.092	0.102	2.34	2.59
J	0.018	0.024	0.46	0.61
K	0.090	0.110	2.29	2.79
S	0.590	0.625	14.99	15.88
V	0.035	0.045	0.89	1.14
U	0.002	0.010	0.05	0.25
W	0.040	0.070	1.02	1.78

### **Product Selector**

Part Number	Woltage Gate Sensitivity		Time	Dooleana	
Part Number	400V	600V	Gate Sensitivity	Туре	Package
SJxx16L	Х	X	30mA	Standard SCR	TO-220L
SJxx16R	X	X	30mA	Standard SCR	TO-220R
SJxx16N	X	X	30mA	Standard SCR	TO-263
SJxx16L1	X	X	6mA	Standard SCR	TO-220L
SJxx16R1	X	X	6mA	Standard SCR	TO-220R
SJxx16N1	X	X	6mA	Standard SCR	TO-263
SJxx16L2	X	X	10mA	Standard SCR	TO-220L
SJxx16R2	Х	Х	10mA	Standard SCR	TO-220R
SJxx16N2	X	X	10mA	Standard SCR	TO-263

Note: xx = Voltage/10

#### **Packing Options**

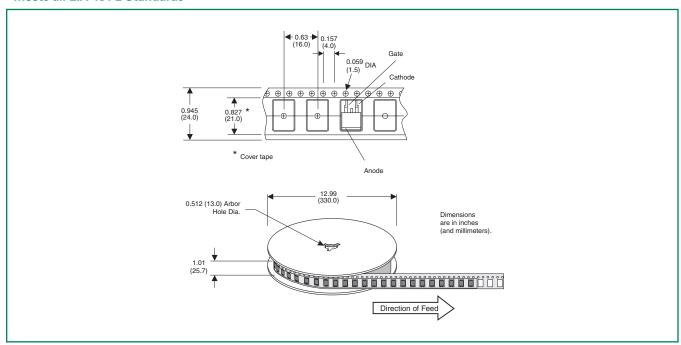
Part Number	Marking	Weight	Packing Mode	Base Quantity
SJxx16LxTP	SJxx16Lx	2.2g	Tube	500 (50 per tube)
SJxx16RxTP	SJxx16Rx	2.2g	Tube	500 (50 per tube)
SJxx16NxTP	SJxx16Nx	1.6g	Tube	500 (50 per tube)
SJxx16NxRP	SJxx16Nx	1.6g	Embossed Carrier	500

Note: xx=voltage/10, x=sensitivity

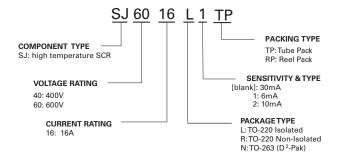
16 Amps High Junction Temperature SCRs

#### TO-263 Embossed Carrier Reel Pack (RP) Specifications

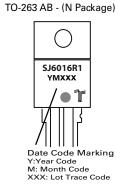
#### Meets all EIA-481-2 Standards



#### **Part Numbering System**



#### **Part Marking System**



TO-220 AB - (L and R Package)

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