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## SKYWORKS

## DATA SHEET

## SKY13293-340LF: 0.25-2.15 GHz $4 \times 2$ Matrix Switch

## Applications

- DBS switching, cable modems, cable TV


## Features

- Broadband frequency range: 0.25 to 2.15 GHz
- High isolation: 57 dB typical @ 0.9 GHz
- Four RF inputs, two RF outputs
- Low current consumption: $500 \mu \mathrm{~A}$ typical @ 5 V
- Miniature QFN (20-pin, $4 \times 4 \mathrm{~mm}$ ) package (MSL1, $260{ }^{\circ} \mathrm{C}$ per JEDEC J-STD-020)

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Figure 1. SKY13293-340LF Block Diagram

## Description

The SKY13293-340LF is a four-input to two-output switch in a low-cost Quad Flat No-Lead (QFN) $4 \times 4 \mathrm{~mm}$ package. The switch can control signals from 0.25 to 2.15 GHz . The SKY13293-340LF enables 16 states, directing any of the four inputs to either of the two outputs. Switch states are selected by four voltage inputs.
A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.


Figure 2. SKY13293-340LF Pinout - 20-Pin QFN (Top View)

Table 1. SKY13293-340LF Signal Descriptions

| Pin \# | Name | Type | I/0 | Description | Pin \# | Name | Type | I/0 | Description |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | V1 | DC | 1 | Control voltage input \#1 | 11 | V4 | DC | I | Control voltage input \#4 |
| 2 | N/C | - | - | No connection | 12 | 13 | RF | 1 | RF input \#3 (250-2150 MHz) |
| 3 | GND | - | GND | RF/DC Ground | 13 | GND | - | GND | RF/DC Ground |
| 4 | 11 | RF | 1 | RF input \#1 ( $250-2150 \mathrm{MHz}$ ) | 14 | N/C | - | - | No connection |
| 5 | V2 | DC | I | Control voltage input \#2 | 15 | V3 | DC | 1 | Control voltage input \#3 |
| 6 | 12 | RF | I | RF input \#2 ( $250-2150 \mathrm{MHz}$ ) | 16 | R0 | RF | 0 | RF channel right output ( $250-2150 \mathrm{MHz}$ ) |
| 7 | GND | - | GND | RF/DC Ground | 17 | N/C | - | - | No connection |
| 8 | VDD | DC | 1 | Supply voltage input | 18 | GND | - | GND | RF/DC Ground |
| 9 | GND | - | GND | RF/DC Ground | 19 | N/C | - | - | No connection |
| 10 | 14 | RF | 1 | RF input \#4 (250-2150 MHz) | 20 | L0 | RF | 0 | RF channel left output ( $250-2150 \mathrm{MHz}$ ) |

Table 2. SKY13293-340LF Absolute Maximum Ratings

| Parameter | Symbol | Minimum | Typical | Maximum | Units |
| :--- | :--- | :--- | :---: | :---: | :---: |
| Input power | PIN |  |  | +15 | dBm |
| Logic control input voltage |  |  |  | 5.5 |  |
| Control voltage |  |  |  | 5.5 |  |
| Storage temperature | TsTG | -65 |  | +150 | ${ }^{\circ}$ |
| Operating temperature | TOP | -40 |  | +85 | ${ }^{\circ} \mathrm{C}$ |

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

## Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY13293-340LF are provided in Table 2. The electrical specifications are provided in Table 3.

Typical performance characteristics of the SKY13293-340LF are illustrated in Figures 3 through 11.

The state of the SKY13293-340LF is determined by the logic provided in Table 4.
An application circuit diagram is presented in Figure 12.

Table 4. SKY13293-340LF Electrical Specifications (Note 1)
(Vod =5 V, Top = +25 ${ }^{\circ} \mathbf{C}$, Piw = -20 dBm, Characteristic Impedance [Z0] = $\mathbf{5 0} \Omega$, Unless Otherwise Noted)

| Parameter | Symbol | Test Condition | Min | Typical | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| RF Switch Performance |  |  |  |  |  |  |
| Insertion loss |  | $\begin{aligned} & 0.25-0.95 \mathrm{GHz} \\ & 0.95-2.15 \mathrm{GHz} \end{aligned}$ |  | $\begin{aligned} & 8.0 \\ & 9.0 \end{aligned}$ | $\begin{aligned} & 8.2 \\ & 9.7 \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Insertion loss flatness |  | $\begin{aligned} & 0.25-0.95 \mathrm{GHz} \\ & 0.95-2.15 \mathrm{GHz} \end{aligned}$ |  | $\begin{aligned} & 0.75 \\ & 1.50 \end{aligned}$ | $\begin{aligned} & 1.00 \\ & 2.50 \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Isolation (normalized to insertion loss) |  | $\begin{aligned} & 0.25-0.95 \mathrm{GHz} \\ & 0.95-1.45 \mathrm{GHz} \\ & 1.45-2.15 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & 43.0 \\ & 38.5 \end{aligned}$ | $\begin{aligned} & 57.0 \\ & 52.0 \\ & 45.0 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Return loss (input selected) |  | $\begin{aligned} & 0.25-0.95 \mathrm{GHz} \\ & 0.95-2.15 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & 15 \\ & 11 \end{aligned}$ | $\begin{aligned} & 20 \\ & 21 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Return loss (input deselected) |  | $\begin{aligned} & 0.25-0.95 \mathrm{GHz} \\ & 0.95-2.15 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & 15 \\ & 11 \end{aligned}$ | $\begin{aligned} & 20 \\ & 21 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| 1 dB input compression point | IP1dB | 0.25-2.15 GHz |  | +15 |  | dBm |
| $3{ }^{\text {rd }}$ Order Input Intercept Point | IIP3 | $\operatorname{PIN}=-12 \mathrm{dBm} /$ tone, 1 MHz spacing, 2.15 GHz |  | +27 |  | dBm |
| Switching characteristics: <br> Rise/fall time <br> On/off time |  | 10/90\% or 90/10\% RF 50\% Vctı to $90 / 10 \%$ RF |  | $\begin{aligned} & 10 \\ & 29 \end{aligned}$ |  | $\begin{aligned} & \text { ns } \\ & \text { ns } \end{aligned}$ |
| Logic Controls |  |  |  |  |  |  |
| Control input current | IJIG | $0 \mathrm{~V} \leq \mathrm{VCTL} \leq \mathrm{VdD}^{\text {d }}$ |  | 5 |  | $\mu \mathrm{A}$ |
| Control input low logic level | VIL |  |  |  | 0.7 | V |
| Control input high logic level | VIH |  | 2.2 |  |  | V |
| Power Supply |  |  |  |  |  |  |
| Supply voltage | Vdo |  | 3.0 | 5.0 | 5.5 | V |
| Supply current | IDD |  |  | 500 |  | $\mu \mathrm{A}$ |

Note 1: Performance is guaranteed only under the conditions listed in this Table.

## Typical Performance Characteristics

( $\mathbf{T o p}_{\mathrm{op}}=\mathbf{+ 2 5}{ }^{\circ} \mathrm{C}$, Characteristic Impedance $\left[Z_{0}\right]=50 \Omega$, Unless Otherwise Noted)


Figure 3. Insertion Loss, All States


Figure 5. I2 to LO Path, All States (Isolation is Normalized to Insertion Loss)


Figure 4. I1 to LO Path, All States (Isolation is Normalized to Insertion Loss)


Figure 6. I3 to LO Path, All States (Isolation is Normalized to Insertion Loss)


Figure 7. 14 to LO Path, All States (Isolation is Normalized to Insertion Loss)


Figure 9. RO Path, All States (Isolation is Normalized to Insertion Loss)


Figure 11. 14 to RO Path, All States (Isolation is Normalized to Insertion Loss)

Figure 8. RO Path, All States (Isolation is Normalized to Insertion Loss)


Figure 10. 13 to RO Path, All States (Isolation is Normalized to Insertion Loss)

Table 5. SKY13293-340LF Truth Table

| State | Control Voltage Inputs (Note 1) |  |  |  | Signal Path (Note 2) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | V1 | V2 | V3 | V4 |  |
| 0 | 0 | 0 | 0 | 0 | $\mathrm{I} \rightarrow \mathrm{LO}, \mathrm{IT} \rightarrow \mathrm{RO}$ |
| 1 | 0 | 0 | 0 | 1 | $\mathrm{I} \rightarrow \mathrm{LO}, \mathrm{I} 2 \rightarrow \mathrm{RO}$ |
| 2 | 0 | 0 | 1 | 0 | $\mathrm{I} \rightarrow \mathrm{L} 0, \mathrm{I} 3 \rightarrow \mathrm{RO}$ |
| 3 | 0 | 0 | 1 | 1 | $\mathrm{I} \rightarrow \rightarrow \mathrm{LO}, \mathrm{I} 4 \rightarrow \mathrm{RO}$ |
| 4 | 0 | 1 | 0 | 0 | $\mathrm{L} 2 \rightarrow \mathrm{LO}, \mathrm{I} \rightarrow$ R ${ }^{\text {a }}$ |
| 5 | 0 | 1 | 0 | 1 | $\mathrm{L} 2 \rightarrow \mathrm{LO}, \mathrm{I} 2 \rightarrow \mathrm{RO}$ |
| 6 | 0 | 1 | 1 | 0 | $\mathrm{I} 2 \rightarrow \mathrm{LO}, \mathrm{I} 3 \rightarrow \mathrm{RO}$ |
| 7 | 0 | 1 | 1 | 1 | $\mathrm{L} 2 \rightarrow \mathrm{LO}, \mathrm{I} 4 \rightarrow \mathrm{RO}$ |
| 8 | 1 | 0 | 0 | 0 | $\mathrm{I} 3 \rightarrow \mathrm{LO}, \mathrm{I} \rightarrow$ R 0 |
| 9 | 1 | 0 | 0 | 1 | $\mathrm{I} 3 \rightarrow \mathrm{LO}, \mathrm{I} 2 \rightarrow \mathrm{RO}$ |
| 10 | 1 | 0 | 1 | 0 | $\mathrm{I} 3 \rightarrow \mathrm{LO}, \mathrm{I} 3 \rightarrow \mathrm{RO}$ |
| 11 | 1 | 0 | 1 | 1 | $13 \rightarrow \mathrm{LO}, 14 \rightarrow \mathrm{RO}$ |
| 12 | 1 | 1 | 0 | 0 | $14 \rightarrow \mathrm{LO}, \mathrm{I} \rightarrow$ R 0 |
| 13 | 1 | 1 | 0 | 1 | $14 \rightarrow \mathrm{LO}, \mathrm{I} 2 \rightarrow \mathrm{RO}$ |
| 14 | 1 | 1 | 1 | 0 | $14 \rightarrow \mathrm{LO}, \mathrm{I} 3 \rightarrow \mathrm{RO}$ |
| 15 | 1 | 1 | 1 | 1 | $14 \rightarrow \mathrm{LO}, \mathrm{I} 4 \rightarrow \mathrm{RO}$ |

Note 1: "1" = VIH. " 0 " $=$ VIL. Any state other than described in this Table places the switch into an undefined state. An undefined state will not damage the device.
Note 2: All other paths in isolation state.


Figure 12. Application Circuit

## Package Dimensions

The PCB layout footprint for the SKY13293-340LF is provided in Figure 13. Typical case markings are shown in Figure 14. Package dimensions for the 20-pin QFN are shown in Figure 15, and tape and reel dimensions are provided in Figure 16.

## Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

THE SKY13293-340LF is rated to Moisture Sensitivity Level 1 (MSL1) at $260^{\circ} \mathrm{C}$. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, Solder Reflow Information, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format. For packaging details, refer to the Skyworks Application Note, Discrete Devices and IC Switch/Attenuators Tape and Reel Package Orientation, document number 200083.


Detail A

Figure 13. SKY13293-340LF PCB Layout Footprint (Top View)


Figure 14. Typical Case Markings
(Top View)


Figure 14. SKY13293-340LF 20-Pin QFN Package Dimensions


## Figure 15. SKY13293-340LF Tape and Reel Dimensions

## Ordering Information

| Model Name | Manufacturing Part Number |
| :---: | :--- |
| SKY13293-340LF 0.25-2.15 GHz 4 x 2 Matrix Switch | SKY13293-340LF |

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